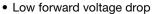
High Performance Schottky Rectifier, 1.0 A

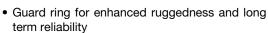


SMB (DO-214AA)

PRIMARY CHARACTERISTICS				
I _{F(AV)} 1.0 A				
V_{R}	15 V			
V _F at I _F	0.21 V			
I _{RM}	35 mA at 100 °C			
T _J max.	125 °C			
E _{AS}	1.0 mJ			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

FEATURES







• 125 °C T_J operation (V_B < 5 V)

- · Optimized for OR-ing applications
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-10BQ015HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS			
I _{F(AV)}	Rectangular waveform	1.0	Α		
V _{RRM}		15	V		
I _{FSM}	$t_p = 5 \mu s sine$	140	Α		
V _F	1.0 A _{pk} , T _J = 125 °C	0.21	V		
T _J	Range	-55 to +125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ015HM3	UNITS	
Maximum DC reverse voltage	V_{R}	15	V	
Maximum working peak reverse voltage	V_{RWM}	25	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _L = 134 °C	, rectangular waveform	1.0	Α
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	140	
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	40	А
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 2 \text{mH}$		1.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		Α	



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.33	V
Maximum forward voltage drop		2 A		0.39	
See fig. 1		1 A	T _J = 125 °C	0.21	
		2 A		0.29	
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _R	0.5	- mA
See fig. 2	I _{RM}	T _J = 100 °C		35	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		-	V
Forward slope resistance	r _t			-	mΩ
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		390	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0 nH		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µs		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

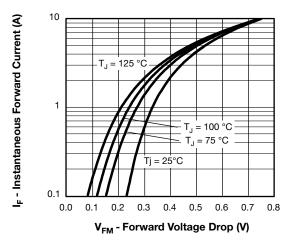
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		-55 to +125	°C
Maximum storage temperature range	T _{Stg}		-55 to +150	C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC operation See fig. 4	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (DO-214AA)	10	0

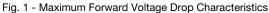
Notes

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB







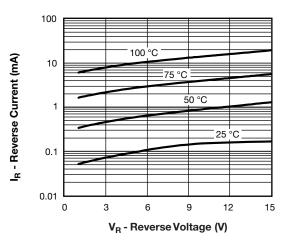


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

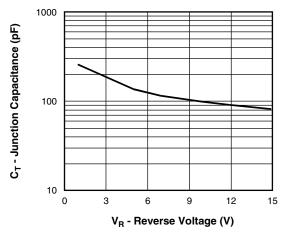


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

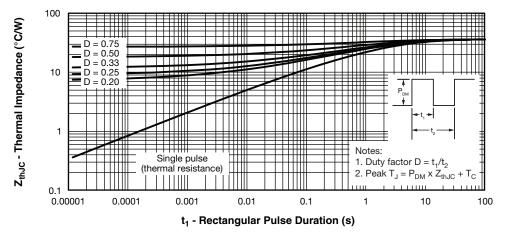


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

Allowable Case Temperature (°C)

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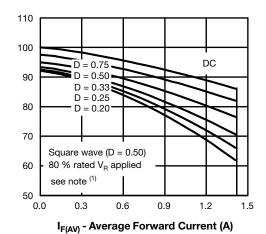


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

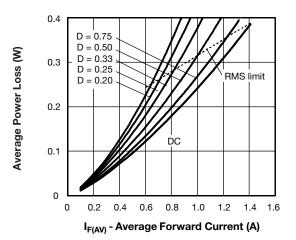


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

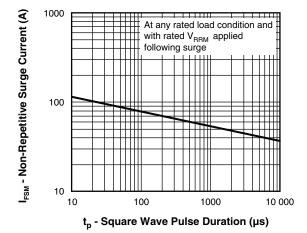


Fig. 7 - Maximum Non-Repetitive Surge Current

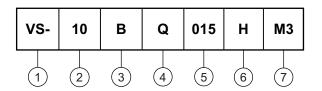
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating
- 3 B = SMB
- 4 Q = Schottky "Q" series
- Voltage rating (015 = 15 V)
- 6 H = AEC-Q101 qualified
- 7 Environmental digit:

M3 = halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-10BQ015HM3/5BT	5BT	3200	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95401		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		
SPICE model	www.vishay.com/doc?95666		



SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout





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