RoHS

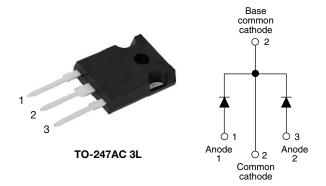
COMPLIANT

HALOGEN FREE



### Vishay Semiconductors

# High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 20 A				
V <sub>R</sub>	15 V				
V <sub>F</sub> at I <sub>F</sub>	See Electrical table				
I <sub>RM</sub> max.	600 mA at 100 °C				
T <sub>J</sub> max.	125 °C				
E <sub>AS</sub>	10 mJ				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

#### **FEATURES**

- 125 °C T<sub>J</sub> operation (V<sub>R</sub> < 5 V)</li>
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-40L15CW... center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	40	Α			
V <sub>RRM</sub>		15	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	700	Α			
V <sub>F</sub>	19 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg, typical)	0.25	V			
TJ		-55 to +125	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VS-40L15CW-N3	UNITS	
Maximum DC reverse voltage	$V_R$	T <sub>1</sub> = 100 °C	15	W	
Maximum working peak reverse voltage	$V_{RWM}$	1j = 100 C	15	v	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg					20	
forward current See fig. 5	per device	per device $I_{F(AV)}$ 50 % duty cycle at $T_C$ = 86 °C, r		rectangular waveform	40	A
Maximum peak one cycle	Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	700	A
non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	330	
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	$T_J = 25 ^{\circ}\text{C}$ , $I_{AS} = 2 \text{A}$ , $L = 5 \text{mH}$		10	mJ
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	А



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS			MAX.	UNITS	
		19 A	T <sub>.1</sub> = 25 °C	1	0.41	V	
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	40 A	1j = 25 C	-	0.52		
See fig. 1	VFM (1)	19 A	T 405.00	0.25	0.33	v	
		40 A	T <sub>J</sub> = 125 °C	0.37	0.50		
Reverse leakage current per leg	I <sub>RM</sub> (1)	T <sub>J</sub> = 25 °C	V DetectV	-	10	mA	
See fig. 2	'RM '''	T <sub>J</sub> = 100 °C	V <sub>R</sub> = Rated V <sub>R</sub>	-	600	IIIA	
Threshold voltage	$V_{F(TO)}$	T, =T, maximum 0.182		82	V		
Forward slope resistance	r <sub>t</sub>	I J = I J Maximum		7.	.6	mΩ	
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF	
Typical series inductance per leg	Ls	Measured lead to lead 5 mm from package body		8	-	nΗ	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		Rated V <sub>R</sub> 10 000		000	V/µs

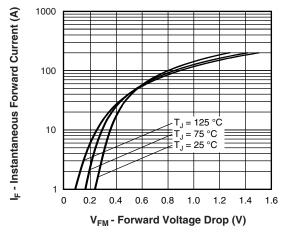
#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature ran	ge T <sub>J</sub>		-55 to 125	00		
Maximum storage temperature rang	ge T <sub>Stg</sub>		-55 to 150	°C		
Maximum thermal resistance, junction to case per leg	В	DC operation See fig. 4	1.4	°C/W		
Maximum thermal resistance, junction to case per package	R <sub>thJC</sub>	DC operation	0.7			
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24			
A			6	g		
Approximate weight			0.21	OZ.		
Mounting torque minim	num	Non-lubricated threads	6 (5)	kgf · cm		
Mounting torque maxir	num	Non-lubricated tiffeads	12 (10)	(lbf·in)		
Marking device		Case style TO-247AC 3L	40L1	5CW		







1000 I<sub>R</sub> - Reverse Current (mA) 100 T<sub>J</sub> = 75 °C 10 = 25 °C 0.1 3 9 12 15 V<sub>R</sub> - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

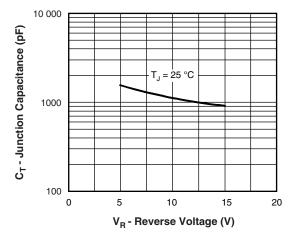


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

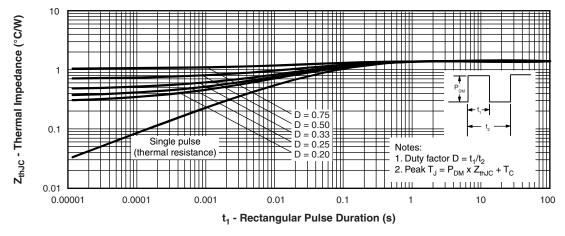


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

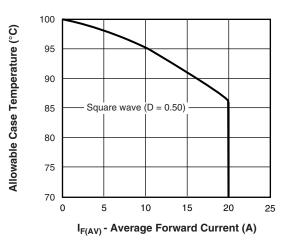


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

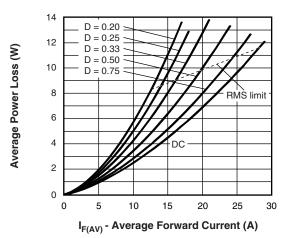


Fig. 6 - Forward Power Loss Characteristics

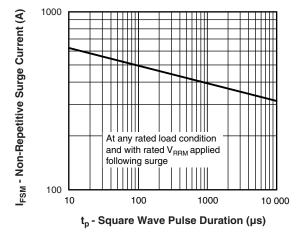


Fig. 7 - Maximum Non-Repetitive Surge Current

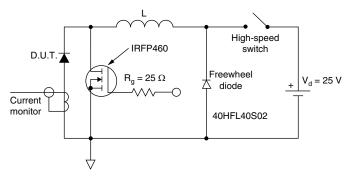
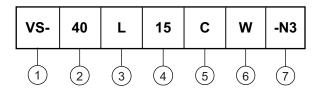


Fig. 8 - Unclamped Inductive Test Circuit



#### **ORDERING INFORMATION TABLE**

**Device code** 



- Vishay Semiconductors product
- 2 Current rating (40 = 40 A)
- 3 Schottky "L" series
- 4 Voltage code (15 = 15 V)
- 5 Circuit configuration:

C = common cathode

6 - Package:

W = TO-247

7 - Environmental digit

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-40L15CW-N3	25	500	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96138			
Part marking information	www.vishay.com/doc?95007			
SPICE model	www.vishav.com/doc?97119			



### **TO-247AC 3L**

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	MILLIMETERS		INCHES		
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.35	0.020	0.053		
E	15.29	15.87	0.602	0.625	3	
E1	13.46	-	0.53	-		
е	5.46	BSC	0.215	BSC		
ØK	0.2	0.254		)10		
L	14.20	16.10	0.559	0.634		
L1	3.71	4.29	0.146	0.169		
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	7.39	-	0.291		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51 BSC		0.217	BSC		
	·		·	·	·	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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