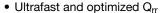


## FRED Pt® Gen 5, Ultrafast Rectifier Diode, 600 V, 240 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub> at 95 °C (per module)	240 A				
V <sub>R</sub>	600 V				
Q <sub>rr</sub> (typical)	260 nC				
t <sub>rr</sub>	52 ns				
Type	Modules - diode, FRED Pt®				
Package	TO-244				
Circuit configuration	Two diodes common cathode				

#### **FEATURES**





• Best in class forward voltage drop and switching losses trade off

ROHS

- Optimized for high speed operation
- 175 °C maximum operation junction temperature
- UL approved file E222165
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **BENEFITS**

- Reduced RFI and EMI
- Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

### **DESCRIPTION / APPLICATIONS**

Featuring a unique combination of low conduction and switching losses the FRED Pt® Gen 5 is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters.

These devices are also ideally suited for HF welding, power converters, and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	$V_R$		600	V	
		T <sub>C</sub> = 25 °C	229		
Continuous forward current per diode	I <sub>F(DC)</sub>	T <sub>C</sub> = 85 °C	160		
		T <sub>C</sub> = 115 °C	120	Α	
Non-repetitive single pulse forward current per diode	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	1300		
Maximum power dissipation per diode	P <sub>D</sub>	T <sub>C</sub> = 25 °C	395	W	
		T <sub>C</sub> = 115 °C	158	VV	
Storage temperature range	T <sub>Stg</sub>		-40 to +150	°C	
Operating junction temperature range	TJ		-40 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MA		MAX.	UNITS	
Breakdown voltage	$V_{BR}$	I <sub>R</sub> = 200 μA	600	-	-	
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 120 A	-	1.52	1.68	
		I <sub>F</sub> = 240 A	-	1.67	1.96	V
		I <sub>F</sub> = 120 A, T <sub>J</sub> = 150 °C	-	1.17	-	
		I <sub>F</sub> = 240 A, T <sub>J</sub> = 150 °C	-	1.46	-	
Reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = 600 V	-	0.18	0.5	mA
Series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane	-	5	=.	nΗ
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>DC</sub> = 5 V, f = 1 MHz, 25 °C 0.7		nF		

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS			TYP.	MAX.	UNITS
Reverse recovery time	+	T <sub>J</sub> = 25 °C		-	52	-	20
neverse recovery time	ne t <sub>rr</sub>	T <sub>J</sub> = 125 °C	$I_F = 50 \text{ A},$ $dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 300 \text{ V}$	-	135	-	ns
Dook was a van a summant	Peak recovery current I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	4.0	-	A
Peak recovery current		T <sub>J</sub> = 125 °C		-	11.0	-	
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	260	-	nC
	$Q_{rr}$	T <sub>J</sub> = 125 °C		-	1530	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Thermal resistance,	per leg	Б	=	-	0.38		
junction to case	per module	$R_{thJC}$	-	-	0.19	°C/W	
Thermal resistance, case	to heatsink	R <sub>thCS</sub>	=	0.10	-		
Weight			-	68	-	g	
			-	2.4	-	OZ.	
Mounting torque  Mounting torque center hole			30 (3.4)	-	40 (4.6)		
			12 (1.4)	-	18 (2.1)	lbf · in (N · m)	
Terminal torque			30 (3.4)	-	40 (4.6)	(14 111)	
Vertical pull		80		lbf ⋅ in			
2" lever pull			=	-	35	- IDT · IN	
Case style			TO-244				

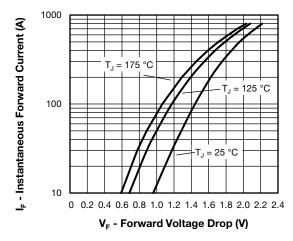
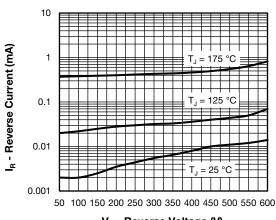


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)



V<sub>R</sub> - Reverse Voltage (V)

Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

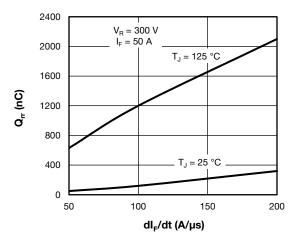


Fig. 3 - Typical Reverse Recovery Charge vs dl<sub>F</sub>/dt (Per Diode)

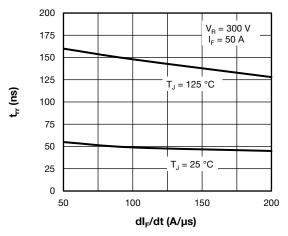


Fig. 4 - Typical Reverse Recovery Time vs dl<sub>F</sub>/dt (Per Diode)

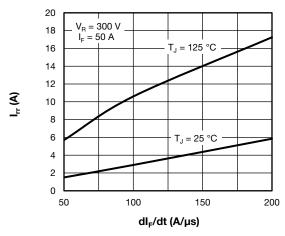


Fig. 5 - Typical Reverse Recovery Current vs dl<sub>F</sub>/dt (Per Diode)

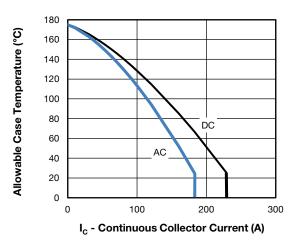


Fig. 6 - Maximum Continuous Forward Current vs.

Case Temperature

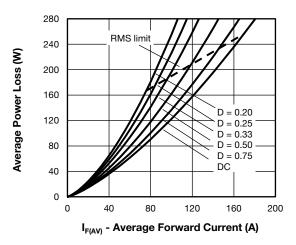


Fig. 7 - Average Power Loss vs. Average Forward Current (Forward Power Loss Characteristics)

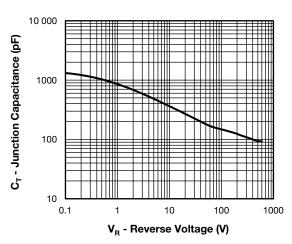


Fig. 8 - Typical Junction Capacitance vs. Reverse Voltage

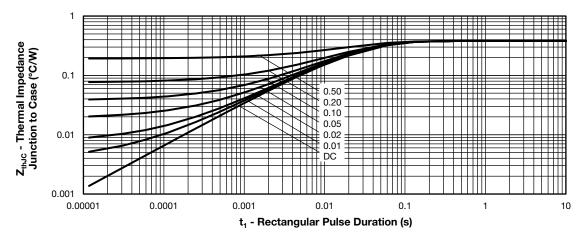
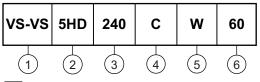


Fig. 9 - Z<sub>thJC</sub> Maximum Thermal Impedance Junction to Case vs. t<sub>1</sub> Rectangular Pulse Duration

#### **ORDERING INFORMATION TABLE**

#### **Device code**



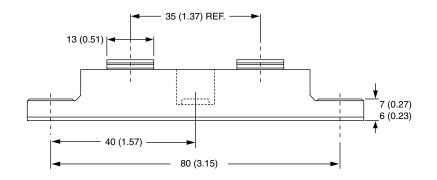
- Vishay Semiconductors product
- 2 5HD = high speed FRED Pt® Gen 5
- 3 Current rating (240 = 240 A)
- 4 Circuit configuration:
  - C = two diodes common cathode
- 5 W = TO-244 wire bondable not isolated
- Voltage rating (60 = 600 V)

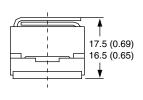
CIRCUIT CONFIGURATION			
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING	
Two diodes common cathode	C	Lug Lug terminal terminal anode 1 anode 2  Base common cathode	

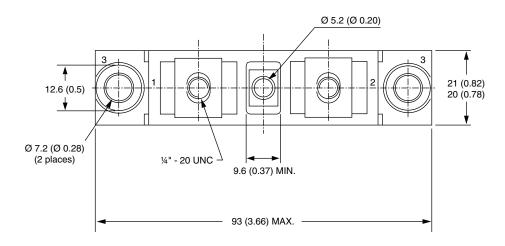
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95021		



#### **DIMENSIONS** in millimeters (inches)









## **Legal Disclaimer Notice**

Vishay

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