RoHS

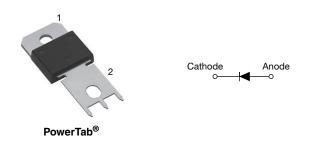
COMPLIANT

HALOGEN FREE



Vishay Semiconductors

Ultrafast Soft Recovery Diode, 80 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I _{F(AV)}	80 A				
V _R	400 V				
V _F at I _F	0.92 V				
t _{rr} (typ.)	See recovery table				
T _J max.	175 °C				
Package	PowerTab [®]				
Circuit configuration	Single				

FEATURES

- · Ultrafast recovery time
- 175 °C max. operating junction temperature
- Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47
- PowerTab[®] package
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- · Reduced parts count

DESCRIPTION / APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

MECHANICAL DATA

Case: PowerTab®

Molding compound meets UL 94 V-0 flammability rating

Terminal: nickel plated, screwable

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V_R		400	V
Continuous forward current	I _{F(AV)}	T _C = 101 °C	80	
Single pulse forward current	I _{FSM}	T _C = 25 °C	800	Α
Maximum repetitive forward current	I _{FRM}	Square wave, 20 kHz	160	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	$I_{R} = 100 \mu A$	400	1	-	
		$I_F = 80 \text{ A}$	ı	1.1	1.3	V
Forward voltage V _F	I _F = 80 A, T _J = 175 °C	-	0.92	1.08	ď	
	I _F = 80 A, T _J = 125 °C		0.98	1.15		
Poverse leekege overent	I_	$V_R = V_R$ rated	-	-	50	μA
Reverse leakage current I _R		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	-	2	mA
Junction capacitance	C _T	$V_{R} = 200 \text{ V}$	-	50	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	3.5	-	nH



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I _F = 1 A, dI _F /dt = 200 /	4/μs, V _R = 30 V	-	50	60	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	87	-	ns
		T _J = 125 °C	I _F = 80 A V _B = 200 V	-	151	-	
Peak recovery current I _{RRM}	ı	T _J = 25 °C		-	9.3	-	Α
	T _J = 125 °C	v _R = 200 v dl _F /dt = 200 A/µs	-	17.2	-		
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	405	-	nC
	Q _{rr}	T _J = 125 °C		-	1300	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R _{thJC}		-	-	0.70	°C/W
Thermal resistance, junction to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.2	-	C/VV
Weight			-	-	5.02	g
Mounting torque			1.2		2.4	N·m
Wounting torque			(10)	_	(20)	(lbf · in)
Marking device		Case style PowerTab® 80EBU04				

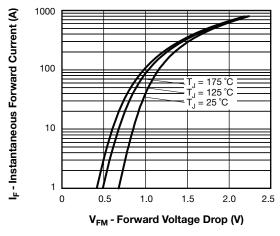


Fig. 1 - Maximum Forward Voltage Drop Characteristics

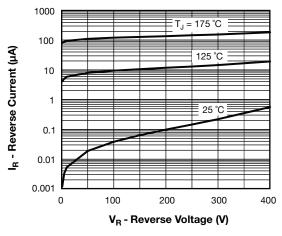


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

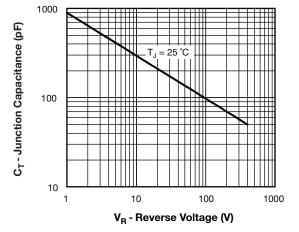


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



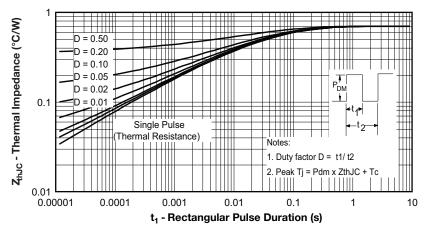


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

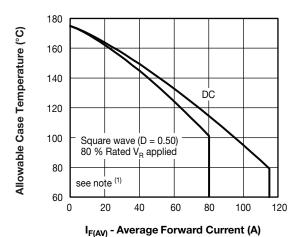


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

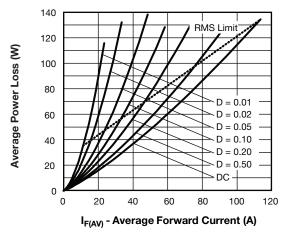


Fig. 6 - Forward Power Loss Characteristics

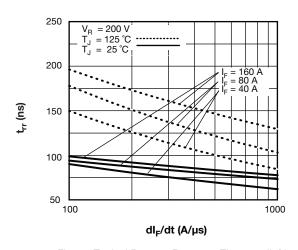


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

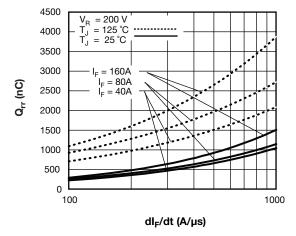
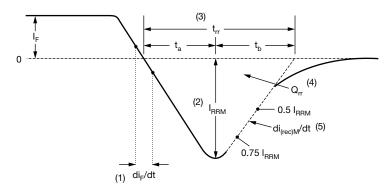


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D) (see fig. 6)$; $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80 \%$ rated V_R

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- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RBM} and 0.50 I_{RBM} extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RRM}$

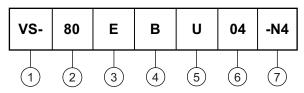
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- 2 Current rating (80 = 80 A)
- 3 E = Single diode
- 4 B = PowerTab[®] (ultrafast/hyperfast only)
- 5 U = Ultrafast recovery
- 6 Voltage rating (04 = 400 V)
- 7 Environmental digit:

-N4 = Halogen-free, RoHS-compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION		
VS-80EBU04-N4	25/tube	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95240			
Part marking information	www.vishay.com/doc?95467			
Application note	www.vishay.com/doc?95179			



Vishay Semiconductors

PowerTab®

DIMENSIONS in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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