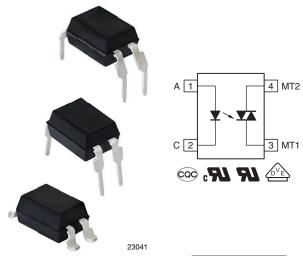


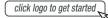
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Optocoupler, Phototriac Output, Non-Zero Crossing, High dV/dt, Low Input Current, DIP-4 Package



DESIGN SUPPORT TOOLS









DESCRIPTION

The VOT8121A consists of a GaAs IRLED optically coupled to a photosensitive TRIAC packaged in a board space saving DIP-4 package.

The VOT8121A isolates low-voltage logic from 120 V_{AC} , 240 V_{AC} , and 380 V_{AC} lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

FEATURES

- Space saving package
- High static dV/dt 1000 V/µs
- High input sensitivity I_{FT} = 10 mA
- 100 mA on-state current
- 800 V peak off-state blocking voltage
- Isolation rated voltage 5300 V_{RMS}
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

APPLICATIONS

- Power TRIAC driver in solid-state relays
- 3-phase AC equipment
- Motor control
- Industrial control
- · White goods / household equipment

AGENCY APPROVALS

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option "V"
- CQC

ORDERING INFORMATION			
ORDERING INFORMATION			
V O T 8 1 2	1 A # - V T #		
PART NUMBER	PACKAGE VDE TAPE AND OPTION OPTION REEL		
AGENCY CERTIFIED/PACKAGE	TRIGGER CURRENT, I _{FT} (mA)		
UL, cUL	10		
DIP-4	VOT8121AD		
DIP-4, 400 mil	VOT8121AG		
SMD-4	VOT8121AB-T ⁽¹⁾		
SMD-4, 90° orientation	VOT8121AB-T1		
SMD-4, 180° orientation	VOT8121AB-T2		
SMD-4, 270° orientation	VOT8121AB-T3		
VDE, UL, cUL	10		
DIP-4	VOT8121AD-V		
DIP-4, 400 mil	VOT8121AG-V		
SMD-4	VOT8121AB-VT ⁽¹⁾		
SMD-4, 90° orientation	VOT8121AB-VT1		
SMD-4, 180° orientation	VOT8121AB-VT2		
SMD-4, 270° orientation	VOT8121AB-VT3		

Note

(1) Also available in tubes; do not add T to end

VOT8121AB, VOT8121AD, VOT8121AG

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT	·			
Reverse voltage		V _R	6	V
Forward current		I _F	50	mA
Power dissipation		P _{diss}	120	mW
Junction temperature		Tj	125	°C
OUTPUT	·			
Peak off-state voltage		V_{DRM}	800	V
Power dissipation		P _{out}	300	mW
On-state current		I _{T(RMS)}	100	mA
Peak repetitive surge current	PW = 100 μs, 120 pps	V _{TSM}	1	А
Junction temperature		Tj	125	°C
COUPLER	·			
Storage temperature range		T _{stg}	-55 to +125	°C
Ambient temperature range		T _{amb}	-55 to +110	°C
Total power dissipation		P _{diss}	330	mW
Soldering temperature	For 10 s	T _{sld}	260	°C

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.
 This phototriac should not be used to drive a load directly. It is intended to be a trigger device only

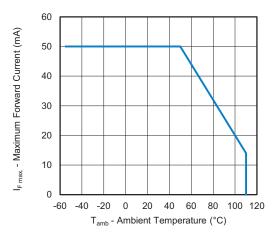


Fig. 1 - Maximum Forward Current vs. Ambient Temperature

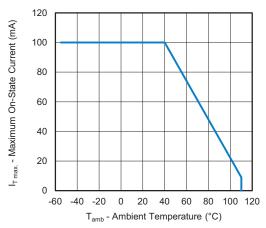


Fig. 2 - Maximum On-State Current vs. Ambient Temperature

VOT8121AB, VOT8121AD, VOT8121AG

Vishay Semiconductors

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I _F = 20 mA	V_{F}	-	1.2	1.4	V
Reverse current	V _R = 3 V	I _R	=	-	10	μΑ
OUTPUT						
Off-state current	V _{DRM} = 800 V	I _{DRM}	-	-	1	μΑ
On-state voltage	I _T = 100 mA peak	V_{TM}	=	-	3	V
Holding current		lΗ	-	400	-	μΑ
Critical rate of rise of off-state voltage		dV/dt (1)	1000	-	-	V/µs
COUPLER						
Trigger current	V _{TM} = 3 V	I _{FT}	-	-	10	mA

Notes

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements

⁽¹⁾ Static dV/dt

SAFETY AND INSULATION RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification	According to IEC 68 part 1		55 / 110 / 21		
Comparative tracking index	Insulation group IIIa	CTI	175		
Maximum rated withstanding isolation voltage	According to UL 1577, t = 1 min	V _{ISO}	5300	V _{RMS}	
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}	
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5, DIP-4, SMD-4	V _{IORM}	890	V _{peak}	
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5, DIP-4, 400 mil	V _{IORM}	1140	V _{peak}	
Isolation resistance	$T_{amb} = 25 ^{\circ}\text{C}, V_{IO} = 500 \text{V}$	R _{IO}	≥ 10 ¹²	Ω	
	T_{amb} = 100 °C, V_{IO} = 500 V	R _{IO}	≥ 10 ¹¹	Ω	
Output safety power		P _{SO}	600	mW	
Input safety current		I _{SI}	400	mA	
Input safety temperature		T _S	175	°C	
Creepage distance	DID 4 CMD 4		≥ 7	mm	
Clearance distance	DIP-4, SMD-4		≥ 7	mm	
Creepage distance	DIP-4, 400 mil		≥ 8	mm	
Clearance distance	DIF-4, 400 IIIII		≥ 8	mm	
Insulation thickness		DTI	≥ 0.4	mm	

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

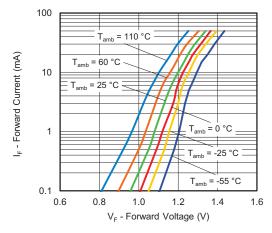


Fig. 3 - Forward Current vs. Forward Voltage

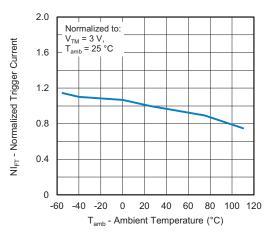


Fig. 4 - Normalized Trigger Current vs. Ambient Temperature

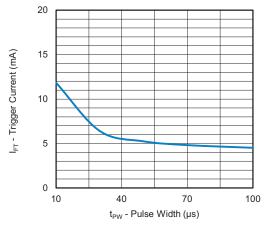


Fig. 5 - Trigger Current vs. Pulse Width

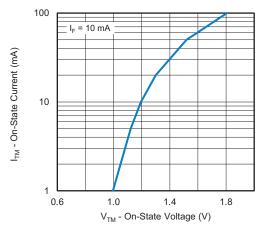


Fig. 6 - On State Current vs. On State Voltage

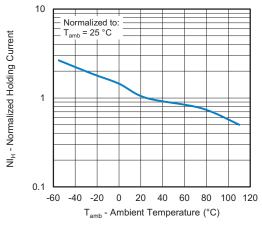


Fig. 7 - Normalized Holding Current vs. Ambient Temperature

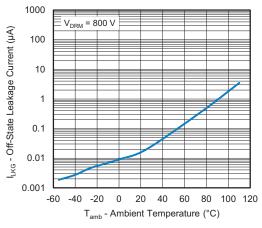


Fig. 8 - Off-State Leakage Current vs. Ambient Temperature

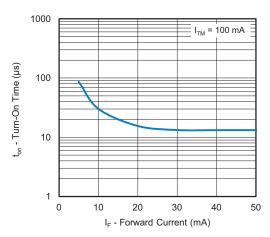


Fig. 9 - Turn-On Time vs. Forward Current

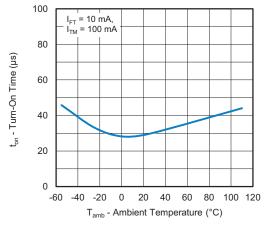


Fig. 10 - Turn-On Time vs. Ambient Temperature

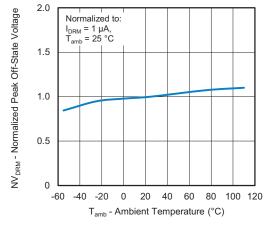
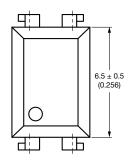
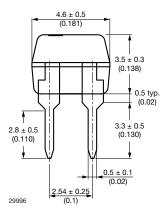


Fig. 11 - Normalized Peak Off-State Voltage vs.
Ambient Temperature

PACKAGE DIMENSIONS (in millimeters)

DIP-4





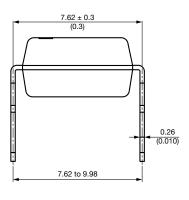
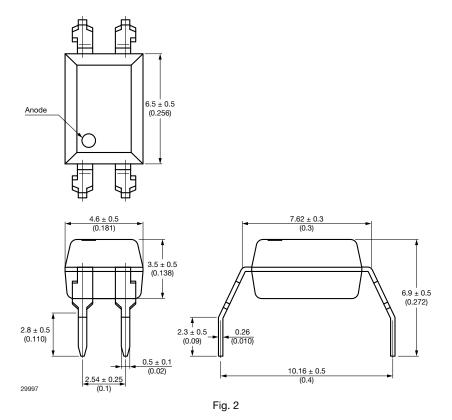
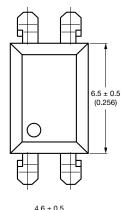


Fig. 1

DIP-4, 400 mil





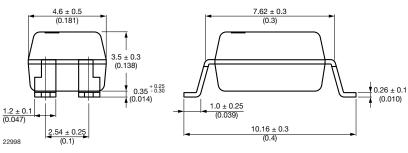


Fig. 3

PACKAGE MARKING



Fig. 12 - Example of VOT8121AD-VT

Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- VDE logo is only marked on VDE option parts
- Tape and reel suffix (T) is not part of the package marking

PACKAGING INFORMATION (in millimeters)

DEVICES PER TUBE				
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX	
DIP-4	100	40	4000	
DIP-4, 400 mil	100	40	4000	
SMD-4	100	40	4000	

DIP-4 Tube

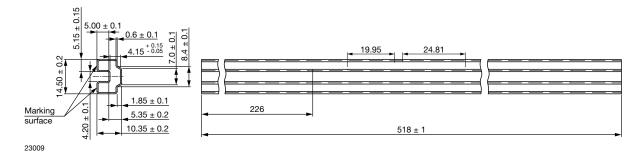


Fig. 13 - Tube

SMD-4 Tape

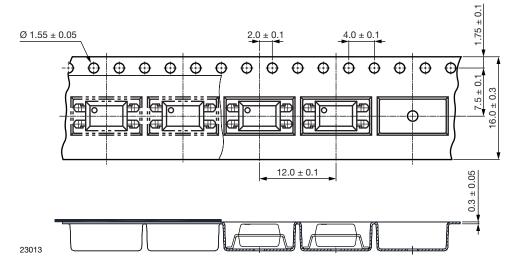


Fig. 14 - Tape and Reel Packaging (1000 pieces on reel)

SMD-4 Tape, 90° Orientation

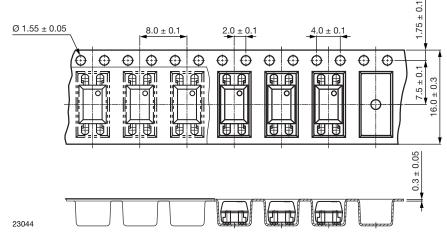


Fig. 15 - Tape and Reel Packaging (2000 pieces on reel)

SMD-4 Tape, 180° Orientation

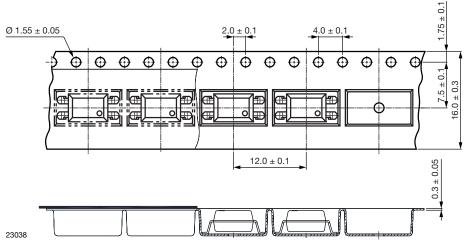
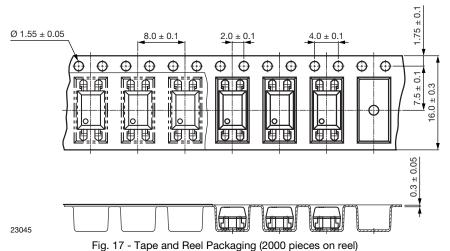


Fig. 16 - Tape and Reel Packaging (1000 pieces on reel)

SMD-4 Tape, 270° Orientation



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Reel

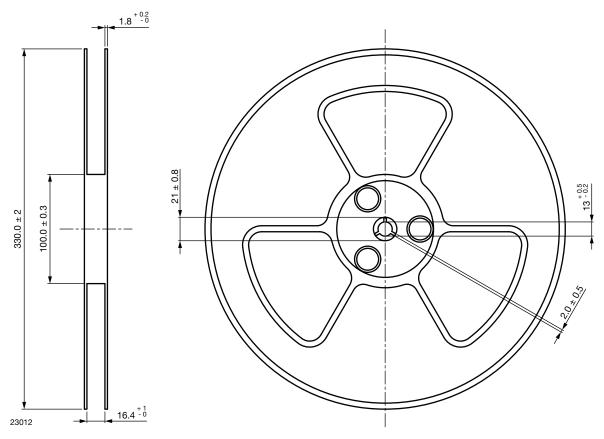


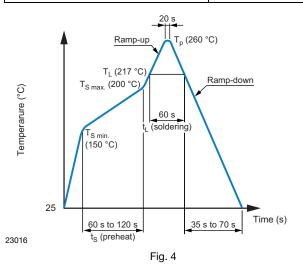
Fig. 18 - Tape and Reel Shipping Medium

SOLDER PROFILES

IR Reflow Soldering (JEDEC® J-STD-020C compliant)

One time soldering reflow is recommended within the condition of temperature and time profile shown below. Do not solder more than three times.

PROFILE ITEM	CONDITIONS
Preheat	
- Temperature minimum (T _{S min.})	150 °C
- Temperature maximum (T _{S max.})	200 °C
- Time (min. to max.) (t _S)	90 s ± 30 s
Soldering zone	
- Temperature (T _L)	217 °C
- Time (t _L)	60 s
Peak temperature (T _p)	260 °C
Ramp-up rate	3 °C/s max.
Ramp-down rate	3 °C/s to 6 °C/s



Wave Soldering (JEDEC JESD22-A111 compliant)

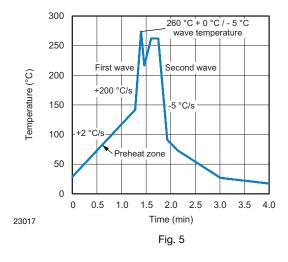
One time soldering is recommended within the condition of temperature.

Temperature: 260 °C + 0 °C / - 5 °C

Time: 10 s

Preheat temperature: 25 °C to 140 °C

Preheat time: 30 s to 80 s



Hand Soldering by Soldering Iron

Allow single lead soldering in every single process. One time

soldering is recommended.

Temperature: 380 °C + 0 °C / - 5 °C

Time: 3 s max.

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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Vishay

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