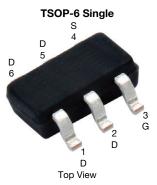


www.vishay.com

Vishay Siliconix

Automotive P-Channel 30 V (D-S) 175 °C MOSFET



Marking Code: 9F

PRODUCT SUMMARY					
V _{DS} (V)	-30				
$R_{DS(on)}$ (Ω) at $V_{GS} = -4.5 \text{ V}$	0.021				
$R_{DS(on)}$ (Ω) at V_{GS} = -2.5 V	0.033				
I _D (A)	-8				
Configuration	Single				
Package	TSOP-6				

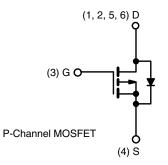
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	-30	V	
Gate-source voltage		V _{GS}	± 12	V	
Continuous drain current	T _C = 25 °C	1	-8	A	
	T _C = 125 °C	- I _D	-7		
Continuous source current (diode conduction	on)	Is	-4.5		
Pulsed drain current		I _{DM}	-32		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	-19.5		
Single pulse avalanche Energy	L = U.1 MH	E _{AS}	19	mJ	
Manian and a discipation 2	T _C = 25 °C	D	5	w	
Maximum power dissipation ^a	T _C = 125 °C	P_{D}	1.6		
Operating junction and storage temperature	range	T _J , T _{stq}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-ambient	PCB mount b	R_{thJA}	110	°C/W		
Junction-to-foot (drain)		R_{thJF}	30	C/VV		

Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT		
Static		-						
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		-30	-	-	V	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$		-1	-1.4		
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		-	-	± 100	nA	
		$V_{GS} = 0 V$	$V_{DS} = -30 \text{ V}$	-	-	-1		
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = -30 V, T _J = 125 °C	-	-	-50	μΑ	
		$V_{GS} = 0 V$	V _{DS} = -30 V, T _J = 175 °C	-	-	-150		
On-state drain current a	I _{D(on)}	V _{GS} = -10 V	$V_{DS} = -5 V$	-10	-	-	Α	
		V _{GS} = -4.5 V	I _D = -5 A	-	0.017	0.021	Ω	
Drain-source on-state resistance ^a	Б	V _{GS} = -4.5 V	I _D = -5 A, T _J = 125 °C	-	-	0.030		
	R _{DS(on)}	V _{GS} = -4.5 V	I _D = -5 A, T _J = 175 °C	-	-	0.034		
		V _{GS} = -2.5 V	I _D = -4 A	-	0.027	0.034		
Forward transconductance b	9 _{fs}	$V_{DS} = -15 \text{ V}, I_{D} = -5 \text{ A}$		-	24	-	S	
Dynamic ^b								
Input capacitance	C _{iss}		V _{DS} = -20 V, f = 1 MHz	-	3032	3950	pF	
Output capacitance	C _{oss}	$V_{GS} = 0 V$		-	220	285		
Reverse transfer capacitance	C _{rss}			-	217	285		
Total gate charge ^c	Qg			-	29	41		
Gate-source charge ^c	Q _{gs}	$V_{GS} = -4.5 \text{ V}$	$V_{DS} = -15 \text{ V}, I_{D} = -7.9 \text{ A}$	-	5.7	-	nC	
Gate-drain charge c	Q _{gd}			-	8.4	-		
Gate resistance	Rg	f = 1 MHz		2.2	5.6	9	Ω	
Turn-on delay time ^c	t _{d(on)}			-	20	28		
Rise time ^c	t _r	V_{DD} = -15 V, R_L = 1.9 Ω I_D \cong -7.9 A, V_{GEN} = -4.5 V, R_g = 1 Ω		-	51	72	ns	
Turn-off delay time ^c	t _{d(off)}			-	71	100		
Fall time ^c	t _f			-	68	96		
Source-Drain Diode Ratings and Char	acteristics b				•	•		
Pulsed current ^a	I _{SM}			-	-	-32	Α	
Forward voltage	V_{SD}	I _F = -5 A, V _{GS} = 0 V		_	-0.8	-1.2	V	

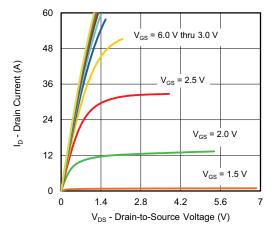
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

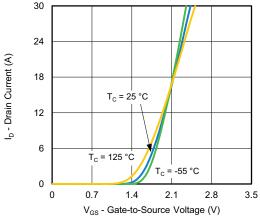
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



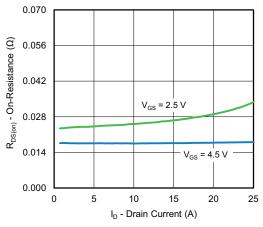
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



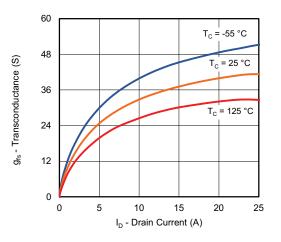
Output Characteristics



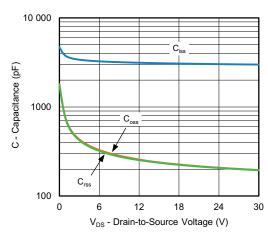
Transfer Characteristics



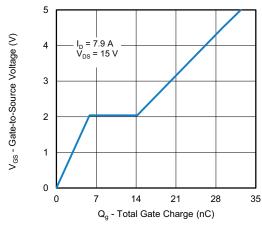
On-Resistance vs. Drain Current



Transconductance



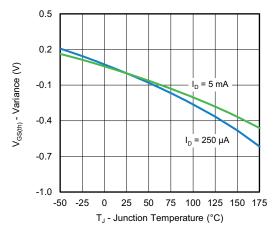
Capacitance



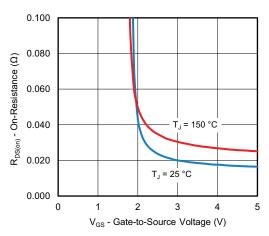
Gate Charge



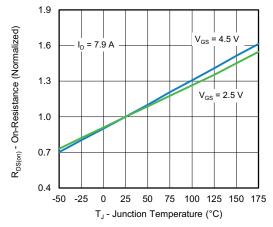
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



Threshold Voltage



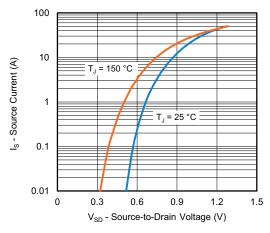
On-Resistance vs. Gate-to-Source Voltage



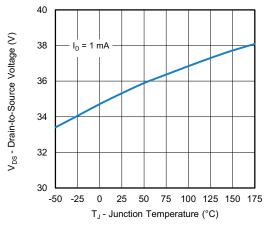
On-Resistance vs. Junction Temperature

Note

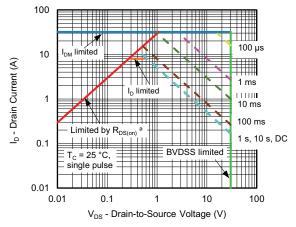
a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



Source Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature

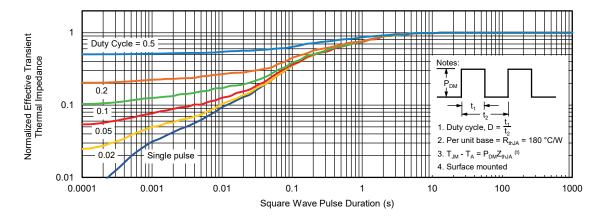


Safe Operating Area

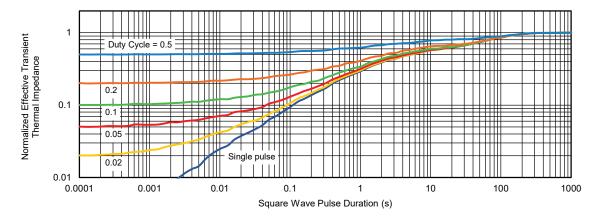
For technical questions, contact: automostechsupport@vishay



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

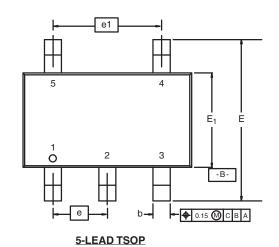
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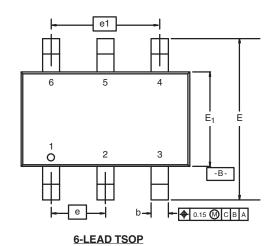


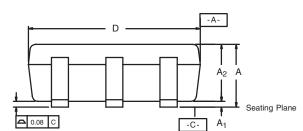


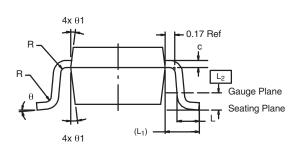
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C









	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref		0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200 www.vishay.com 18-Dec-06



Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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