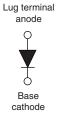


High Performance Schottky Rectifier, 240 A





HAL	F-PA	K ([0-67)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	240 A			
V_{R}	100 V			
Package	HALF-PAK (D-67)			
Circuit configuration	Single diode			

FEATURES

- 175 °C T_J operation
- · Low forward voltage drop



- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION

The VS-243NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES			
I _{F(AV)}	Rectangular waveform	240	А		
V _{RRM}		100	V		
I _{FSM}	$t_p = 5 \mu s sine$	25 500	А		
V _F	240 A _{pk} , T _J = 125 °C	0.72	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-243NQ100PbF	UNITS	
Maximum DC reverse voltage	V_{R}	100 V		
Maximum working peak reverse voltage	V_{RWM}	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 132 °C, rectangular waveform		240	
Maximum peak one cycle non-repetitive surge current	I	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	25 500	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	3300	
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 ^{\circ}\text{C}, I_{AS} = 5.5 \text{A}, L = 1 \text{mH}$		15	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α

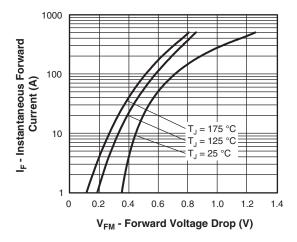


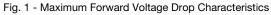
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	240 A	T _J = 25 °C	0.95	V
Maximum forward voltage drop		480 A		1.26	
See fig. 1		240 A	T _J = 125 °C	0.72	
		480 A		0.85	
Maximum reverse leakage current		T _J = 25 °C	V _B = Rated V _B	6	mA
See fig. 2	I _{RM}	T _J = 125 °C	v _R = nateu v _R	80	IIIA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5500	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}}$ Pulse width = 500 μ s

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage tempe	rature range	T _J , T _{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05	
Approximate weight				30	g
				1.06	OZ.
minimum				3 (26.5)	
Mounting torque	maximum		Non-lubricated threads	4 (35.4)	N ⋅ m (lbf ⋅ in)
Tanakallan	minimum		Non-lubricated tilleads	3.4 (30)	
Terminal torque	maximum			5 (44.2)	
Case style				HALF-PAI	K module





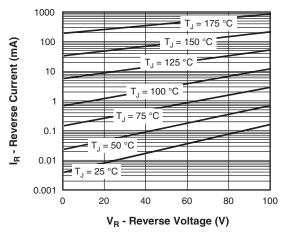


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

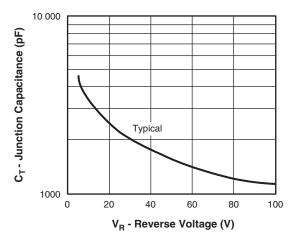


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

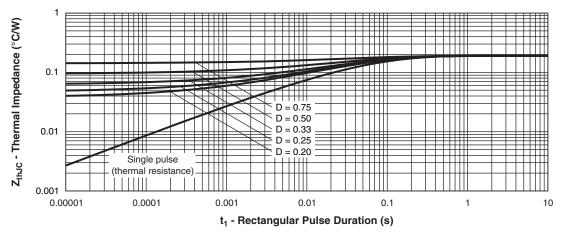


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

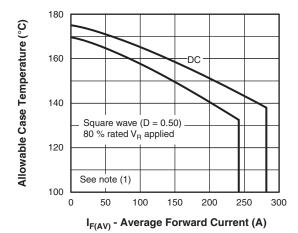
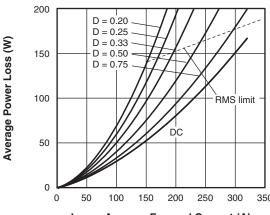
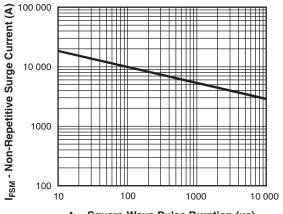


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics



 $\boldsymbol{t_p}$ - Square Wave Pulse Duration (µs)

Fig. 7 - Maximum Non-Repetitive Surge Current

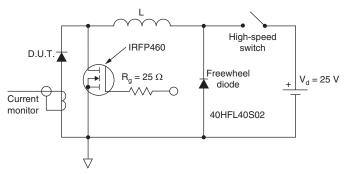


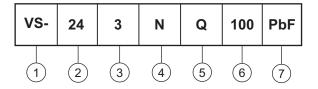
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



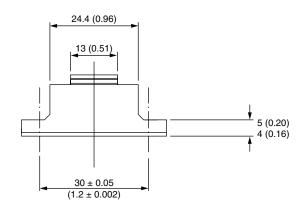
- Vishay Semiconductors product
- 2 Average current rating (x 10)
- 3 Product silicon identification
- 4 N = not isolated
- 5 Q = Schottky rectifier diode
- Voltage rating (100 = 100 V)
- 7 Lead (Pb)-free

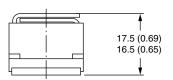
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			

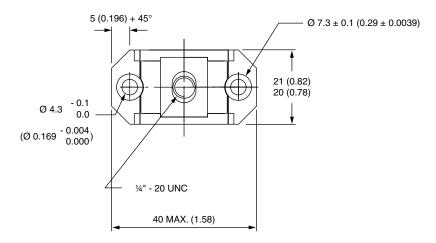


D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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