HALOGEN

FREE



## Vishay General Semiconductor

# Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier



**SMC (DO-214AB)** 



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	5.0 A			
$V_{RRM}$	200 V			
I <sub>FSM</sub>	100 A			
$V_F$ at $I_F = 5.0$ A	0.67 V			
T <sub>J</sub> max.	150 °C			
Package	SMC (DO-214AB)			
Circuit configuration	cuit configuration Single			

#### **FEATURES**

- Low profile package
- · Ideal for automated placement
- Trench MOS Schottky technology
- · Low power losses, high efficiency
- · Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **TYPICAL APPLICATIONS**

For use in high frequency converters, freewheeling diodes, DC/DC converters and polarity protection applications.

### **MECHANICAL DATA**

Case: SMC (DO-214AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free and RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		VSSC520S	UNIT	
Device marking code		V5D		
Maximum repetitive peak reverse voltage	$V_{RRM}$	200	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	5.0	A	
	I <sub>F</sub> <sup>(2)</sup>	2.2		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	100	А	
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000	V/µs	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

#### Notes

- (1) Units mounted on PCB with 25 mm x 25 mm copper pad areas, 1 oz. FR4 PCB
- (2) Free air, mounted on recommended PCB 1 oz. pad area

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	1.19	1.70	V
		T <sub>A</sub> = 125 °C		0.67	0.75	
Reverse current per diode	V <sub>R</sub> = 180 V	T <sub>A</sub> = 25 °C	- I <sub>R</sub> <sup>(2)</sup>	2.0	-	μA
		T <sub>A</sub> = 125 °C		2.0	-	mA
	V <sub>R</sub> = 200 V	T <sub>A</sub> = 25 °C		4	200	μA
		T <sub>A</sub> = 125 °C		3.2	25	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	280	-	pF

### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	VSSC520S	UNIT		
Typical thermal resistance	R <sub>0JA</sub> (1)	95	°C/W	
	R <sub>0JM</sub> (2)	9	C/VV	

#### Notes

- (1) Free air, mounted on recommended PCB 1 oz. pad area; thermal resistance R<sub>BJA</sub> junction to ambient
- (2) Units mounted on PCB with 25 mm x 25 mm copper pad areas; thermal resistance R<sub>BJM</sub> junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSSC520S-M3/57T	0.235	57T	850	7" diameter plastic tape and reel	
VSSC520S-M3/9AT	0.235	9AT	3500	13" diameter plastic tape and reel	

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

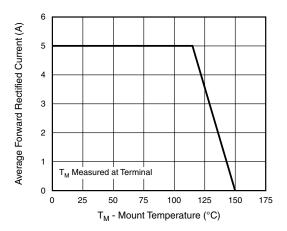


Fig. 1 - Maximum Forward Current Derating Curve

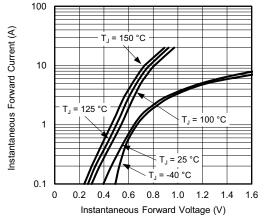


Fig. 3 - Typical Instantaneous Forward Characteristics

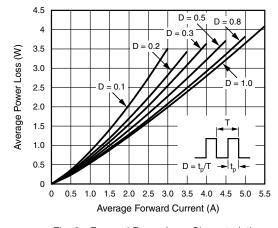


Fig. 2 - Forward Power Loss Characteristics

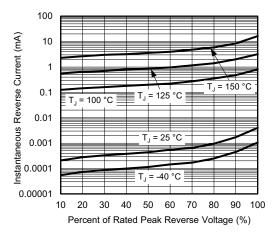


Fig. 4 - Typical Reverse Characteristics



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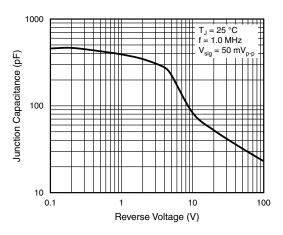


Fig. 5 - Typical Junction Capacitance

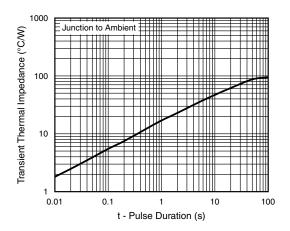
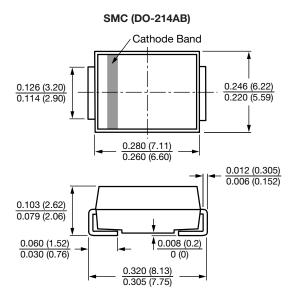
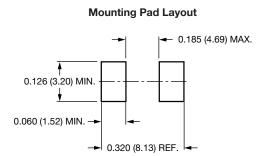


Fig. 6 - Typical Transient Thermal Impedance

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)







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Vishay

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