

Hyperfast Rectifier, 15 A FRED Pt® G5



LINKS TO ADDITIONAL RESOURCES

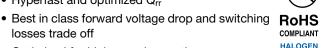




PRIMARY CHARACTERISTICS								
I _{F(AV)}	15 A							
V _R	1200 V							
V _F at I _F at 125 °C	1.7 V							
t _{rr}	37 ns							
T _J max.	175 °C							
Package	TO-220AC 2L							
Circuit configuration	Single							

FEATURES

Hyperfast and optimized Q_{rr}



FREE

Optimized for high speed operation

175 °C maximum operating junction temperature

Polyimide passivation

losses trade off

· Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for high frequency converters, both soft switched / resonant.

Specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

MECHANICAL DATA

Case: TO-220AC 2L

Molding compound meets UL 94 V-0 flammability rating Terminals: matte tin plated leads, solderable per

J-STD-002

Polarity: as per marking device details

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS						
Repetitive peak reverse voltage	V _{RRM}		1200	V						
Average rectified forward current	I _{F(AV)}	T _C = 110 °C, D = 0.50	15							
Repetitive peak forward current	I _{FRM}	T _C = 110 °C, D = 0.50, f = 20 kHz	30	Α						
Non-repetitive peak surge current	I _{FSM}	$T_C = 45$ °C, $t_p = 10$ ms, sine wave	125							
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C						

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	1200	-	-	.,			
Familiand walks as	V _F	I _F = 15 A	-	1.9	2.5	V			
Forward voltage		I _F = 15 A, T _J = 125 °C	-	1.7	-]			
Deverage legistres everywent	I _R	$V_R = V_R$ rated	-	-	50				
Reverse leakage current		T _J = 125 °C, V _R = V _R rated	-	-	500	μΑ			
Junction capacitance	C _T	V _R = 200 V	-	10	-	рF			
Series inductance	L _S	Measured to lead 5 mm from package body	ı	8	-	nH			



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
		I _F = 1.0 A, dI _F /dt =	-	37	50					
Reverse recovery time	t _{rr}	T _J = 25 °C		ı	95	-	ns			
		T _J = 125 °C		ı	146	ı				
Peak recovery current	1	T _J = 25 °C	I _F = 10 A dI _F /dt = 600 A/μs V _B = 400 V	1	14	1	А			
Feak recovery current	I _{RRM}	T _J = 125 °C		-	19	-				
Poverse receivent charge	0	T _J = 25 °C		-	545	-	nC			
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	1200	-				
Reverse recovery time	t _{rr}	T _J = 25 °C		-	75.5	-	ns A			
neverse recovery time		T _J = 125 °C		-	100	-				
Dook recovery ourrent	I _{RRM}	T _J = 25 °C	I _F = 15 A dI _F /dt = 1000 A/µs	-	23	-				
Peak recovery current		T _J = 125 °C	$V_{R} = 800 \text{ V}$	-	35	-				
Payaraa raaayan, aharaa		T _J = 25 °C		-	935	-	200			
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	1985	-	nC			

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Thermal resistance, junction-to-case	R _{thJC}		-	-	1.7	°C/W				
Weight			-	2.0	-	g				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C				
Marking device		Case style TO-220AC 2L	E5TH1512							

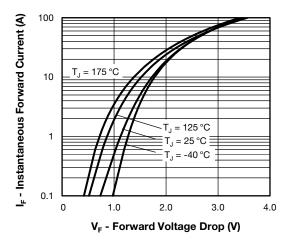


Fig. 1 - Forward Voltage Drop Characteristics

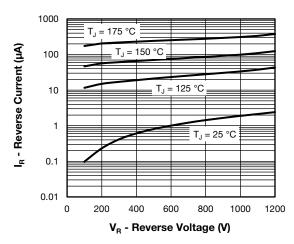


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



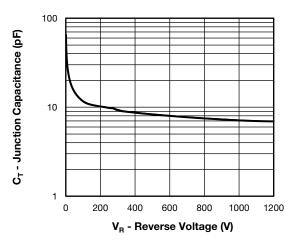


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

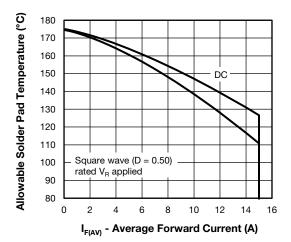


Fig. 4 - Maximum Allowable Case Temperature vs.
Average Forward Current

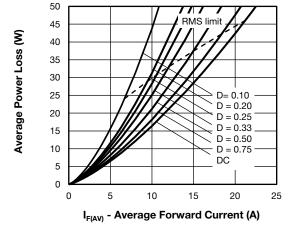


Fig. 5 - Forward Power Loss Characteristics

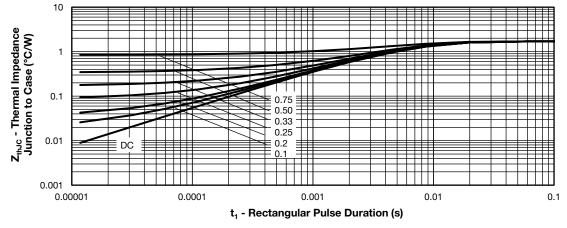
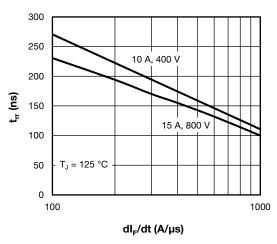


Fig. 6 - Transient Thermal Impedance, Junction to Case







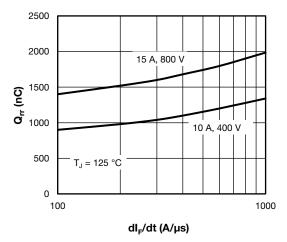


Fig. 8 - Typical Stored Charge vs. dl_F/dt

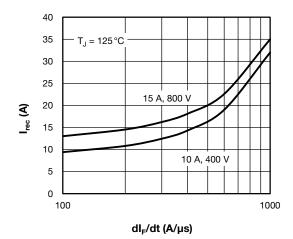


Fig. 9 - Typical Stored Charge vs. dl_F/dt

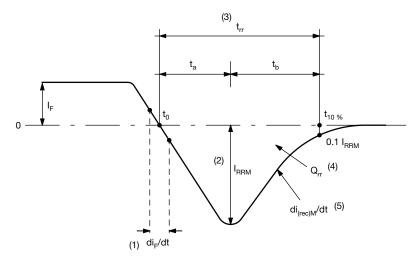


Fig. 10 - Reverse Recovery Waveform and Definitions

Notes

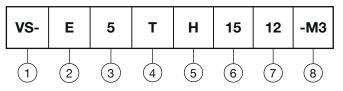
- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- $^{(3)}$ t_{rr} reverse recovery time measured from t_0 , crossing point of negative going I_F , to point $t_{10\%}$, 0.1 I_{RRM}
- $^{(4)}$ $\,$ Q $_{rr}$ area under curve defined by t_0 and t_{10} $_{\%}$

$$Q_{rr} = \int_{t_0}^{t_{10}\%} I(t)dt$$

 $^{(5)}$ di_(rec)M/dt - peak rate of change of current during t_b portion of t_{rr}

ORDERING INFORMATION TABLE





- 1 Vishay Semiconductors product
- 2 E = single diode
- 3 5 = FRED generation 5
- 4 Package:
 - T = TO-220AC 2L
- 5 H = hyperfast recovery
- 6 Current rating (15 = 15 A)
- 7 Voltage rating (12 = 1200 V)
- 8 Environmental digit:
 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	PACKAGING DESCRIPTION								
VS-E5TH1512-M3	50	Antistatic plastic tubes							

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96156
Part marking information	www.vishay.com/doc?95391



TO-220AB 3L

DIMENSIONS in millimeters and inches





Conforms to JEDEC® outline TO-220AB

SYMBOL	MILLIM	IETERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			Е	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355								

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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Vishay

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