## VS-16CTQ...S-M3, VS-16CTQ...-1-M3 Series

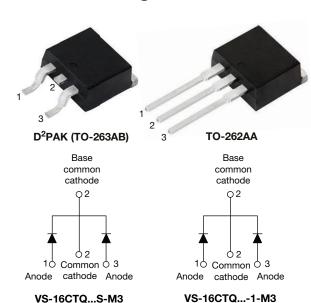
Vishay Semiconductors

COMPLIANT

HALOGEN

**FREE** 

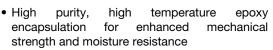
# High Performance Schottky Rectifier, 2 x 8 A



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 8 A				
$V_R$	60 V, 80 V, 100 V				
V <sub>F</sub> at I <sub>F</sub>	0.58 V				
I <sub>RM</sub>	7.0 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
E <sub>AS</sub>	7.5 mJ				
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Center tap configuration
- Low forward voltage drop



- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **DESCRIPTION**

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I <sub>F(AV)</sub>	Rectangular waveform	16	A				
$V_{RRM}$		60 to 100	V				
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	A				
V <sub>F</sub>	8 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.58	V				
TJ	Range	-55 to +175	°C				

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-16CTQ060S-M3 VS-16CTQ080S-M3 VS-16CTQ100S-M3 VS-16CTQ080-1-M3 VS-16CTQ080-1-M3 VS-16CTQ100-1-M3 UNIT							
Maximum DC reverse voltage	$V_R$	60	80	100	V		
Maximum working peak reverse voltage	$V_{RWM}$	60	60	100	V		



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ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum average per leg					8			
forward current See fig. 5	per device	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 148 °C, rectangular waveform		16	Α		
Maximum peak one cycle	е		5 μs sine or 3 μs rect. pulse	Following any rated load	850			
non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	275	Α		
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	$T_J = 25$ °C, $I_{AS} = 0.50$ A, $L = 60$ mH		7.50	mJ		
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		0.50	А		

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		8 A	T 05 %C	0.72	V			
Maximum forward voltage drop per leg See fig. 1	V (1)	16 A	T <sub>J</sub> = 25 °C	0.88				
	V <sub>FM</sub> <sup>(1)</sup>	8 A	T 105 °C	0.58				
		16 A	T <sub>J</sub> = 125 °C	0.69				
Maximum reverse leakage current per leg	ı (1)	T <sub>J</sub> = 25 °C	V Dated V	0.55	mA			
See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	7.0				
Threshold voltage	V <sub>F(TO)</sub>	T T manyimuum		0.415	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		11.07	mΩ			
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	500	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 r	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs			

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANI	THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 175	°C				
Maximum thermal resistance, junction to case per leg		D	DC operation	3.25					
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.63	°C/W				
Typical thermal resistance, case to heatsink		R <sub>thCS</sub> Mounting surface, smooth and greased		0.50					
Approximate weight				2	g				
Approximate weight				0.07	OZ.				
M minimu				6 (5)	kgf · cm				
Mounting torque	maximum			12 (10)	(lbf · in)				
Marking davisa	Madia da ta		Case style D <sup>2</sup> PAK (TO-263AB)	16CT	QS				
Marking device			Case style TO-262AA	16CT	Q1				

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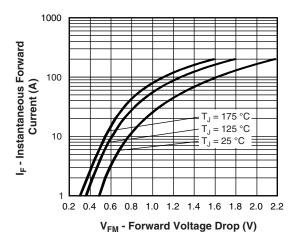


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

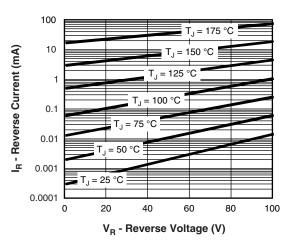


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

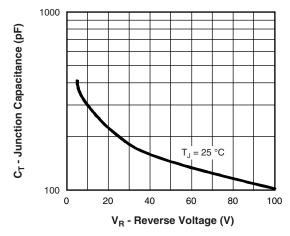


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

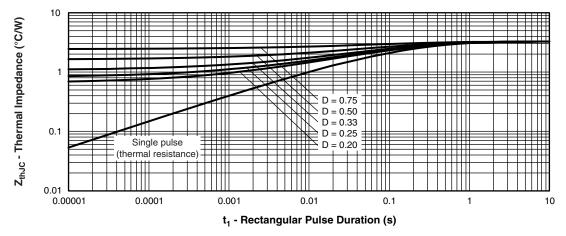


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

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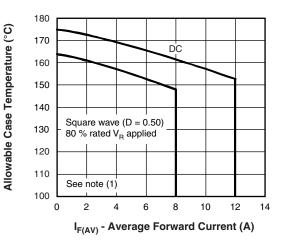


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

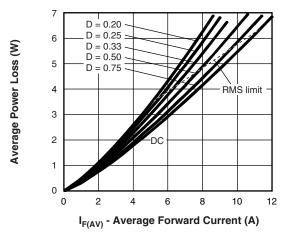


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

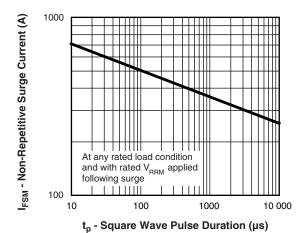


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

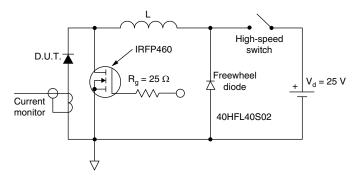


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

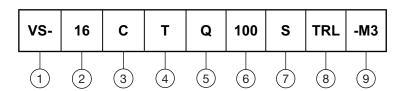
 $^{(1)}$  Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = <math display="inline">V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$  applied

# VS-16CTQ...S-M3, VS-16CTQ...-1-M3 Series

# Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code



Vishay Semiconductors product

Current rating (16 A)

Circuit configuration: C = common cathode

T = TO-220

Schottky "Q" series

060 = 60 V

080 = 80 VVoltage ratings 100 = 100 V

•  $S = D^2PAK (TO-263AB)$ 

• -1 = TO-262AA

8 • None = tube

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK (TO-263AB) only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK (TO-263AB) only)

9 -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION	ORDERING INFORMATION							
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION						
VS-16CTQ060S-M3	50	Antistatic plastic tubes						
VS-16CTQ060STRL-M3	800	13" diameter plastic tape and reel						
VS-16CTQ060STRR-M3	800	13" diameter plastic tape and reel						
VS-16CTQ080S-M3	50	Antistatic plastic tubes						
VS-16CTQ080STRL-M3	800	13" diameter plastic tape and reel						
VS-16CTQ080STRR-M3	800	13" diameter plastic tape and reel						
VS-16CTQ100S-M3	50	Antistatic plastic tubes						
VS-16CTQ100STRL-M3	800	13" diameter plastic tape and reel						
VS-16CTQ100STRR-M3	800	13" diameter plastic tape and reel						
VS-16CTQ100-1-M3	50	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS							
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164					
Dimensions	TO-262AA	www.vishay.com/doc?96165					
Post and in the smaller	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information		www.vishay.com/doc?96424					
SPICE model		www.vishay.com/doc?95279					



# Vishay Semiconductors

### D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		HES NOTES		SYMBOL	MILLIMETERS		INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

# Vishay Semiconductors

### **TO-262**

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	METERS	INC	INCHES			
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		
Α	4.06	4.83	0.160	0.190			
A1	2.03	3.02	0.080	0.119			
b	0.51	0.99	0.020	0.039			
b1	0.51	0.89	0.020	0.035	4		
b2	1.14	1.78	0.045	0.070			
b3	1.14	1.73	0.045	0.068	4		
С	0.38	0.74	0.015	0.029			
c1	0.38	0.58	0.015	0.023	4		
c2	1.14	1.65	0.045	0.065			
D	8.51	9.65	0.335	0.380	2		
D1	6.86	8.00	0.270	0.315	3		
E	9.65	10.67	0.380	0.420	2, 3		
E1	7.90	8.80	0.311	0.346	3		
е	2.54 BSC		0.100	BSC			
L	13.46	14.10	0.530	0.555			
L1	-	1.65	-	0.065	3		
L2	3.36	3.71	0.132	0.146			

### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- 5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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