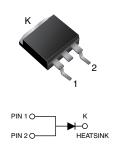


Vishay General Semiconductor

TMBS® (Trench MOS Barrier Schottky) Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.28 \text{ V}$ at $I_F = 5 \text{ A}$

D²PAK (TO-263AB)



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(DC)}	40 A			
V _{RRM}	45 V			
I _{FSM}	240 A			
V_F at $I_F = 40 A$	0.51 V			
T _{OP} max. (AC mode)	150 °C			
T _J max. (DC forward current)	200 °C			
Package	D ² PAK (TO-263AB)			
Circuit configuration	Single			

FEATURES

- · Trench MOS Schottky technology
- Low forward voltage drop, low power losses

• High efficiency operation

COMPLIANT HALOGEN

- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Epoxy meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VBT4045BP	UNIT		
Maximum repetitive peak reverse voltage	V_{RRM}	45	V		
Maximum DC forward bypassing current (fig. 1)	I _{F(DC)} (1)	40	Α		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	240	Α		
Operating junction temperature range (AC mode)	T _{OP}	-40 to +150	°C		
Junction temperature in DC forward current without reverse bias, $t \le 1\ h$	T _J ⁽¹⁾	≤ 200	°C		

Notes

⁽¹⁾ With heatsink

⁽²⁾ Meets the requirements of IEC 61215 Ed. 2 bypass diode thermal test



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.41	-	V
	I _F = 20 A			0.50	-	
	I _F = 40 A			0.57	0.67	
	I _F = 5 A	T _A = 125 °C		0.28	-	
	I _F = 20 A			0.41	-	
	I _F = 40 A			0.51	0.63	
Reverse current	V - 45 A	T _A = 25 °C	I _R ⁽²⁾	-	3000	μΑ
	$V_R = 45 \text{ A}$ $T_A = 125 \text{ °C}$	$T_A = 25 ^{\circ}\text{C}$ $T_A = 125 ^{\circ}\text{C}$		29	85	mA

Notes

(1) Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	OL VBT4045BP		
Typical thermal resistance	$R_{ heta JC}$	0.8	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
D ² PAK (TO-263AB)	VBT4045BP-M3/4W	1.37	4W	50/tube	Tube	
D ² PAK (TO-263AB)	VBT4045BP-M3/8W	1.37	8W	800/reel	Tape and reel	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

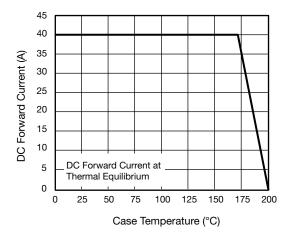


Fig. 1 - Forward Current Derating Curve

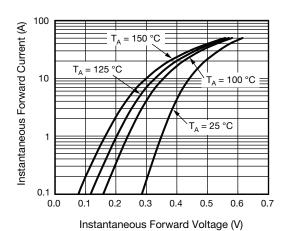
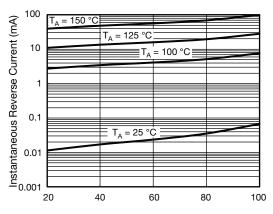


Fig. 2 - Typical Instantaneous Forward Characteristics



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Percent of Rated Peak reverse Voltage (%)

Fig. 3 - Typical Reverse Characteristics

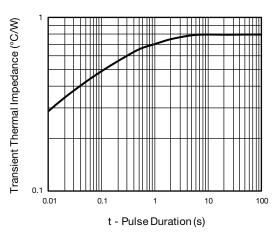


Fig. 5 - Typical Transient Thermal Impedance

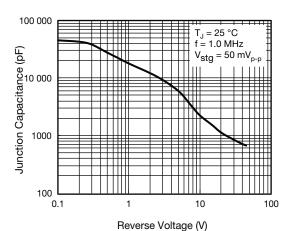
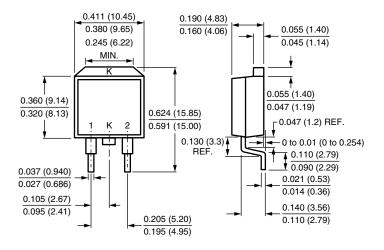


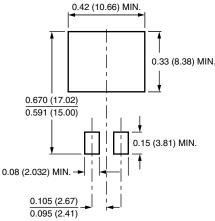
Fig. 4 - Typical Junction Capacitance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

D²PAK (TO-263AB)



Mounting Pad Layout





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