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Surface Mount Multilayer Ceramic Chip Capacitors With Lead-Bearing Finish Termination



FEATURES

- Tin / lead termination finish minimum 4 % lead
- Available in 0402 to 1210 body size
- Two dielectrics
- · High operating temperature, high reliability
- Wet build process
- Reliable Noble Metal Electrode (NME) system
- AEC-Q200 qualified with PPAP available
- · Custom combinations to meet specific need
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

APPLICATIONS

- · Low earth orbit satellites (LEO)
- Space
- Aerospace
- Avionic
- Military
- Tin whisker mitigation

ELECTRICAL SPECIFICATIONS

COG (NPO) DIELECTRIC (1)

GENERAL SPECIFICATION

Note

Electrical characteristics at +25 °C unless otherwise specified

Operating Temperature: -55 °C to +150 °C (above +125 °C changed characteristics)

Capacitance Range: 1 pF to 15 nF Voltage Range: $25 V_{DC}$ to $630 V_{DC}$

Temperature Coefficient of Capacitance (TCC): 0 ppm/°C ± 30 ppm/°C from -55 °C to +125 °C

Dissipation Factor (DF):

0.1 % maximum at 1.0 \acute{V}_{RMS} and 1 MHz for values \leq 1000 pF 0.1 % maximum at 1.0 V_{RMS} and 1 kHz for values > 1000 pF

Insulating Resistance:

at +25 °C 100 000 M Ω min. or 1000 Ω F whichever is less at +125 °C 10 000 M Ω min. or 100 Ω F whichever is less

Aging: 0 % maximum per decade

Dielectric Strength Test:

performed per method 103 of EIA 198-2-E.

Applied test voltages

 $\begin{array}{lll} \text{7-pind test voltage} \\ \text{500 V}_{\text{DC}}\text{-rated:} & 250 \text{ % of rated voltage} \\ \text{500 V}_{\text{DC}}\text{-rated:} & 200 \text{ % of rated voltage} \\ \text{630 V}_{\text{DC}}\text{-rated:} & 150 \text{ % of rated voltage} \\ \end{array}$

Note

(1) Under qualification; contact factory for availability

X7R DIELECTRIC

GENERAL SPECIFICATION

Note

Electrical characteristics at +25 °C unless otherwise specified

Operating Temperature: -55 °C to +150 °C (above +125 °C changed characteristics)

Capacitance Range: 120 pF to 1 μ F Voltage Range: 16 V_{DC} to 630 V_{DC}

Temperature Coefficient of Capacitance (TCC): \pm 15 % from -55 °C to +125 °C, with 0 V_{DC} applied

Dissipation Factor (DF):

16 V, 25 V ratings: 3.5 % maximum at 1.0 V_{RMS} and 1 kHz > 25 V ratings: 2.5 % maximum at 1.0 V_{RMS} and 1 kHz

Insulating Resistance:

at +25 °C 100 000 M Ω min. or 1000 Ω F whichever is less at +125 °C 10 000 M Ω min. or 100 Ω F whichever is less

Aging Rate: 1 % maximum per decade

Dielectric Strength Test:

performed per method 103 of EIA 198-2-E.

Applied test voltages

Revision: 06-Dec-2023 1 Document Number: 45256

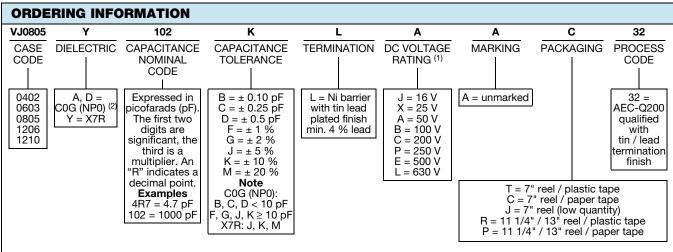


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QUICK REFERENCE DATA									
DIELECTRIC	CASE CODE	MAXIMUM VOLTAGE	CAPACITANCE						
DIELECTRIC	CASE CODE	(V)	MINIMUM	MAXIMUM					
	0402	100	1.0 pF	220 pF					
	0603	200	1.0 pF	820 pF					
C0G (NP0)	0805	500	1.0 pF	3.9 nF					
	1206	630	1.0 pF	8.2 nF					
	1210	630	100 pF	12 nF					
	0402	100	120 pF	33 nF					
	0603	200	330 pF	150 nF					
X7R	0805	200	330 pF	470 nF					
	1206	630	220 pF	1.0 μF					
	1210	630	390 pF	1.0 μF					

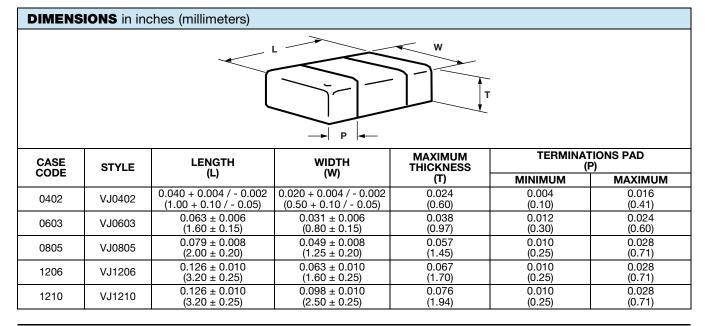
Note

Detail ratings see "Selection Chart"



Notes

- (1) DC voltage rating should not be exceeded in application. Other application factors may affect the MLCC performance. Consult for questions: mlcc@vishay.com
- (2) Consult Selection Chart table for correct dielectric code



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SELECTIO	N CHAR	T								
DIELECTRIC						COG (NPO)				
STYLE		VJ0	402		VJ0603			VJ	0805	
CASE CODE		040	02		0603			30	305	
VOLTAGE (V	oc)	25 / 50	100	50	100	200	50	100	200	500
VOLTAGE CO	DDE	X/A	В	Α	В	С	Α	В	С	Е
CAP. CODE	CAP.									
1R0	1.0 pF	••	••	••	••	••	••	••	••	••
1R2	1.2 pF	••	••	••	••	••	••	••	••	••
1R5	1.5 pF	••	••	••	••	••	••	••	••	••
1R8	1.8 pF	••	••	••	••	••	••	••	••	••
2R2	2.2 pF	••	••	••	••	••	••	••	••	••
2R7	2.7 pF	••	••	••	••	••	••	••	••	••
3R3	3.3 pF	••	••	••	••	••	••	••	••	••
3R9	3.9 pF	••	••	••	••	••	••	••	••	••
4R7	4.7 pF	••	••	••	••	••	••	••	••	••
5R6	5.6 pF	••	••	••	••	••	••	••	••	••
6R8	6.8 pF	••	••	••	••	••	••	••	••	••
8R2	8.2 pF	••	••	••	••	••	••	••	••	••
100	10 pF	••	••	••	••	••	••	••	••	••
120	12 pF	••	••	••	••	••	••	••	••	••
150	15 pF	••	••	••	••	••	••	••	••	••
180	18 pF	••	••	••	••	••	••	••	••	••
220	22 pF	••	••	••	••	••	••	••	••	••
270	27 pF	••	••	••	••	••	••	••	••	••
330	33 pF	••	••	••	••	••	••	••	••	••
390	39 pF	••	••	••	••	••	••	••	••	••
470	47 pF	••	••	••	••	••	••	••	••	••
560	56 pF	••	••	••	••	••	••	••	••	••
680	68 pF	••	••	••	••	••	••	••	••	••
820	82 pF	••	••	••	••	••	••	••	••	••
101	100 pF	••	••	••	••	••	••	••	••	••
121	120 pF	••	••	••	••	••	••	••	••	••
151	150 pF	••		••	••	••	••	••	••	••
181	180 pF	••		••	••	•	••	••	••	••
221	220 pF	••		••	••	•	••	••	••	•
271	270 pF			••	••	•	••	••	••	•
331	330 pF			••	••		••	••	••	•
391	390 pF			••	••		••	••	••	•
471	470 pF			••	••		••	••	•	•
561	560 pF			••			••	••	•	
681	680 pF			••			••	••	•	
821	820 pF			••			••	••	•	

Notes

These values use dielectric code "D". All other NP0 (C0G) part numbers use dielectric code "A"

[•] Paper tape, • Plastic tape



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SELECTIO	N CHAR	Т								
DIELECTRIC						C0G (NP0)				
STYLE		VJ04	102		VJ0603			VJC	805	
CASE CODE		040)2		0603			08	805	
VOLTAGE (VD	oc)	25 / 50	100	50	100	200	50	100	200	500
VOLTAGE CO	DE	X/A	В	Α	В	С	Α	В	С	E
CAP. CODE	CAP.									
102	1.0 nF						••	••	•	
122	1.2 nF						••	•		
152	1.5 nF						••	•		
182	1.8 nF						•	•		
222	2.2 nF						•			
272	2.7 nF						•			
332	3.3 nF						•			
392	3.9 nF						•			
472	4.7 nF									
562	5.6 nF									
682	6.8 nF									
822	8.2 nF									
103	10 nF									
123	12 nF									
153	15 nF									
183	18 nF									
223	22 nF									
273	27 nF									
333	33 nF									
393	39 nF									
473	47 nF									
563	56 nF									
					1	1		1	1	<u> </u>

Note

^{••} Paper tape, • Plastic tape

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SELECTIO	ON CHART								
DIELECTRIC					COG	(NP0)			
STYLE			VJ1	1206			VJ1	210	
CASE CODE			1	206	·		12	210	
VOLTAGE (V		50	100	200	500 / 630	50	100	200	500 / 630
VOLTAGE CO		Α	В	С	E/L	Α	В	С	E/L
CAP. CODE	CAP.			••					
1R0	1.0 pF	••	••		••				
1R2	1.2 pF	••	••	••	••				
1R5	1.5 pF	••	••	••	••				
1R8	1.8 pF	••	••	••	••				
2R2	2.2 pF	••	••	••	••				
2R7	2.7 pF	••	••	••	••				
3R3	3.3 pF	••	••	••	••				
3R9	3.9 pF	••	••	••	••				
4R7	4.7 pF	••	••	••	••				
5R6	5.6 pF	••	••	••	••				
6R8	6.8 pF	••	••	••	••				
8R2	8.2 pF	••	••	••	••				
100	10 pF	••	••	••	••				
120	12 pF	••	••	••	••				
150	15 pF	••	••	••	••				
180	18 pF	••	••	••	••				
220	22 pF	••	••	••	••				
270	27 pF	••	••	••	••				
330	33 pF	••	••	••	••				
390	39 pF	••	••	••	••				
470	47 pF	••	••	••	••				
560	56 pF	••	••	••	••				
680	68 pF	••	••	••	••				
820	82 pF	••	••	••	••				
101	100 pF	•	•	•	•	•	•	•	•
121	120 pF	•	•	•	•	•	•	•	•
151	150 pF	•	•	•	•	•	•	•	•
181	180 pF	•	•	•	•	•	•	•	•
221	220 pF	•	•	•	•	•	•	•	•
271	270 pF	•	•	•	•	•	•	•	•
331	330 pF	•	•	•	•	•	•	•	•
391	390 pF	•	•	•	•	•	•	•	•
471	470 pF	•	•	•	•	•	•	•	•
561	560 pF	•	•	•	•	•	•	•	•
681	680 pF	•	•	•	•	•	•	•	•
821	820 pF	•	•	•	•	•	•	•	•
021	020 pi		_					<u> </u>	

Notes

• Paper tape, • Plastic tape

These values use dielectric code "D". All other NP0 (C0G) part numbers use dielectric code "A"



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DIELECTRIC					C0G (I	VPO)			
STYLE			V.I ⁴	1206	1) 200	11 0)	V.11	210	
CASE CODE				206				210	
VOLTAGE (V _{DO}	.)	50	100	200	500 / 630	50	100	200	500 / 630
VOLTAGE COL		Α	В	С	E/L	Α	В	С	E/L
CAP. CODE	CAP.								
102	1.0 nF	•	•	•	•	•	•	•	•
122	1.2 nF	•	•	•		•	•	•	•
152	1.5 nF	•	•	•		•	•	•	•
182	1.8 nF	•	•	•		•	•	•	•
222	2.2 nF	•	•	•		•	•	•	
272	2.7 nF	•	•	•		•	•	•	
332	3.3 nF	•	•	•		•	•	•	
392	3.9 nF	•	•			•	•	•	
472	4.7 nF	•	•			•	•	•	
562	5.6 nF	•	•			•	•	•	
682	6.8 nF	•	•			•	•	•	
822	8.2 nF	•	•			•	•	•	
103	10 nF					•	•		
123	12 nF					•	•		
153	15 nF								
183	18 nF								
223	22 nF								
273	27 nF								
333	33 nF								
393	39 nF								
473	47 nF								
563	56 nF								

Note

[•] Paper tape, • Plastic tape

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SELECTIO	N CHAF	RT													
DIELECTRIC								X	7R						
STYLE			VJC	402				VJ0603					VJ0805	,	
CASE CODE			04	102				0603			0805				
VOLTAGE (VD	c)	16	25	50	100	16	25	50	100	200	16	25	50	100	200
VOLTAGE CO	DE	J	Х	Α	В	J	Х	Α	В	С	J	Х	Α	В	С
CAP. CODE	CAP.														
121	120 pF	••	••	••	••										
151	150 pF	••	••	••	••										
181	180 pF	••	••	••	••										
221	220 pF	••	••	••	••										
271	270 pF	••	••	••	••										
331	330 pF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
391	390 pF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
471	470 pF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
561	560 pF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
681	680 pF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
821	820 pF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
102	1.0 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
122	1.2 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
152	1.5 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
182	1.8 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
222	2.2 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
272	2.7 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
332	3.3 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
392	3.9 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
472	4.7 nF	••	••	••	••	••	••	••	••	••	••	••	••	••	••
562	5.6 nF	••	••	••		••	••	••	••		••	••	••	••	••
682	6.8 nF	••	••	••		••	••	••	••		••	••	••	••	••
822	8.2 nF	••	••	••		••	••	••	••		••	••	••	••	••
103	10 nF	••	••	••		••	••	••	••		••	••	••	••	••
123	12 nF	••	••			••	••	••	••		••	••	••	••	•
153	15 nF	••	••			••	••	••	••		••	••	••	••	•
183	18 nF	••	••			••	••	••	••		••	••	••	••	•
223	22 nF	••				••	••	••	••		••	••	••	••	•
273	27 nF	••				••	••	••	••		••	••	••	••	•
333	33 nF	••				••	••	••	••		••	••	••		
393	39 nF					••	••	••	••		••	••	••		
473	47 nF					••	••	••			••	••			
563	56 nF					••	••	••			••	••		•	
683	68 nF					••	••	••			•	•	•	•	
823	82 nF					••	••	••			•	•	•	•	

Note

• Paper tape, • Plastic tape



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SELECTIO	N CHAF	₹T													
DIELECTRIC								X	7R						
STYLE			VJC)402				VJ0603	}				VJ0805	;	
CASE CODE			04	102				0603					0805		
VOLTAGE (V	c)	16	25	50	100	16	25	50	100	200	16	25	50	100	200
VOLTAGE CO	DE	J	Х	Α	В	J	Х	Α	В	С	J	Х	Α	В	С
CAP. CODE	CAP.														
104	100 nF					••	••	••			•	•	•	•	
124	120 nF					•					•	•	•		
154	150 nF					•					•	•	•		
184	180 nF										•	•			
224	220 nF										•	•			
274	270 nF										•	•			
334	330 nF										•	•			
394	390 nF										•				
474	470 nF										•				
564	560 nF														
684	680 nF														
824	820 nF														
105	1.0 µF														
125	1.2 µF														
155	1.5 µF														
185	1.8 µF														
225	2.2 µF														
275	2.7 µF														
335	3.3 µF														
395	3.9 µF														
475	4.7 μF														
565	5.6 µF														
685	6.8 µF														

Note

• Paper tape, • Plastic tape

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DIELECTRIC							X7	'R					
STYLE				٧٠	J1206					٧٠	J1210		
CASE CODE				1	206					1	1210		
VOLTAGE (VDC	c)	16	25	50	100	200	500 / 630	16	25	50	100	200	500 / 630
VOLTAGE COI	DE	J	Х	Α	В	С	E/L	J	Х	Α	В	С	E/L
CAP. CODE	CAP.												
121	120 pF												
151	150 pF												
181	180 pF												
221	220 pF						•						
271	270 pF						•						
331	330 pF						•						
391	390 pF						•						•
471	470 pF						•						•
561	560 pF						•						•
681	680 pF						•						•
821	820 pF			•	•	•	•						•
102	1.0 nF	•	•	•	•	•	•						•
122	1.2 nF	•	•	•	•	•	•						•
152	1.5 nF	•	•	•	•	•	•						•
182	1.8 nF	•	•	•	•	•	•						•
222	2.2 nF	•	•	•	•	•	•					•	•
272	2.7 nF	•	•	•	•	•	•					•	•
332	3.3 nF	•	•	•	•	•	•					•	•
392	3.9 nF	•	•	•	•	•	•					•	•
472	4.7 nF	•	•	•	•	•	•					•	•
562	5.6 nF	•	•	•	•	•	•					•	•
682	6.8 nF	•	•	•	•	•	•					•	•
822	8.2 nF	•	•	•	•	•	•					•	•
103	10 nF	•	•	•	•	•	•	•	•	•	•	•	•
123	12 nF	•	•	•	•	•	•	•	•	•	•	•	•
153	15 nF	•	•	•	•	•	•	•	•	•	•	•	•
183	18 nF	•	•	•	•	•	•	•	•	•	•	•	•
223	22 nF	•	•	•	•	•		•	•	•	•	•	•
273	27 nF	•	•	•	•	•		•	•	•	•	•	•
333	33 nF	•	•	•	•	•		•	•	•	•	•	•
393	39 nF	•	•	•	•	•		•	•	•	•	•	•
473	47 nF	•	•	•	•	•		•	•	•	•	•	
563	56 nF	•	•	•	•	•		•	•	•	•	•	
683	68 nF	•	•	•	•	•		•	•	•	•	•	
823	82 nF	•	•	•	•	•		•	•	•	•	•	

Note

• Plastic tape



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DIELECTRIC		X7R											
STYLE				V	J1206					V	J1210		
CASE CODE				•	1206			1210					
VOLTAGE (VDC	;)	16	25	50	100	200	500 / 630	16	25	50	100	200	500 / 630
VOLTAGE COI	DE	J	Х	Α	В	С	E/L	J	х	Α	В	С	E/L
CAP. CODE	CAP.												
104	100 nF	•	•	•	•	•		•	•	•	•	•	
124	120 nF	•	•	•	•	•		•	•	•	•	•	
154	150 nF	•	•	•	•			•	•	•	•	•	
184	180 nF	•	•	•	•			•	•	•	•	•	
224	220 nF	•	•	•	•			•	•	•	•		
274	270 nF	•	•	•	•			•	•	•	•		
334	330 nF	•	•	•				•	•	•	•		
394	390 nF	•	•	•				•	•	•	•		
474	470 nF	•	•	•				•	•	•	•		
564	560 nF	•	•					•	•	•			
684	680 nF	•	•					•	•	•			
824	820 nF	•	•					•	•	•			
105	1.0 μF	•	•					•	•	•			
125	1.2 µF												
155	1.5 µF												
185	1.8 µF												
225	2.2 μF												
275	2.7 µF												
335	3.3 µF												
395	3.9 µF												
475	4.7 μF												
565	5.6 µF												
685	6.8 µF												

Note

• Plastic tape



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STANDA	TANDARD PACKAGING QUANTITIES (1)(2)									
			7" REEL QUANTITIES	i	11 1/4" AND 13" F	REEL QUANTITIES				
CASE CODE	TAPE SIZE	PAPER TAPE PACKAGING CODE "C"	PLASTIC TAPE PACKAGING CODE "T"	LOW QUANTITY PACKAGING CODE "J"	PAPER TAPE PACKAGING CODE "P"	PLASTIC TAPE PACKAGING CODE "R"				
0402	8 mm	5000	n/a	1000	10 000	n/a				
0603	8 mm	4000	4000	1000	10 000	10 000				
0805	8 mm	3000	3000	1000	10 000	10 000				
1206 ⁽³⁾	8 mm	3000	2500 / 3000	1000	10 000	9000 / 10 000				
1210 ⁽³⁾	8 mm	n/a	2000 / 2500 / 3000	1000	n/a	9000 / 10 000				

Notes

⁽¹⁾ Reference: EIA standard RS 481 - "Taping of Surface Mount Components for Automatic Placement"

⁽²⁾ n/a = not available

⁽³⁾ Packaging quantity can depend from product thickness



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1 - GENERAL CERTIFICATES

# Quality management system according to IATF 16949	Yes
# Quality management system according to ISO 9001	Yes
# Environmental certification according to ISO 14001	Yes
# Health and safety system according to ISO 45001	Yes

2 - TECHNICAL REQUIREMENTS

Unless specified in component specification, these parameters are the minimum requirements for the components.

2.1 OPERATING TEMPERATURE RANGE

For standard applications	T _A : -55 °C to +125 °C	See characteristics 2.2
For high temperature applications	T _A : -55 °C to +150 °C	See characteristics 2.2
For ultra high temperature applications	T _A : -55 °C to +175 °C	See characteristics 2.2

2.2 CHARACTERISTICS

PARAMETER	CERAMIC TYPE	SYMBOL	RATINGS	TEST CONDITIONS / REMARKS
Dated with the interest was arranged for a 105 °C	C0G (NP0)	U _R	25 V to 630 V	
Rated voltage in temperature range -55 °C to +125 °C	X7R		16 V to 630 V	
	C0G (NP0)		25 V to 100 V	$U_{DC} \le \frac{1}{2} U_{R}$
Derating at higher temperature up to +150 °C	X7R		16 V to 100 V	$U_{DC} \le {}^{1}/_{2} U_{R}$
3				$U_{DC} \le {}^{1}/_{4} U_{R}$ for VJ0603Y104*A (100 nF / 50 V)
Derating at higher temperature up to +175 °C	C0G (NP0)		25 V to 100 V	$U_{DC} \le \frac{1}{4} U_{R}$
	X7R		16 V to 100 V	$U_{DC} \le \frac{1}{4} U_{R}$
Temperature coefficient in temperature range	C0G (NP0)	α_{C}	≤ ± 30 ppm/°C	if $C_R < 10$ pF: $\alpha_C \le \pm 120$ ppm/°C
-55 °C to +125 °C	X7R	ΔC	+ 15 % / - 30 %	
Temperature coefficient in temperature range	C0G (NP0)	α_{C}	≤ ± 30 ppm/°C	if $C_R < 10$ pF: $\alpha_C \le \pm 120$ ppm/°C
-55 °C to +150 °C	X7R	ΔC	+ 15 % / - 30 %	
Temperature coefficient in temperature range -55 °C to +175 °C	X7R	ΔC	+ 15 % / - 50 %	
Dissipation factor in temperature range -55 °C to +175 °C	C0G (NP0)	tan δ	≤ 0.0015	
	X7R		≤ 0.06	

2.3 STORAGE AND HANDLING CONDITIONS

- (1) Store the components at 5 $^{\circ}$ C to 40 $^{\circ}$ C ambient temperature and \leq 70 % relative humidity conditions.
- (2) The product is recommended to be used within a time-frame of 2 years after shipment. Check solderability in case extended shelf life beyond the expiry date is needed.

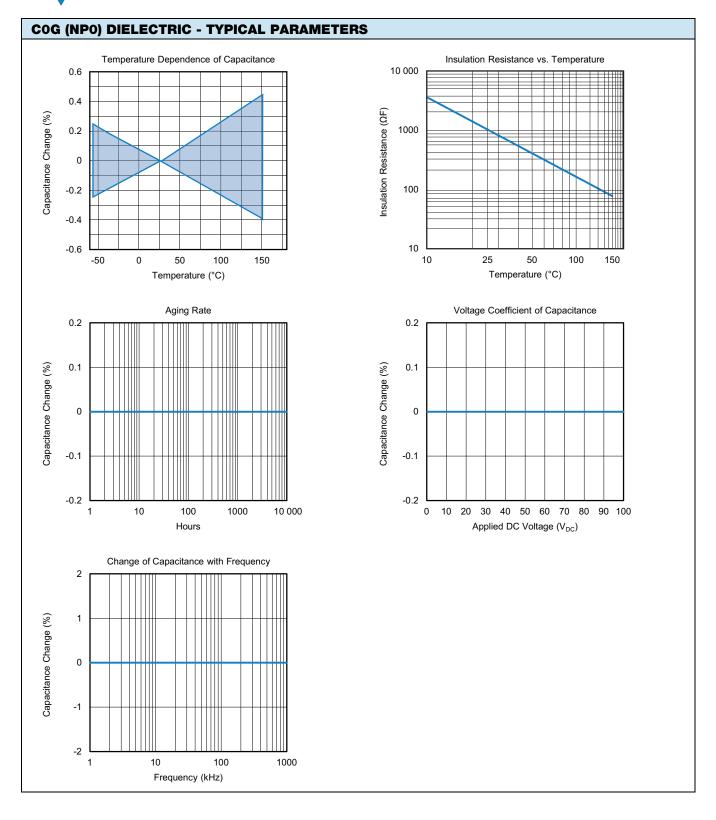
Precautions:

- a. Do not store products in an environment containing corrosive elements, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. This may cause corrosion or oxidization of the terminations, which can easily lead to poor soldering.
- b. Store products on the shelf and avoid exposure to moisture or dust.
- c. Do not expose products to excessive shock, vibration, direct sunlight, and so on.



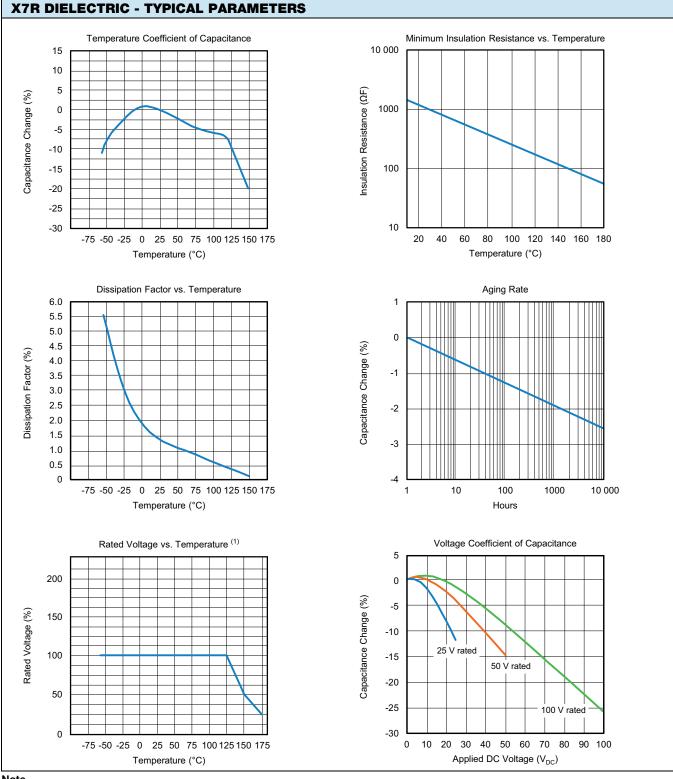


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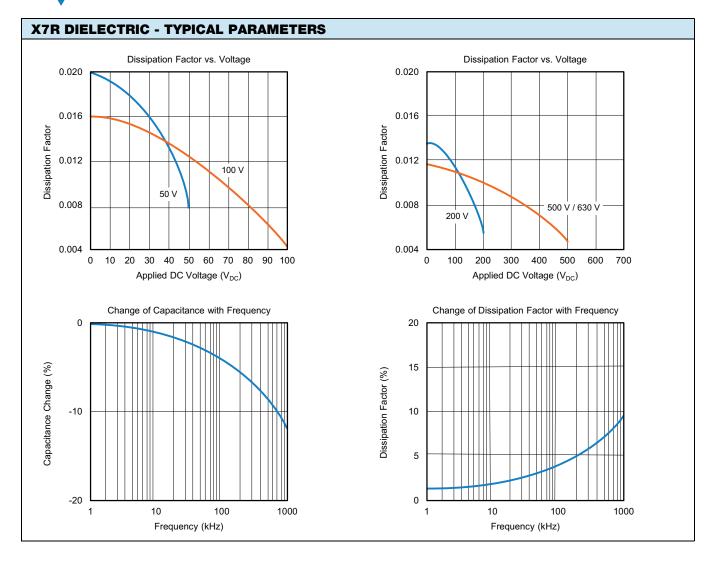
Note

⁽¹⁾ Except for VJ0603Y104*A (100 nF / 50 V), see section "2.2 Characteristics"





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3 - LOT ACCEPTANCE TESTS

Process tests available in classes (on request)

GROUP	ACTION
Α	Components are tested within the monitoring program of the supplier. The supplier shall submit the part numbers of the selected component to the customer during the component specification discussions.
В	Components (customer P/N) shall be tested quarterly. Records available only on special request by the customer.
С	Test with each shipment. Records are provided on a monthly basis. Customer special requirement; requirement should be determined in a specific component specification.

Upon request the records can be submitted in electronic format on monthly basis.

3.1 THERMAL STRENGTH, THERMAL SHOCK SENSIBILITY

Sample size	200	
Handling	Mounted on PCB	
Thermal shock	1 x 280 °C, no pre-heat, 5 s to 10 s	
IR - test (IRATS)	U = U _R , T = room temperature, verified	
Burn in (BIATS)	Equivalent to 12 h burn-in, 2 x U _R /125 °C, verification time to failure	

Acceptance criteria: zero defects (IRATS and BIATS).

3.2 BOARD FLEX TEST

Sample size	20 pcs/lot
Frequency	At least three different part numbers of one component family matrix per quarter
Max. deflection	8 mm (data to be reported, available on request)

3.3 SOLDERABILITY / RESISTANCE TO SOLDERING HEAT

Temperature profile for reflow soldering of SMD parts IPC/JEDEC-J-STD-020C.

Test is done on a regular basis for samples taken randomly out of the line.

Acceptance criteria: at least 95 % new solder and no detachment or leaching of terminations.

4 - ENVIRONMENTAL REQUIREMENTS

A list of the chemical substances content, which must not be used or whose use shall be limited by international law, is available on request.



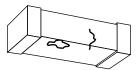
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5 - INSPECTION CRITERIA

The supplier shall carry out visual examination with suitable equipment with approximately 10 x magnification and lighting appropriate to the specimen under test and the required quality level.

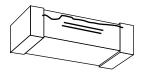
Chipping

The components shall be free of cracks or fissures. Small damages which do not deteriorate the performance of the component shall be less than 50 % of the surface of the MLCC as defined in EIA 595.



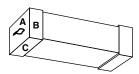
Delamination or Exposed Electrodes

No visible separation or delamination between layers of the capacitor and no exposed electrodes between the two terminals of the capacitor must be seen.



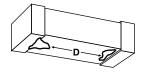
Metallization

For the metallization, no visible detachment of the metallized terminals and no exposed electrodes must be seen. Defects and gaps in the metallization on each sides of the terminal must not exceed 10 % of the total area (e.g. A, B, C, ...). Leaching shall not exceed 25 %.



Electrode Distance

The ceramic body shall be free of any conducting material between the terminals which reduces the distance of the electrodes. The minimum distance "D" is 400 μ m for all package sizes, except 0402. For the component package 0402 the minimum distance is 200 μ m.



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6 - BOARD FLEX TEST CONDITIONS

6.1 BOARD FLEX DEFINITIONS OF TEST

PCB thickness = (1.6 ± 0.1) mm

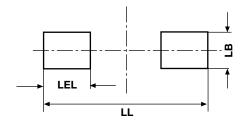
Copper thickness = 35 µm

Material FR4 (EP-GC 02 according to DIN 40 802)

LAYOUT / PAD DESIGN (Dimensions in mm)				
CASE CODE	PAD SIZE			
	LL	LB	LEL	
0603	2.20	1.00	0.75	
0805	3.40	1.30	1.20	
1206	4.50	1.80	1.20	
1210	4.50	2.80	1.30	

Note

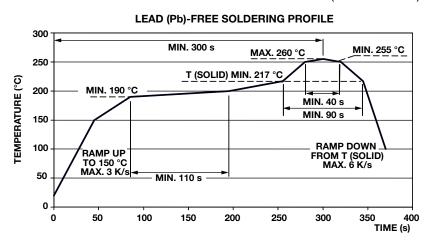
• LL = total length; LB = width of the pad; LEL = single pad length



6.2 SOLDERING INSTRUCTIONS

THICKNESS, RECOMMENDED FOR SOLDER PASTE (Reflow soldering)		
CASE CODE	THICKNESS in µm	
0402	75 to 90	
0603	150 to 200	
0805	150 to 200	
1206	150 to 200	
1210	150 to 200	

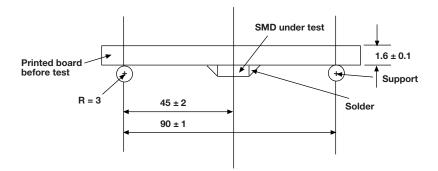
6.3 TYPICAL TEMPERATURE PROFILE FOR REFLOW SOLDERING (Boardflex test)



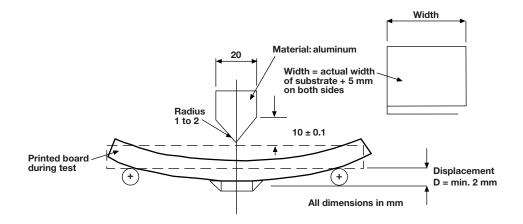
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6.4 MOUNTING, DIMENSIONS, AND TESTING

Mounting



Testing



6.5 PERFORMANCE OF THE TEST(S)

- A) Electrical test according to component specification (Cap, DF, IR)
- B) Mounting to PCB
- C) Storage at room temperature (min. 10 h)
- D) Board flex test

6.6 DETAILS

X7R	PCB to be deflected continuously, speed 1 mm/s (± 0.5 mm/s)
COG	PCB to be deflected in steps until cracks or other damages are visible or can be measured. Dwell time between steps: (5 ± 1) s

6.7 FAILURE CRITERIA

X7R	Piezoelectric sensor, no failure up to min. 2 mm
COG (NPO)	Δ C/C < 1 % or < 1 pF, no failure up to min. 2 mm
Both	Electrical test according to component specification

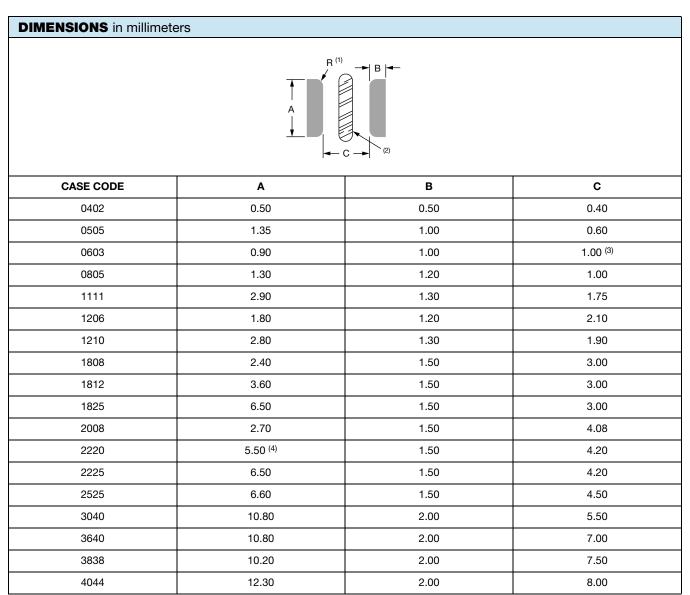
7 - QUALIFICATION TESTING

Qualification is performed per the appropriate tests as detailed in the "Stress Qualifications for Ceramic Capacitors" section of the prevailing revision of AEC-Q200 specification.



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Solder Pad Dimensions for Vishay Surface-Mount Multilayer Ceramic Chip Capacitors



Notes

⁽¹⁾ For safety capacitors and voltages above 3000 V, corner rounding (R) of 0.5 mm is recommended to suppress arcing

⁽²⁾ Add a 1 mm slot in PCB between pads to allow cleaning and coating under MLCC

⁽³⁾ For VJ HiFREQ Series, this dimension is 0.6 mm

⁽⁴⁾ For safety capacitors, the A dimension should be 5.80 mm

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Guidelines for MLCC Solder Pads and PCBs

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PRINTED CIRCUIT BOARD PCB DESIGN CONSIDERATIONS FOR HIGH VOLTAGE SURFACE-MOUNT MLCCS

Special assembly process and design considerations should be employed for today's high voltage rating MLCCs. As case sizes remain the same and voltage ratings increase, MLCC manufacturers must design, evaluate, and qualify their capacitors using methods that reduce the occurrence of corona discharge and arcover events. To meet similar capability in high voltage applications, users should employ similar cautionary design and assembly methods.

MLCC PAD LAYOUT

A capacitor's arcover inception point can degrade due to factors such as the MLCC termination, PCB pad design, PCB cleanliness, solder flux residue, surface contamination / deposits and environmental conditions. PCB pads and their design affect the air gap distance between the opposing polarities of the MLCC termination. For voltage rating greater than 1500 V_{DC} add a corner radius to the inward facing edge of the MLCC pads and as large a gap as possible between the pads. Too small of a pad gap distance will reduce the capacitor's own arcover inception voltage level. Refer to the Figure and Table Figure 1.0, MLCC Pad Layout and Table 1.0, Vishay MLCC Solder Pad Dimensions for the recommended MLCC solder pad dimensions.

SLOT OR TRENCH BETWEEN PADS

PCB assembly can deposit dust, trap solder balls, or flux residue underneath the capacitors. These contaminants will reduce conductive clearances and the arcover inception level. Assembly methods must include a final PCB cleaning process. A slot or trench can be cut into the PCB in between the pads to allow cleaners to penetrate underneath the MLCC. The slot will also allow conformal or epoxy coatings to flow underneath the MLCC and build an insulative barrier between pads. Refer to Figure 1.0 MLCC Pad Layout for slot reference location.

COATING PRINTED CIRCUIT BOARD

Coating a printed circuit board with materials such as acrylic, silicone and urethane resins provide a protective dielectric barrier that is non-conductive and will enhance the resistance to arcing. Various processes exist which include dipping, brushing, and spaying. Optimal performance will come from coating the MLCC on all sides, top and bottom. The PCB slot in between the pads should extend slightly beyond the width of the MLCC. Refer to Figure 1.0 MLCC Pad Layout for slot reference location.



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