RoHS

COMPLIANT HALOGEN

FREE

High Performance Schottky Rectifier, 100 A



PowerTab[®]

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	100 A			
V_R	15 V			
V _F at I _F	0.45 V			
I _{RM}	870 mA at 100 °C			
T _J max.	125 °C			
E _{AS}	9 mJ			
Package	PowerTab [®]			
Circuit configuration	Single			

FEATURES

- Ultralow forward voltage drop
- · Optimized for OR-ing applications
- Guard ring for enhanced ruggedness and long term reliability
- Screw mounting only
- AEC-Q101 qualified
- 125 °C max. operating junction temperature (V_R < 5 V)
- High frequency operation
- · Continuous high current operation
- PowerTab[®] package
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-100BGQ015HN4 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MECHANICAL DATA

Case: PowerTab®

Molding compound meets UL 94 V-0 flammability rating

Terminal: nickel plated, screwable

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
1	Rectangular waveform	100	Α	
I _F (AV)	T _C	88	°C	
V _{RRM}		15	V	
I _{FSM}	t _p = 5 μs sine	5000	Α	
V _F	100 A _{pk} (typical)	0.39	V	
	T _J	125	°C	
T_J	Range	-55 to +125	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VS-100BGQ015HF4	UNITS
Maximum DC reverse voltage	V_{R}	T _J = 100 °C	15	V
Maximum DC reverse voltage	٧R	T _J = 125 °C	5	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 88 °C, rectangular waveform 100 A		Α	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load	5000	
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	900	А
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 4.5 \text{mH}$ 9 mJ		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 3 x V _R typical 2 A		A	



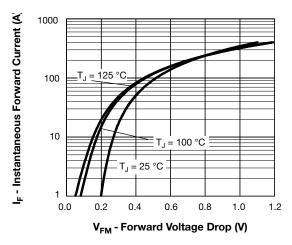
ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		MAX.	UNITS
	V _{FM} ⁽¹⁾	50 A	T _J = 25 °C	0.36	0.4	V
Forward voltage drop		100 A		0.45	0.52	
r orward voltage drop		50 A	T _J = 125 °C	0.27	0.31	
		100 A		0.39	0.45	
	I _{RM} ⁽¹⁾	$T_J = 100 ^{\circ}\text{C}, V_R = 12 ^{\circ}\text{V}$		480	700	mA
Maximum reverse leakage current		$T_J = 125 ^{\circ}\text{C}, V_R = 5 ^{\circ}\text{V}$		1	1.23	Α
		T _J = 25 °C	V _R = Rated V _R	7	20	mA
		T _J = 100 °C		580	870	IIIA
Maximum junction capacitance	C _T	V_R = 5 V_{DC} , (test signal range 100 kHz to 1 MHz), 25 °C		38	00	pF
Typical series inductance	L _S	Measured from tab to mounting plane		3.	.5	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10	000	V/µs

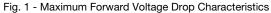
Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS
Maximum junction temper	rature range	TJ		-55 to +125	°C
Maximum storage temper	rature range	T _{Stg}		-55 to +150	30
Maximum thermal resistar junction to case	nce,	R _{thJC}	DC operation	0.50	°C ///
Maximum thermal resistar case to heatsink	nce,	R _{thCS}	Mounting surface, smooth and greased	0.30 °C/W	
Approximate weight				5	g
Mounting torque —	minimum			1.2 (10)	N⋅m
wounting torque —	maximum			2.4 (20)	(lbf \cdot in)
Marking device			Case style PowerTab®	100BGQ015H	







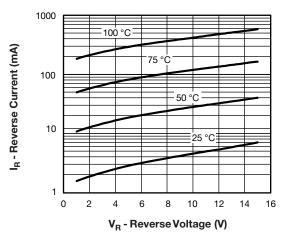


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

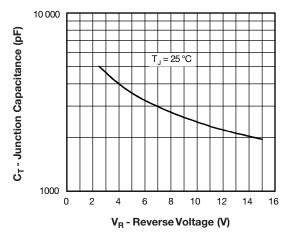


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

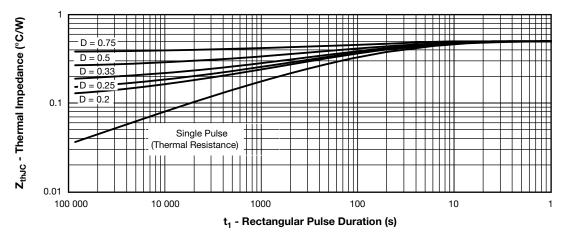


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

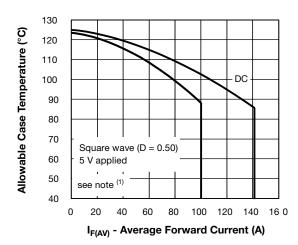


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

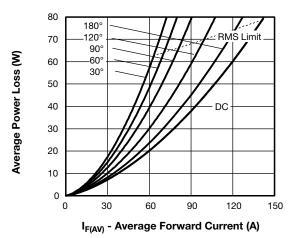


Fig. 6 - Forward Power Loss Characteristics

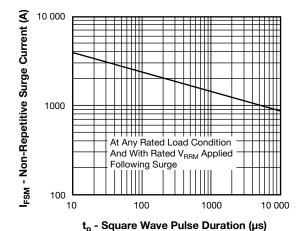


Fig. 7 - Maximum Non-Repetitive Surge Current

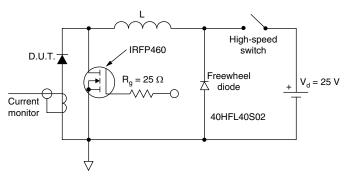


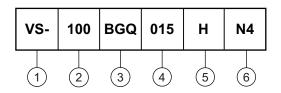
Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 5 V

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (100 = 100 A)

3 - Essential part number

4 - Voltage rating (015 = 15 V)

5 - H = AEC-Q101 qualified

6 - Environmental digit:

- N4 = Halogen-free, RoHS compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION		
VS-100BGQ015HN4	25/tube	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95240		
Part marking information	www.vishay.com/doc?95467		
SPICE model	www.vishay.com/doc?95428		
Application note	www.vishay.com/doc?95179		



PowerTab®

DIMENSIONS in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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Vishay

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