**FREE** 



## Vishay Semiconductors

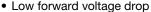
# **High Performance Schottky Rectifier, 1 A**

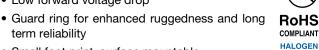


**SMB (DO-214AA)** 

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1 A			
$V_{R}$	100 V			
V <sub>F</sub> at I <sub>F</sub>	0.59 V			
I <sub>RM</sub>	1 mA at 125 °C			
T <sub>J</sub> max.	175 °C			
E <sub>AS</sub>	1.0 mJ			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

#### **FEATURES**





• Small foot print, surface mountable

High frequency operation

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION**

The VS-10BQ100HM3 surface-mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN		
I <sub>F(AV)</sub>	Rectangular waveform	1	Α	
V <sub>RRM</sub>		100	V	
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	780	Α	
V <sub>F</sub>	1.0 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.59	V	
TJ	Range	-55 to +175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ100HM3	UNITS	
Maximum DC reverse voltage	$V_{R}$	100	V	
Maximum working peak reverse voltage	$V_{RWM}$	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS		UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 143 °C	, rectangular waveform	1.0	А
Maximum peak one cycle	_	5 μs sine or 3 μs rect. pulse	Following any rated	780	_
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	38	Α
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 ^{\circ}\text{C},  I_{AS} = 0.5 \text{A},  L = 8 \text{mH}$ 1.0		mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical 0.5		Α	



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V (1)	1 A	- T <sub>J</sub> = 25 °C	0.75	V
Maximum forward voltage drop		2 A		0.82	
See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	1 A	T <sub>J</sub> = 125 °C	0.59	
		2 A		0.65	
Maximum reverse leakage current	1	T <sub>J</sub> = 25 °C	$V_{\rm R}$ = Rated $V_{\rm R}$	0.5	mA
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	VR = nateu VR	1	IIIA
Typical junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		65	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of charge	dV/dt	Rated V <sub>R</sub> 10 000 V/μs		V/µs	

#### Note

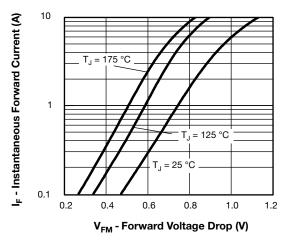
 $<sup>^{(1)}</sup>$  Pulse width = 300  $\mu$ s, duty cycle = 2 %

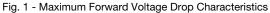
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +175	°C
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> (2)	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>		80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	OZ.
Marking device		Case style SMB (DO-214AA)	1	J

#### Notes

- $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink
- (2) Mounted 1" square PCB







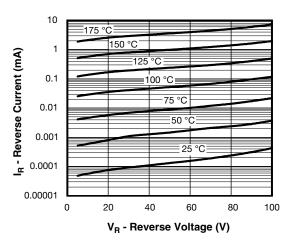


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

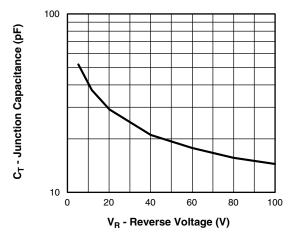


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

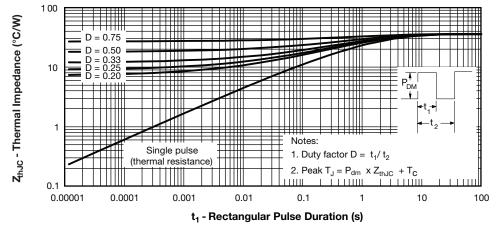
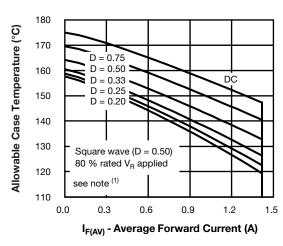


Fig. 4 - Maximum Thermal Impedance  $Z_{\text{thJC}}$  Characteristics (Per Leg)

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Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

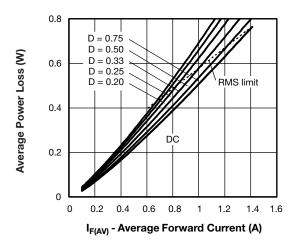


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

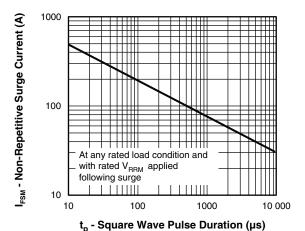


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

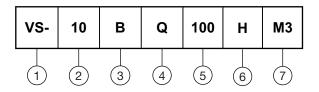
 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>



# Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

### **Device code**



- 1 Vishay Semiconductors product
- 2 Current rating
- 3 B = SMB (DO-214AA)
- 4 Q = Schottky "Q" series
- Voltage rating (100 = 100 V)
- 6 H = AEC-Q101 qualified
- 7 Environmental digit:

M3 = halogen-free, RoHS compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-10BQ100HM3/5BT	5BT	3200	13" diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS			
Dimensions <u>www.vishay.com/doc?95401</u>			
Part marking information	www.vishay.com/doc?95403		
Packaging information <u>www.vishay.com/doc?95404</u>			
SPICE model	www.vishay.com/doc?96603		



# Vishay Semiconductors

## **SMB**

### **DIMENSIONS** in inches (millimeters)

### DO-214AA (SMB)



### **Mounting Pad Layout**





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