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Vishay Semiconductors

High Speed Infrared Emitting Diodes, 890 nm, Surface Emitter Technology



DESCRIPTION

As part of the <u>SurfLightTM</u> portfolio, the VSMY5890 is an infrared, 890 nm emitting diode based on GaAlAs surface emitter chip technology with high radiant intensity, high optical power and high speed, in a low profile 0805 surface mount (SMD) package.

FEATURES

• Package type: surface-mount

• Package form: 0805

• Dimensions (L x W x H in mm): 2 x 1.25 x 0.8

Peak wavelength: λ_p = 890 nm

· High speed

• Angle of half intensity: $\varphi = \pm 60^{\circ}$

• 0805 standard surface-mountable package

 Floor life: 168 h, MSL 3, according to J-STD-020

• Lead (Pb)-free reflow soldering

 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



- Miniature light barrier
- · Optical switch
- IR point source

PRODUCT SUMMARY				
COMPONENT	I_e (mW/sr) at I_F = 100 mA	φ (°)	$\lambda_{\mathbf{p}}$ (nm)	t _r (ns)
VSMY5890	13	± 60	890	7

Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VSMY5890	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805	

Note

MOQ: minimum order quantity



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V _R	5	V	
Forward current		I _F	100	mA	
Peak forward current	$t_p/T = 0.1$, $t_p = 100 \mu s$	I _{FM}	200	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	500	mA	
Power dissipation		P _V	210	mW	
Junction temperature		T _j	125	°C	
Operating temperature range		T _{amb}	-40 to +110	°C	
Storage temperature range		T _{stg}	-40 to +110	°C	
Soldering temperature	According to Fig. 7, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction-to-ambient	EIA / JESD51	R _{thJA}	280	K/W	

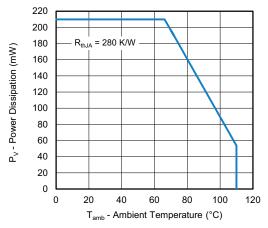


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

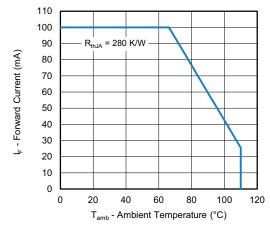


Fig. 2 - Forward Current Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	-	1.8	2.1	V
Temperature coefficient of V _F	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	TK _{VF}	-	-1.6	-	mV/K
Reverse current		I _R	Not designed for reverse operation			μΑ
Junction capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz,}$ $E = 0 \text{ mW/cm}^2$	CJ	-	30	-	pF
Radiant intensity	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	l _e	10	13	18	mW/sr
Temperature coefficient of radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	TΚφ _e	-	-0.2	-	%/K
Angle of half intensity		φ	-	± 60	-	0
Peak wavelength	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	λ_{p}	-	890	-	nm
Spectral bandwidth	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	Δλ	-	40	-	nm
Temperature coefficient of λ _p	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	TK_{\lambdap}	-	0.25	-	nm/K
Rise time	I _F = 100 mA, 10 % to 90 %	t _r	-	8	-	ns
Fall time	I _F = 100 mA, 10 % to 90 %	t _f	-	8	-	ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

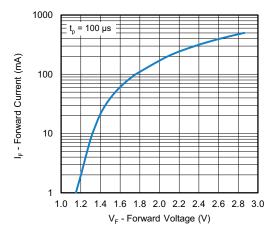
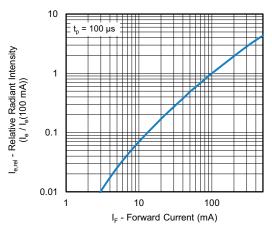


Fig. 3 - Forward Current vs. Forward Voltage



REFLOW SOLDER PROFILE

Fig. 4 - Relative Radiant Intensity vs. Forward Current

300 Max. 260 °C 255 250 240 200 remperature (°C) Max. 30 s 150 Max. 120 s Max. 100 s 100 Max. ramp down 6 °C/s 50 Max. ramp up 3 °C/s 0 50 100 200 250 300 150 19841 Time (s)

Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

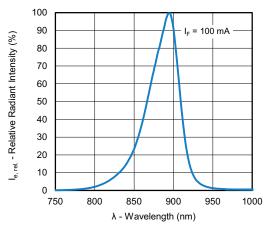


Fig. 5 - Relative Radiant Power vs. Wavelength

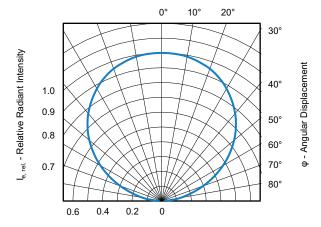


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

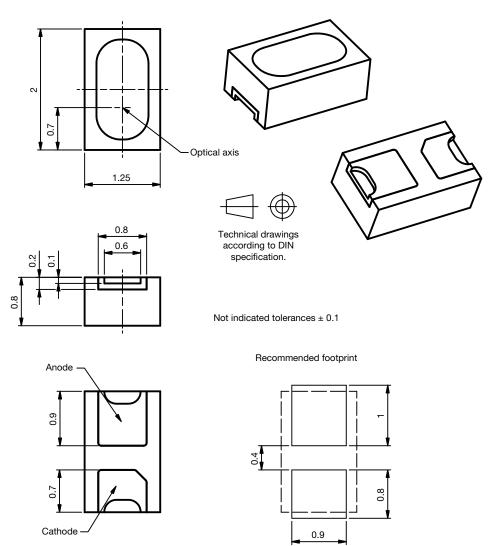
Floor life: 168 h

Conditions: T_{amb} < 30 °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-033D or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

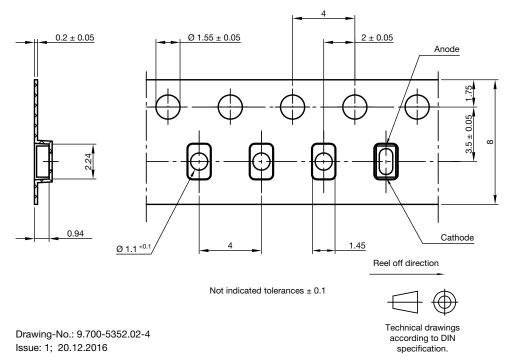
PACKAGE DIMENSIONS in millimeters



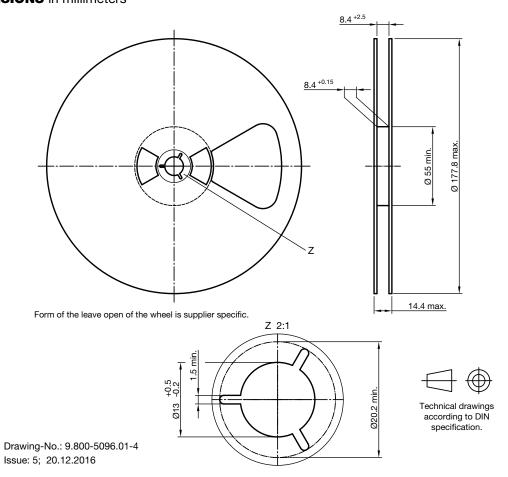
Drawing- No.: 6.550-5352.01-4

Issue: 1; 20.12.2016

BLISTER TAPE DIMENSIONS in millimeters



REEL DIMENSIONS in millimeters





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