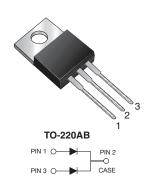


Vishay General Semiconductor

Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.43 \text{ V}$ at $I_F = 5 \text{ A}$



| PRIMARY CHARACTERISTICS | | | |
|--|----------------|--|--|
| I _{F(AV)} | 2 x 15 A | | |
| V_{RRM} | 100 V | | |
| I _{FSM} | 160 A | | |
| V _F at I _F = 15 A (125 °C) | 0.61 V | | |
| T _J max. | 150 °C | | |
| Package | TO-220AB | | |
| Circuit configuration | Common cathode | | |

FEATURES

Trench MOS Schottky technology

• Low forward voltage drop, low power losses

SSES ROHS COMPLIAN

• High efficiency operation

 Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106

HALOGEN , **FREE**

 Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

| MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted) | | | | | |
|--|------------|-------------------------------|-------------|------|--|
| PARAMETER | | SYMBOL | V31103C | UNIT | |
| Maximum repetitive peak reverse voltage | | V_{RRM} | 100 | V | |
| Maximum average forward rectified current (fig. 1) | per device | I _{F(AV)} | 30 | А | |
| | per diode | | 15 | | |
| Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode | | I _{FSM} | 160 | Α | |
| Operating junction temperature range | | T _J ⁽¹⁾ | -40 to +150 | °C | |
| Storage temperature range | | T _{STG} | -55 to +150 | | |

Note

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction to ambient: dP_D/dT_J <1/ R_{8,IA}



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| ELECTRICAL CHARACTERISTICS (T _J = 25 °C unless otherwise noted) | | | | | | |
|---|------------------------|-------------------------|-------------------------------|-------|------|------|
| PARAMETER | TEST CONDITIONS | | SYMBOL | TYP. | MAX. | UNIT |
| Instantaneous forward voltage per diode | I _F = 5 A | T _J = 25 °C | V _F ⁽¹⁾ | 0.50 | - | V |
| | I _F = 7.5 A | | | 0.55 | - | |
| | I _F = 15 A | | | 0.68 | 0.73 | |
| | I _F = 5 A | T _J = 125 °C | | 0.43 | - | |
| | I _F = 7.5 A | | | 0.49 | - | |
| | I _F = 15 A | | | 0.61 | 0.66 | |
| Reverse current at rated V _R per diode | V _R = 70 V | T _J = 25 °C | I _R ⁽²⁾ | 0.012 | - | A |
| | | T _J = 125 °C | | 6.5 | - | |
| | V _R = 100 V | T _J = 25 °C | | - | 0.9 | mA |
| | | T _J = 125 °C | | 16.0 | 35 | |
| Typical junction capacitance | 4 V, 1MHz | - | CJ | 1600 | - | pF |

Notes

 $^{(1)}$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

| THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted) | | | | |
|---|----------------------|---------|------|--|
| PARAMETER | SYMBOL | V31103C | UNIT | |
| Typical thermal resistance per device | R _{0JC} (1) | 1.0 | °C/W | |

Note

(1) Thermal resistance junction-to-case to follow JEDEC® 51-14 transient dual interface test method (TDIM)

| ORDERING INFORMATION (Example) | | | | | |
|--------------------------------|-----------------|--------------|---------------|---------------|--|
| PREFERRED P/N | UNIT WEIGHT (g) | PACKAGE CODE | BASE QUANTITY | DELIVERY MODE | |
| V31103C-M3/P | 1.88 | Р | 50/tube | Tube | |

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

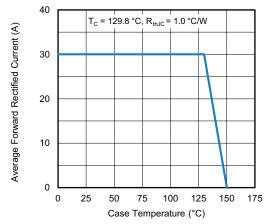


Fig. 1 - Forward Current Derating Curve

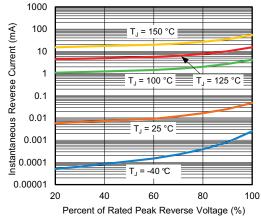


Fig. 4 - Typical Reverse Characteristics Per Diode

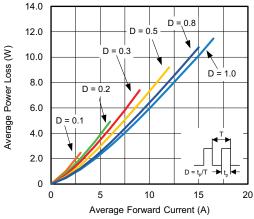


Fig. 2 - Forward Power Loss Characteristics Per Diode

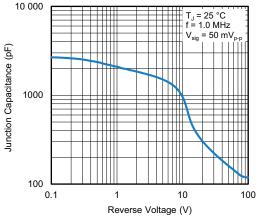


Fig. 5 - Typical Junction Capacitance

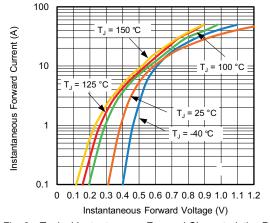


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

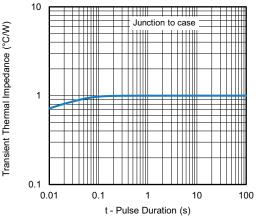
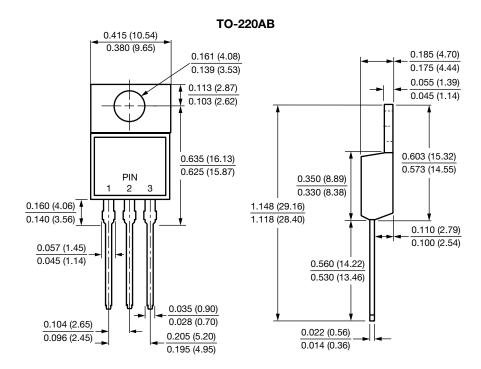


Fig. 6 - Typical Transient Thermal Impedance Per Device



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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