

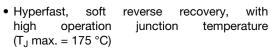
Insulated Hyperfast Rectifier Module, 280 A



PRIMARY CHARACTERISTICS						
V _R	300 V					
I _{F(AV)} per module at T _C = 81 °C	280 A					
t _{rr}	58 ns					
Туре	Modules - diode FRED Pt®					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

FEATURES

- Two fully independent diodes
- · Fully insulated package





RoHS COMPLIANT

- Low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- Easy to use and parallel
- · Industry standard outline
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-UFH280FA30 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V_R		300	V		
Continuous forward current per diode	I _F	T _C = 95 °C	160	۸		
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	1539	A		
Maximum power dissipation per module	P _D	T _C = 95 °C	410	W		
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

ELECTRICAL SPECIFICATIONS PER DIODE (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 200 μA	300	-	-	
Forward voltage	V _{FM}	I _F = 100 A	-	1.07	1.27	V
		I _F = 100 A, T _J = 175 °C	-	0.82	-	
Deverage legisers assured	I _{RM}	V _R = V _R rated	-	0.5	100	μA
Reverse leakage current		T _J = 175 °C, V _R = V _R rated	-	0.74	-	mA
Junction capacitance	C _T	V _R = 300 V	-	216	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t _{rr}	+	T _J = 25 °C	I _F = 50 A dI _F /dt = 200 A/μs V _R = 200 V	-	58	-	ns
	L _{rr}	T _J = 125 °C		-	85	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		=	4.5	-	- A - nC
		T _J = 125 °C		-	10	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	130	-	
		T _J = 125 °C		-	429	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction-to-case, single leg conducting	В		-	-	0.39	
Junction-to-case, both leg conducting	R_{thJC}		-	-	0.195	°C/W
Case-to-heatsink	R _{thCS}	Flat, greased surface	-	0.1	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				S	OT-227	

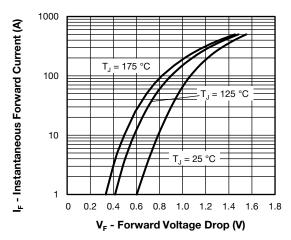


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Diode)

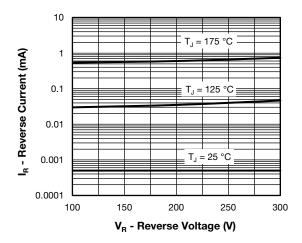


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Diode)

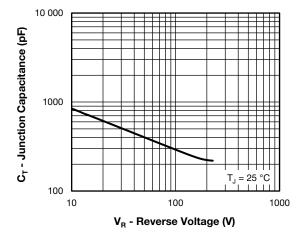


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Diode)

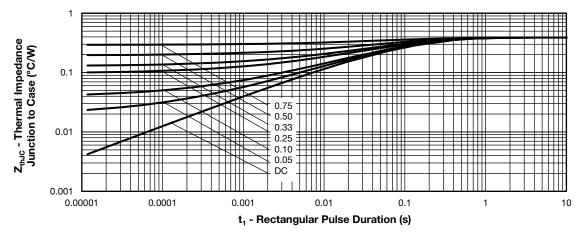


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Diode)

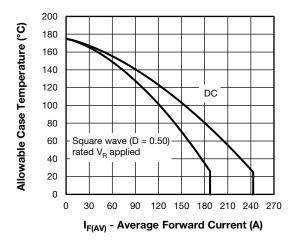


Fig. 5 - Maximum Current Rating Capability (Per Diode)

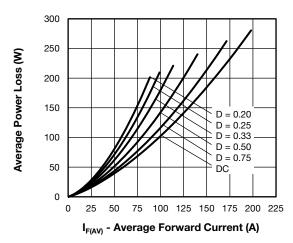


Fig. 6 - Forward Power Loss Characteristics (Per Diode)

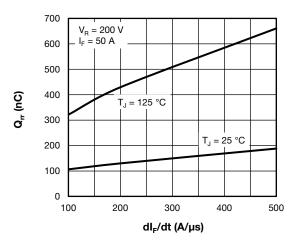


Fig. 7 - Typical Reverse Recovery Charge vs. dl_F/dt (Per Diode)

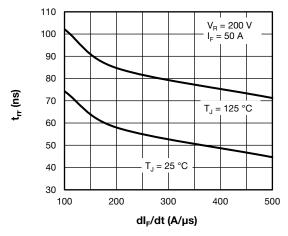


Fig. 8 - Typical Reverse Recovery Time vs. dl_F/dt (Per Diode)

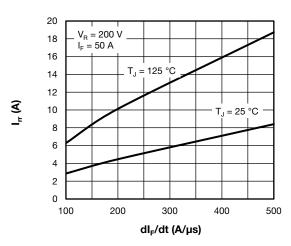


Fig. 9 - Typical Reverse Recovery Current vs. dI_F/dt (Per Diode)

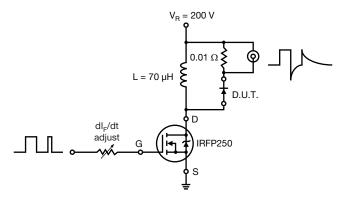
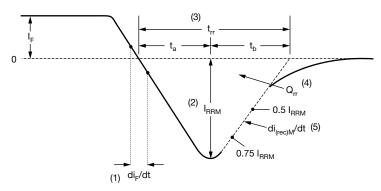


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

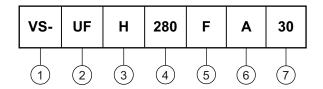
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Ultra fast rectifier

Hyper fast FRED Pt® diffused

4 - Current rating (280 = 280 A)

5 - Circuit configuration (two separate diodes, parallel pin-out)

Package indicator (SOT-227 standard insulated base)

7 - Voltage rating (30 = 300 V)

CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment 4 0 0 3 4 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0			

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95423					
Packaging information	www.vishay.com/doc?95425					

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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