

COMPLIANT

High Performance Schottky Rectifier, 300 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	300 A			
V_{R}	45 V			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

FEATURES

- 150 °C T_J operation
- · Center tap module
- · Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-300CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	300	Α			
V_{RRM}		45	V			
I _{FSM}	t _p = 5 μs sine	27 000	А			
V _F	150 A _{pk} , T _J = 125 °C (per leg)	0.56	V			
T _J	Range	-55 to +150	°C			

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-300CNQ045PbF	UNITS
Maximum DC reverse voltage	V _R	45	V
Maximum working peak reverse voltage	V_{RWM}	70	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average	per leg		50 % duty cycle at T _C = 111 °C, rectangular waveform			150	
forward current See fig. 5	per device	I _{F(AV)}			300	A	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	27 000		
			10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	2400		
Non-repetitive avalanche energy per leg E_{AS} $T_{J} = 25 ^{\circ}C$, $I_{AS} = 18 A$, $L = 1 \text{mH}$		150	mJ				
Repetitive avalanche cur	Repetitive avalanche current per leg I_{AR} Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical			30	А		



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	150 A	T 05 °C	0.61	. V
Maximum forward voltage drop per leg		300 A	T _J = 25 °C	0.77	
See fig. 1		150 A	T _{.1} = 125 °C	0.56	
		300 A	1J = 125 C	0.75	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	15	- mA
See fig. 2		T _J = 125 °C	VR = nateu VR	1100	
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		7750	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		6.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}	- 55	-	150	°C
Thermal resistance,	per leg	В	-	-	0.28	°C/W
junction to case	per module	R _{thJC}	-	-	0.14	
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	
			-	68	-	g
Weight			-	2.4	-	OZ.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf · in (N · m)
Terminal torque			30 (3.4)	-	44.2 (5)	()
Vertical pull			-	-	80	II-£ :
2" lever pull			-	-	35	lbf · in

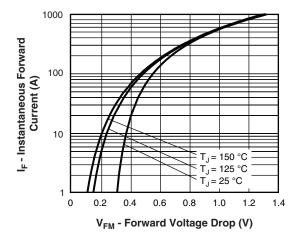


Fig. 1 - Maximum Forward Voltage Drop Characteristics

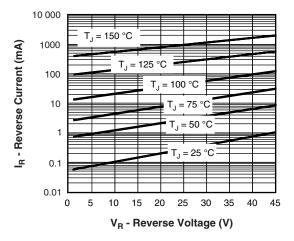


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

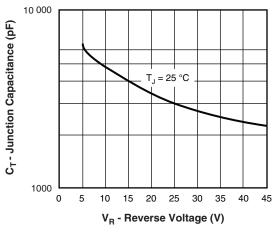


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

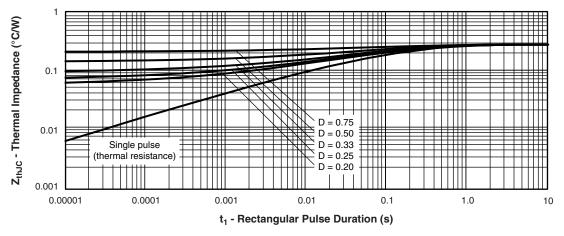


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

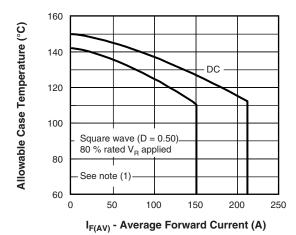


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

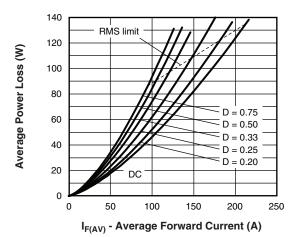
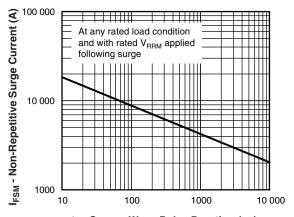


Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

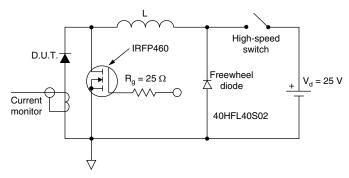


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

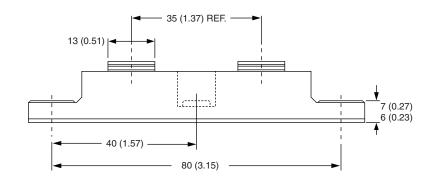
- 1 Vishay Semiconductors product
- Average current rating (x 10)
- Product silicon identification
- C = circuit configuration
- N = not isolated
- Q = Schottky rectifier diode
- 7 Voltage rating (045 = 45 V)
- 8 Lead (Pb)-free

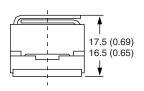
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

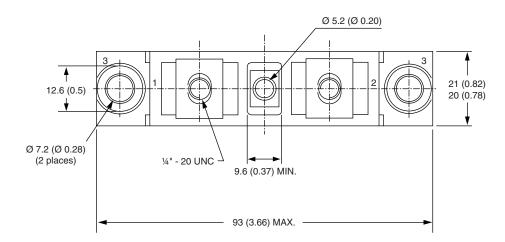


TO-244

DIMENSIONS in millimeters (inches)









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