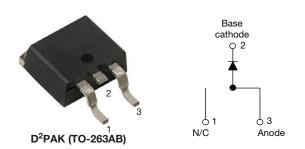


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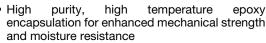
High Performance Schottky Rectifier, 18 A



PRIMARY CHARACTERISTICS							
I _{F(AV)} 18 A							
V_{R}	35 V, 40 V, 45 V						
V _F at I _F	0.53 V						
I _{RM}	25 mA at 125 °C						
T _J max.	175 °C						
E _{AS}	24 mJ						
Package	D ² PAK (TO-263AB)						
Circuit configuration	Single						

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- AEC-Q101 qualified
- Meets JESD 201 class 1 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-18TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES UNI									
I _{F(AV)}	Rectangular waveform	18	Α						
V _{RRM}	Range	35 to 45	V						
I _{FSM}	$t_p = 5 \mu s sine$	1800	Α						
V _F	18 A _{pk} , T _J = 125 °C	0.53	V						
T _J	Range	-55 to 175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-18TQ035SHM3	VS-18TQ040SHM3	VS-18TQ045SHM3	UNITS			
Maximum DC reverse voltage	V_{R}	35	40	45	V			
Maximum working peak reverse voltage	V_{RWM}	33	40	45	V			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 149 °C	18	Α				
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	1800				
non-repetitive surge current See fig. 7		10 ms sine or 6 ms rect. pulse	390	Α				
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 3.6 \text{A}, L = 3.7 \text{mH}$		24	mJ			
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3.6	А			



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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
Maximum forward voltage drop See fig. 1		18 A	T _{.1} = 25 °C	0.60			
	V _{FM} ⁽¹⁾	36 A	11 = 23 0	0.72	V		
	V FM (*)	18 A	T _{.1} = 125 °C	0.53			
		36 A	- IJ = 123 C	0.67			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V_{R} = Rated V_{R}	2.5	- mA		
See fig. 2		T _J = 125 °C	VR = nateu VR	25			
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		1400	pF		
Typical series inductance	L _S	Measured lead to lead 5	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs			

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum junction and storage temperature ran	ge	T _J , T _{Stg}		-55 to 175	°C		
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	1.50	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	G/VV		
Annewimete weight				2	g		
Approximate weight				0.07	oz.		
May enting toward	minimum			6 (5)	kgf · cm		
Mounting torque maximum				12 (10)	(lbf · in)		
				18TQ0)35SH		
Marking device			Case style D ² PAK (TO-263AB)	18TQ0)40SH		
)45SH		

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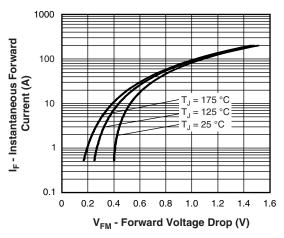


Fig. 1 - Maximum Forward Voltage Drop Characteristics

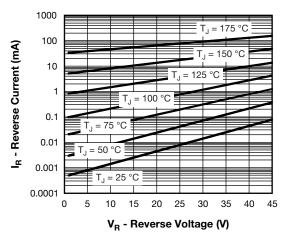


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

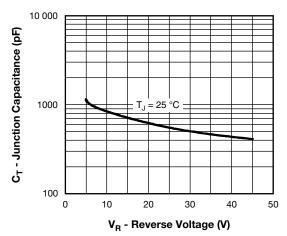


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

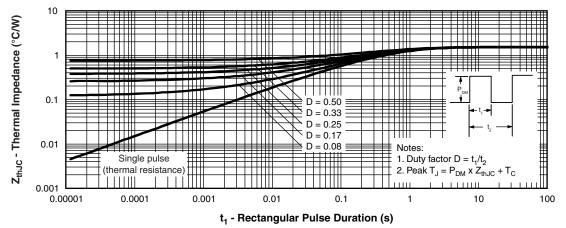


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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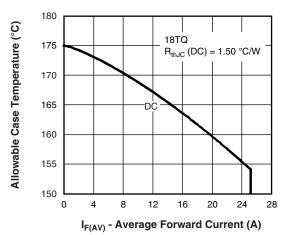


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

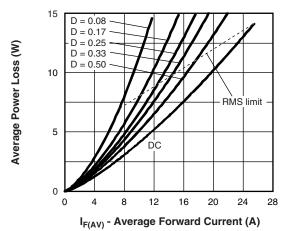


Fig. 6 - Forward Power Loss Characteristics

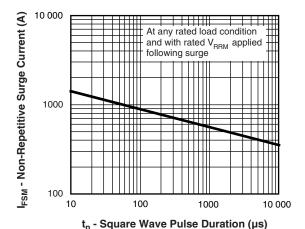


Fig. 7 - Maximum Non-Repetitive Surge Current

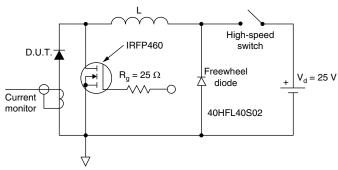
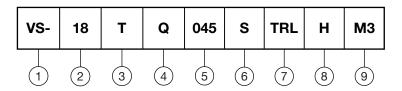


Fig. 8 - Unclamped Inductive Test Circuit

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (18 A)

Circuit configuration: T = TO-220

- Schottky "Q" series 035 = 35 V

- Voltage ratings — 040 = 40 V 045 = 45 V

- $S = D^2PAK (TO-263AB)$

- S = D-PAK (10-263AE - None = tube

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

8 - H = AEC-Q101 qualified

9 - M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-18TQ035SHM3	50	1000	Antistatic plastic tubes						
VS-18TQ035STRRHM3	800	800	13" diameter reel						
VS-18TQ035STRLHM3	800	800	13" diameter reel						
VS-18TQ040SHM3	50	1000	Antistatic plastic tubes						
VS-18TQ040STRRHM3	800	800	13" diameter reel						
VS-18TQ040STRLHM3	800	800	13" diameter reel						
VS-18TQ045SHM3	50	1000	Antistatic plastic tubes						
VS-18TQ045STRRHM3	800	800	13" diameter reel						
VS-18TQ045STRLHM3	800	800	13" diameter reel						

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95046</u>						
Part marking information	www.vishay.com/doc?95444					
Packaging information	www.vishay.com/doc?95032					
SPICE model	www.vishay.com/doc?96209					



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



CVMBOL	SYMBOL MILLIMETERS		MILLIMETERS		MILLIMETERS INCHE		NOTES	NOTES	NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES				
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3				
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3				
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3				
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC					
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625					
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110					
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3				
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070					
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC					
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208					

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



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