

# Ultrafast Rectifier, 2 A FRED Pt®


**SMB (DO-214AA)**


## FEATURES

- Ultrafast recovery time, reduced  $Q_{rr}$  and soft recovery
- 175 °C maximum operating junction temperature
- Specific for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 A
$V_R$	200 V
$V_F$ at $I_F$	0.66 V
$t_{rr}$ typ.	24 ns
$T_J$ max.	175 °C
Package	SMB (DO-214AA)
Circuit configuration	Single

## DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop, ultrafast recovery time, and fast recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in snubber, output operation, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element.

## MECHANICAL DATA

**Case:** SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

**Terminals:** matte tin plated leads, solderable per J-STD-002

**Polarity:** color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		200	V
Average rectified forward current	$I_{F(AV)}$	$T_L = 150\text{ °C}^{(1)}$	2	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$ , 6 ms square pulse	70	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-65 to +175	°C

### Note

<sup>(1)</sup> Mounted on PCB with 6 mm x 3.5 mm lands

ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	200	-	-	V
Forward voltage	$V_F$	$I_F = 2\text{ A}$	-	0.84	0.9	
		$I_F = 2\text{ A}$ , $T_J = 150\text{ °C}$	-	0.66	0.7	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	2	$\mu\text{A}$
		$T_J = 150\text{ °C}$ , $V_R = V_R$ rated	-	-	20	
Junction capacitance	$C_T$	$V_R = 200\text{ V}$	-	12	-	pF
Critical rate of rise of reverse voltage	$dV/dt_r$		-	-	10 000	V/ $\mu\text{s}$

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1.0\text{ A}$ , $dI_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	24	-	ns
		$I_F = 1.0\text{ A}$ , $dI_F/dt = 50\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	27	-	
		$I_F = 0.5\text{ A}$ , $I_R = 1\text{ A}$ , $I_{rr} = 0.25\text{ A}$	-	-	23	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	21	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	26	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	2.7	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	3.4	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	28	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	43	-	

<b>THERMAL - MECHANICAL SPECIFICATIONS</b>						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to mount	$R_{thJM}^{(1)}$		-	-	17	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction to ambient	$R_{thJA}^{(1)}$		-	-	80	
Approximate Weight			0.1			g
			0.003			oz.
Marking device		Case style SMB (DO-214AA)	2H2			

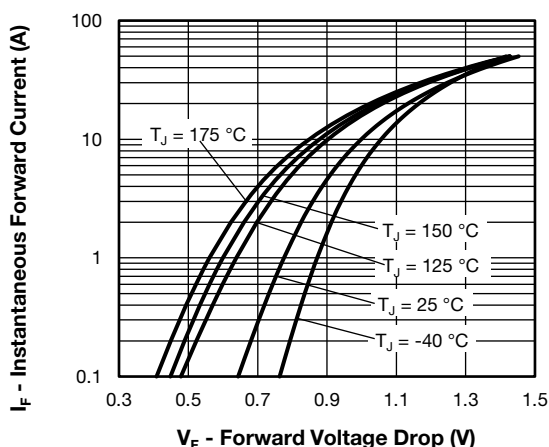
**Note**
<sup>(1)</sup> Units mounted on PCB 6 mm x 3.5 mm land areas


Fig. 1 - Typical Forward Voltage Drop Characteristics

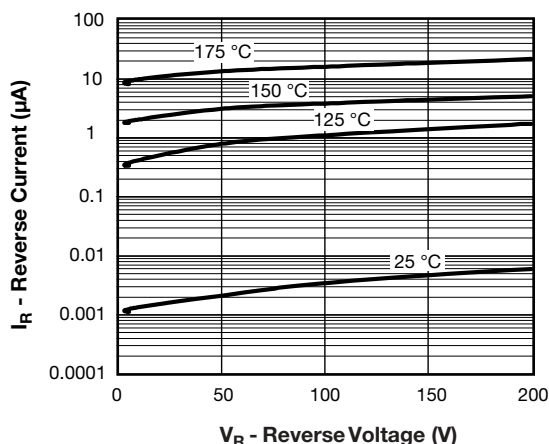


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

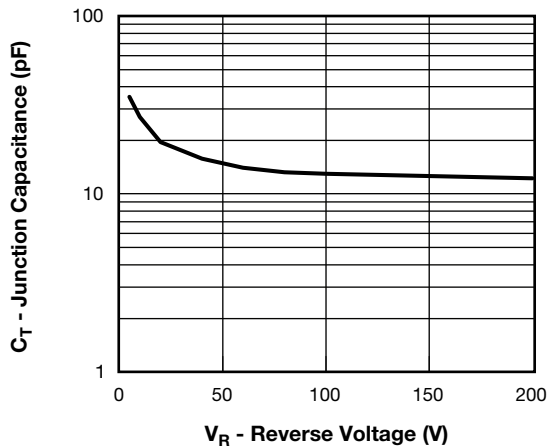


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

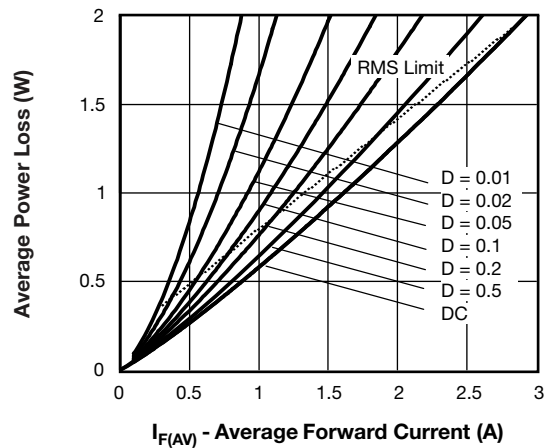


Fig. 5 - Forward Power Loss Characteristics

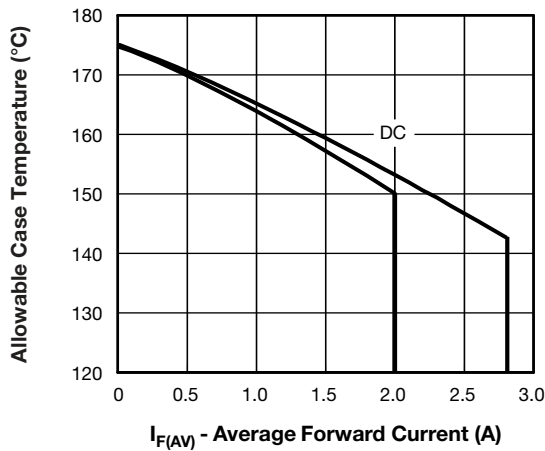
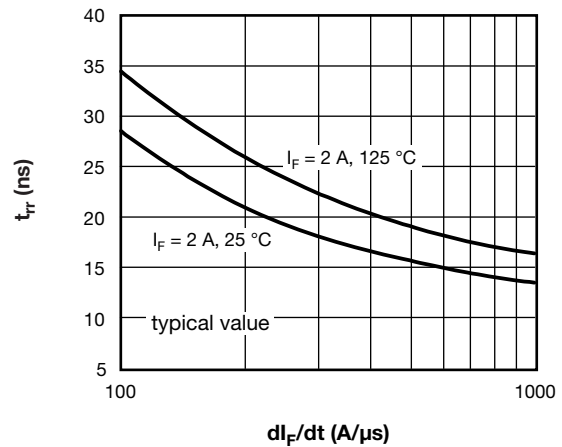
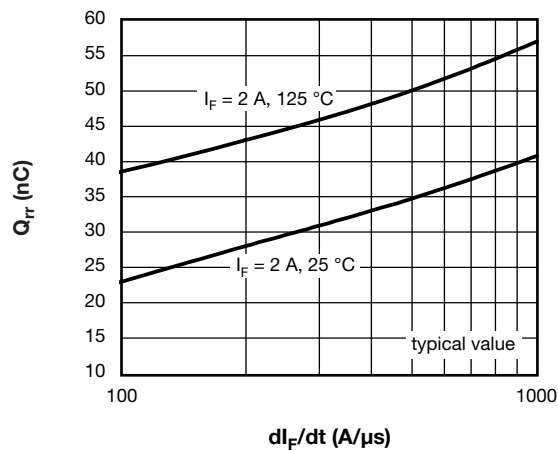


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current


Fig. 6 - Typical Reverse Recovery vs.  $dI_F/dt$ 

Fig. 7 - Typical Stored Charge vs.  $dI_F/dt$

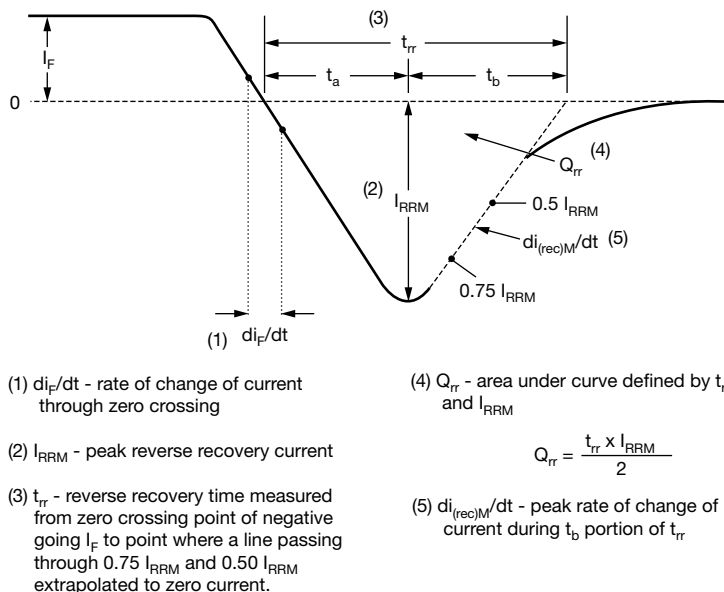


Fig. 8 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION (Example)**

PREFERRED P/N	PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-2EGH02HM3_A/I	I	3200	13" diameter plastic tape and reel

**LINKS TO RELATED DOCUMENTS**

Dimensions	<a href="http://www.vishay.com/doc?95401">www.vishay.com/doc?95401</a>
Part marking information	<a href="http://www.vishay.com/doc?95472">www.vishay.com/doc?95472</a>
Packaging information	<a href="http://www.vishay.com/doc?95404">www.vishay.com/doc?95404</a>
SPICE model	<a href="http://www.vishay.com/doc?96021">www.vishay.com/doc?96021</a>

## SMB

**DIMENSIONS** in inches (millimeters)





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