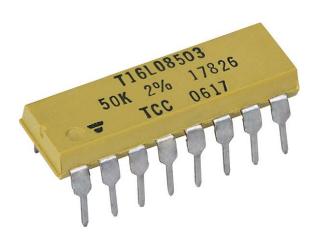


# Thick Film Resistor Networks, Dual-In-Line, Molded DIP



### **FEATURES**

 8 bit, R/2R ladder networks for D/A and A/D converter with bi-polar or CMOS switches



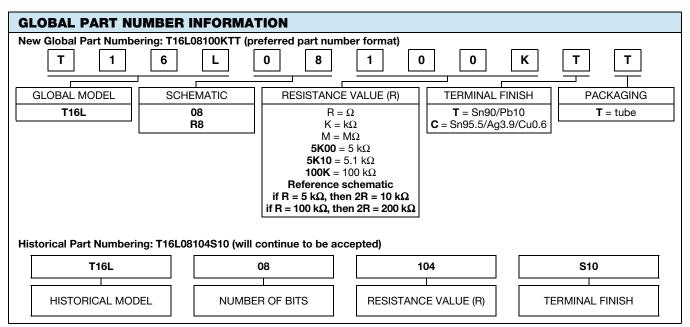
- 0.190" (4.83 mm) maximum seated height
- Rugged, molded case construction
- · Thick film resistive elements
- Low temperature coefficient (-55 °C to 125 °C) ± 100 ppm/°C
- · Reduces total assembly costs
- · Compatible with automatic inserting equipment
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details.

STANDARD ELECTRICAL SPECIFICATIONS							
GLOBAL MODEL	SCHEMATIC	POWER RATING ELEMENT P <sub>70 °C</sub> W	POWER RATING PACKAGE P <sub>70 °C</sub> W	RESISTANCE RANGE <sup>(1)</sup> Ω	TOLERANCE ± %	TEMPERATURE COEFFICIENT (0 °C to 70 °C) ± ppm/°C	LINEARITY (0 °C to 70 °C)
T16L	08 R8	0.050	1.8	50 to 1M	2	100	± 0.5 LSB

(1) 25K, 50K, and 100K are standard, other values available on special order



(1) For additional information on packaging, refer to the "Through-Hole Network Packaging" document (www.vishay.com/doc?31542)

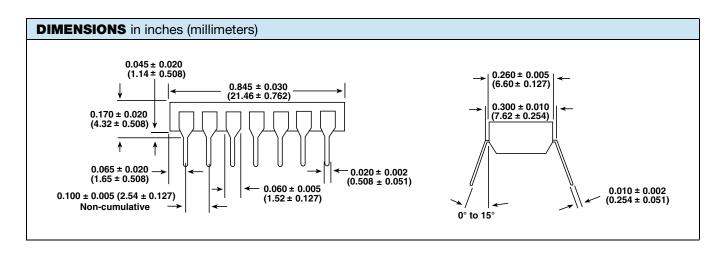


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### **SCHEMATICS** T16L08 **13** R6 = R R8 = R R10 = R R12 = R R14 = R < R16 = 2RR2 = R R4 = R $\le$ R1 = 2R $\le$ R3 = 2R $\le$ R5 = 2R $\le$ R7 = 2R $\le$ R9 = 2R $\le$ R11 = 2R $\le$ R13 = 2R $\le$ R15 = 2R T16LR8 10 R14 = R R16 = 2R R2 = R R4 = R R6 = R R8 = R R10 = R R12 = R

### **RATIO MATCH TOLERANCE**

R1/R2 = 2 % ± 1 % R1/R3 = 1 % ± 1 % R1/R4 = 2 % ± 1 % R1/R5 = 1 % ± 1 % R1/R6 = 2 % ± 1 % R1/R7 = 1 % ± 1 % R1/R8 = 2 % ± 1 % R9/R10 = 2 % ± 0.5 % R11/R12 = 2 % ± 0.4 % R15/R13 = 1 % ± 0.2 % R15/R14 = 2 % ± 0.2 %





# **Legal Disclaimer Notice**

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