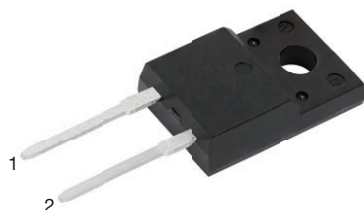
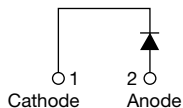


# Ultrafast Rectifier, 8 A FRED Pt®



TO-220 FullPAK 2L



## FEATURES

- State of the art low forward voltage drop
- Ultrafast recovery time
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package ( $V_{INS} = 2500 V_{RMS}$ )
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	8 A
$V_R$	600 V
$V_F$ at $I_F$	0.84 V
$t_{rr}$ (typ.)	65 ns
$T_J$ max.	175 °C
Package	TO-220 FullPAK 2L
Circuit configuration	Single

## DESCRIPTION

State of the art, ultralow  $V_F$ , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

## APPLICATIONS

AC-DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		600	V
Average rectified forward current in DC	$I_{F(AV)}$	$T_C = 134\text{ °C}$	8	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$	120	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu A$	600	-	-	V
Forward voltage	$V_F$	$I_F = 8\text{ A}$	-	0.97	1.07	
		$I_F = 8\text{ A}, T_J = 150\text{ °C}$	-	0.84	0.90	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	0.01	9	$\mu A$
		$T_J = 150\text{ °C}, V_R = V_R$ rated	-	5	50	
Junction capacitance	$C_T$	$V_R = 600\text{ V}$	-	6	-	pF
Series inductance	$L_S$	Measured lead to lead 5 mm from package body	-	8	-	nH

**DYNAMIC RECOVERY CHARACTERISTICS** ( $T_J = 25\text{ }^{\circ}\text{C}$  unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1\text{ A}$ , $di_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	65	100	ns
		$I_F = 8\text{ A}$ , $di_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	150	250	
		$T_J = 25\text{ }^{\circ}\text{C}$	-	180	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	240	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	15	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	19	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	1500	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	2400	-	

**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J$ , $T_{Stg}$		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction-to-case	$R_{thJC}$		-	4.6	5.5	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction-to-ambient	$R_{thJA}$	Typical socket mount	-	-	70	
Typical thermal resistance, case-to-heatsink	$R_{thCS}$	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2	-	g
			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	ETL0806FP			

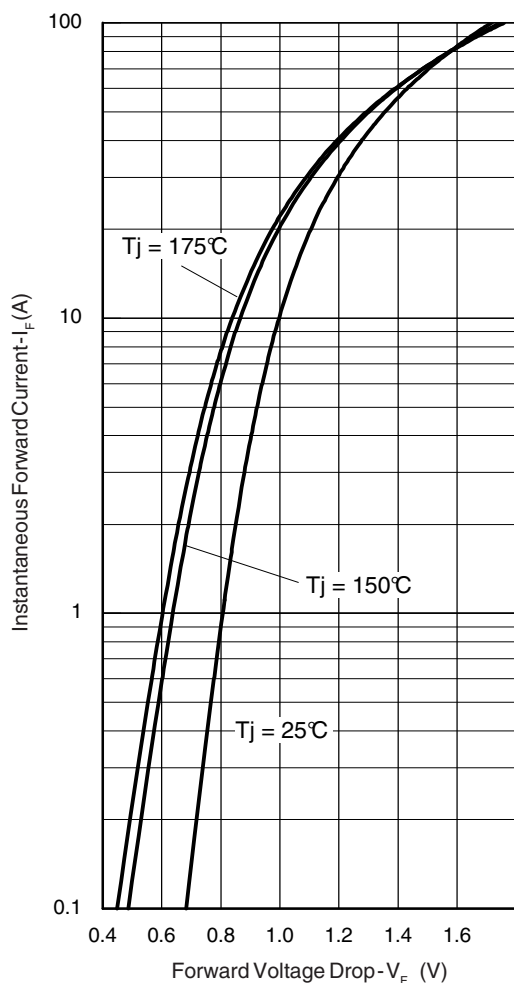


Fig. 1 - Typical Forward Voltage Drop Characteristics

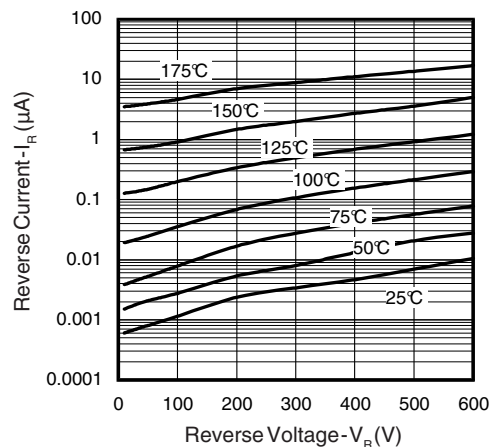


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

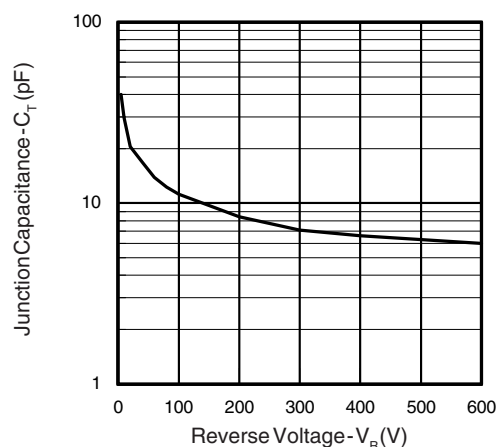
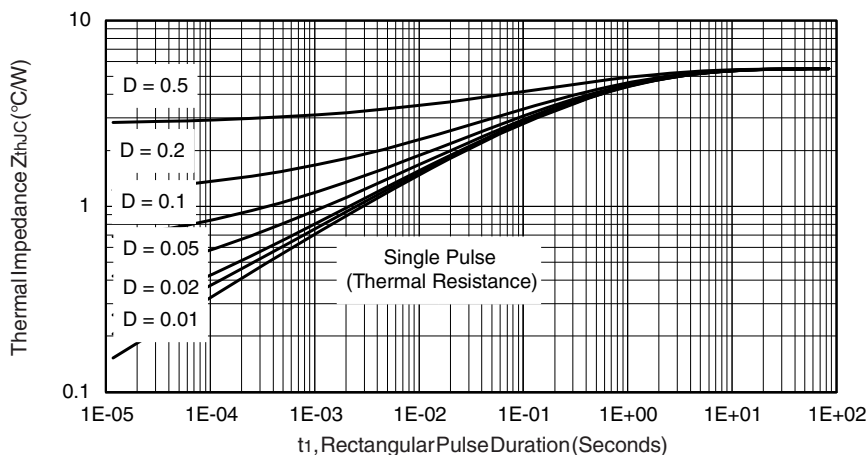


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

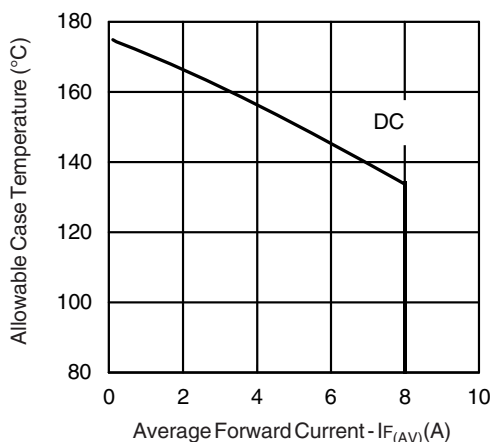


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

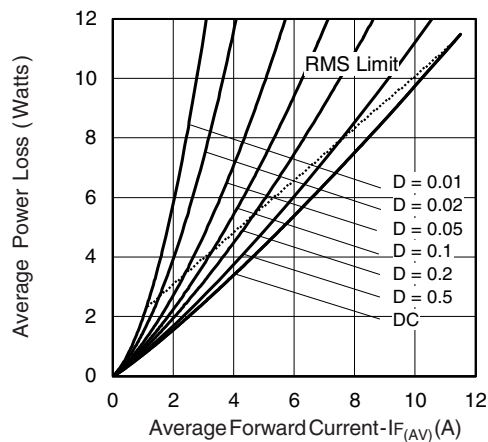


Fig. 6 - Forward Power Loss Characteristics

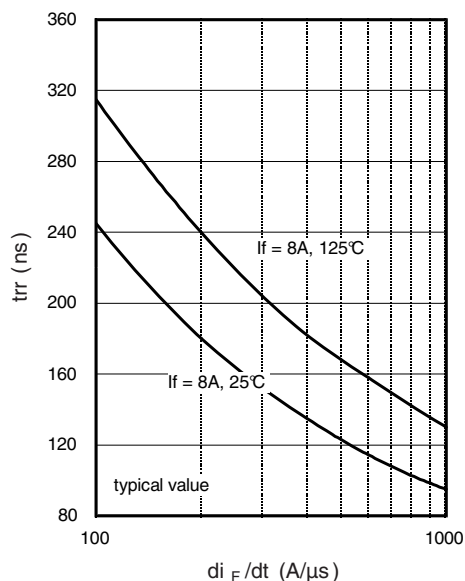


Fig. 7 - Typical Reverse Recovery vs.  $di_F/dt$

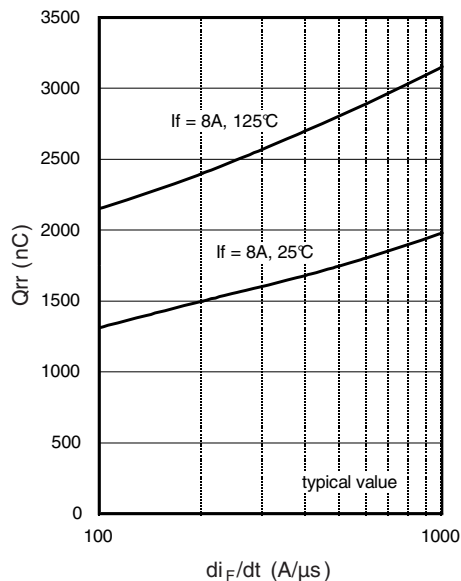


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$

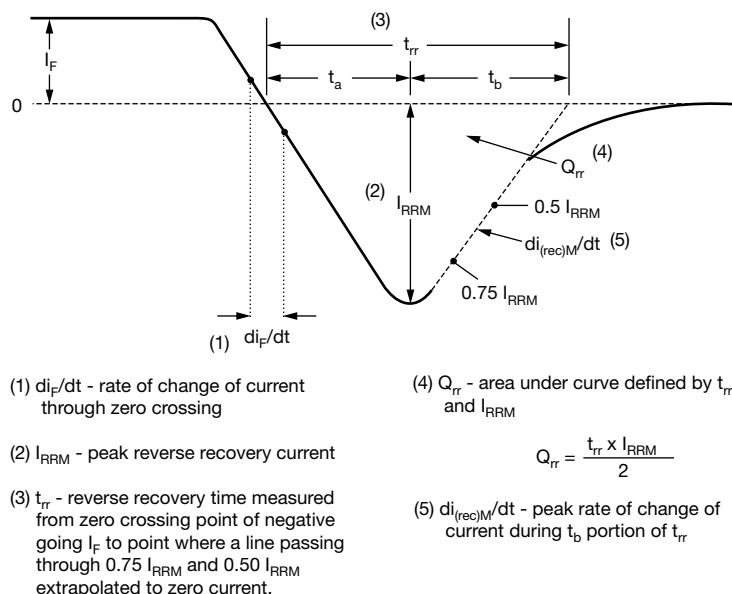


Fig. 9 - Reverse Recovery Waveform and Definitions

## ORDERING INFORMATION TABLE

Device code	VS-	E	T	L	08	06	FP	-M3
	1	2	3	4	5	6	7	8
1	Vishay Semiconductors product							
2	Circuit configuration:							
	E = single							
3	T = TO-220							
4	L = hyperfast recovery time							
5	Current code: 08 = 8 A							
6	Voltage code: 06 = 600 V							
7	FP = TO-220 FullPAK 2L							
8	Environmental digit:							
	-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free							

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-ETL0806FP-M3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?96157">www.vishay.com/doc?96157</a>
Part marking information	<a href="http://www.vishay.com/doc?95392">www.vishay.com/doc?95392</a>



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