

FRED Pt® Gen 4 Doubler Ultrafast Diode, 500 A (INT-A-PAK Power Modules)



PRIMARY CHARACTERISTICS					
V_R	600 V				
I _{F(AV)} at T _C	500 A at 55 °C				
t _{rr} at 25 °C	104 ns				
Туре	Modules - diode, FRED Pt®				
Package	INT-A-PAK				
Circuit configuration	Diode doubler circuit				

FEATURES

- Gen 4 FRED Pt® dices technology
- Ultrasoft reverse recovery characteristics
- Low I_{RRM} and reverse recovery charge
- · Very low forward voltage drop
- 175 °C operating junction temperature
- UL approved file E78996 for application with maximum case temperature up to 140 °C
- Large creepage distances
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

Gen 4 FRED Pt technology, state of the art, ultra low V_F , soft switching optimized for IGBT F/W diode.

The minimized conduction loss, optimized storage charge, and low recovery current, minimized the switching losses and reduce the over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V_R		600	V	
Ocalia a a face and a soci	I _F	T _C = 25 °C	772		
Continuous forward current		T _C = 90 °C	519	A	
Single pulse forward current	I _{FSM}	t_p = 10 ms, 50 Hz, sine half wave, initial T_J = 175 °C	4140	A	
Maximum power dissipation	Б	T _C = 25 °C	1363	W	
	P_{D}	T _C = 90 °C	772	- vv	
Operating junction temperature range	TJ		-40 to +175	°C	
Storage temperature range	T _{Stg}		-40 to +150		
RMS insulation voltage	V _{INS}	50 Hz, circuit to base, all terminals shorted, t = 1 s	3500	V	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 500 μA	600	-	-	
Forward voltage drop		I _F = 250 A	-	1.25	-	
	V	I _F = 500 A	-	1.45	1.66	V
	V_{FM}	I _F = 250 A, T _J = 150 °C	-	1.06	-	
		I _F = 500 A, T _J = 150 °C - 1.35	-			
Reverse leakage current	I	V _R = 600 V	-	10	-	μA
	I _{RM}	T _J = 150 °C, V _R = 600 V	-	2.5	-	V μΑ mA



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	+	T _J = 25 °C		-	104	-	no
Reverse recovery time t _{rr}	L _{rr}	T _J = 125 °C	I _F = 150 A dl/dt = 1000 A/μs V _R = 400 V	-	193	-	ns
Dook receivent ourrent		T _J = 25 °C		-	59	-	А
Peak recovery current	I _{rr}	T _J = 125 °C		-	122	-	
Reverse recovery charge	overse recovery charge	T _J = 25 °C		-	3.5	-	μC
Reverse recovery charge Q _{rr}	T _J = 125 °C		-	13.8	-] μΟ	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	ARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum therma	,	R _{thJC}	DC operation	0.11	K/W
Typical thermal resistance, case to heat sink		R _{thCS}	Mounting surface, flat, smooth, and greased	0.035	K/VV
Mounting	to heat sink		A mounting compound is recommended and the		Nm
torque ± 10 %	busbar		torque should be rechecked after a period of 3 hours to allow the spread of the compound	4 to 6	
Approximate weight				200	g
				7.1	OZ.
Case style				INT-A-PAK	

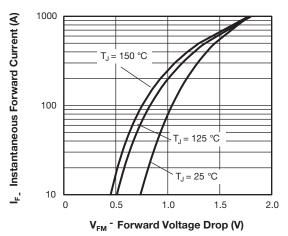


Fig. 1 - Typical Forward Voltage Drop Characteristics

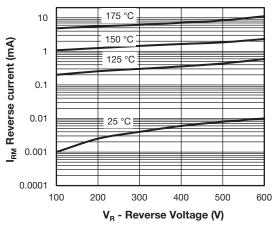


Fig. 2 - Typical Value of Reverse Current vs. Reverse Voltage

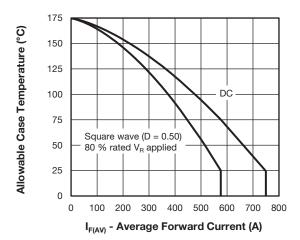


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current

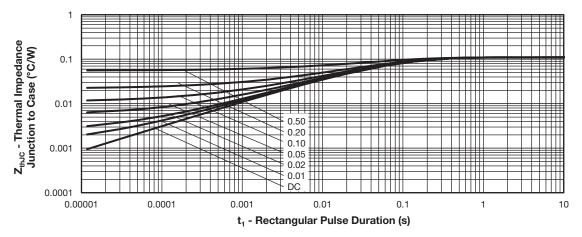


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

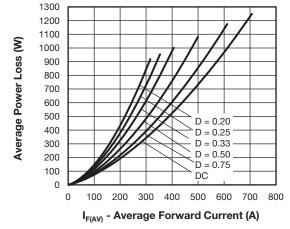


Fig. 5 - Forward Power Loss Characteristics

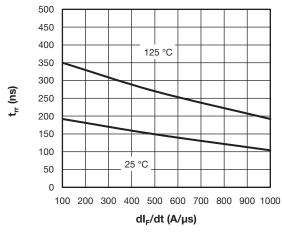


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt $I_{FM} = 150 \text{ A}, V_R = 300 \text{ V}$



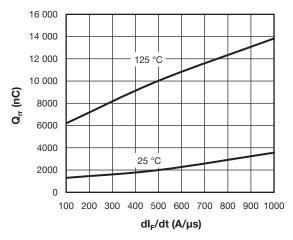


Fig. 7 - Typical Reverse Recovery Charge vs. dI_F/dt $I_{FM} = 150 \text{ A}, V_R = 300 \text{ V}$

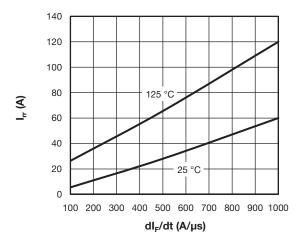
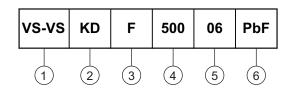


Fig. 8 - Typical Reverse Recovery Current vs. dI_F/dt $I_{FM} = 150 \text{ A}, V_R = 300 \text{ V}$

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Circuit configuration: KD = doubler circuit

F = FRED Pt® ultrafast diode

4 - Current rating (500 = 500 A)

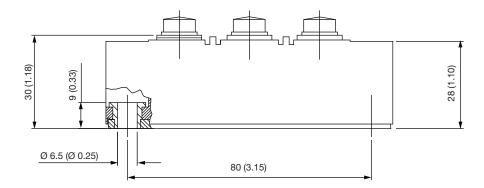
5 - Voltage rating (06 = 600 V)

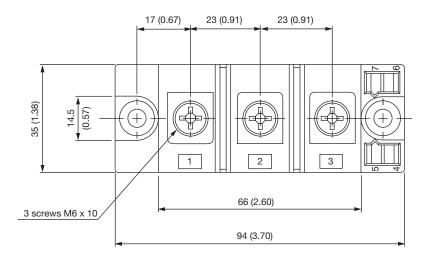
6 - PbF = lead (Pb)-free

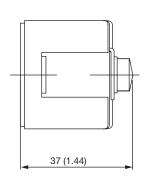
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Diode doubler circuit	KD	KD reversed polarity			



DIMENSIONS in millimeters (inches)



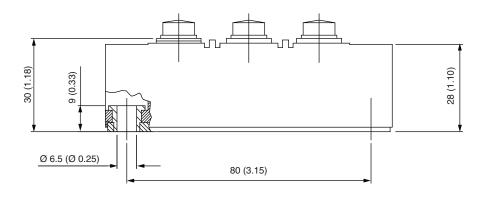


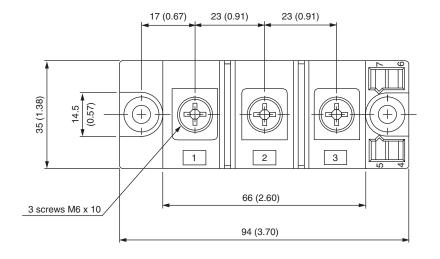


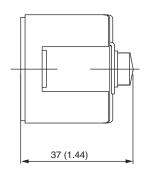


INT-A-PAK DBC

DIMENSIONS in millimeters (inches)









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