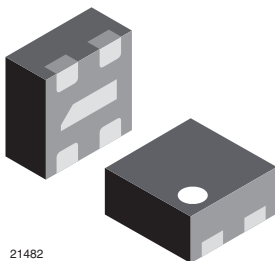
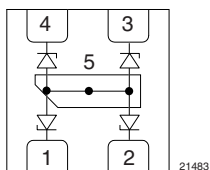


4-Line BUS-Port ESD Protection



FEATURES

- Ultra compact LLP1010-5L package
- Low package profile < 0.4 mm
- 4-line ESD protection
- Low leakage current
- Low load capacitance $C_D = 0.8$ pF
- ESD immunity acc. IEC 61000-4-2
± 15 kV contact discharge
± 15 kV air discharge
- Pin plating NiPdAu (e4) no whisker growth
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

MARKING (example only)



Dot = pin 1 marking

X = date code

Y = type code (see table below)

DESIGN SUPPORT TOOLS

[click logo to get started](#)

3D
Models
Available

ORDERING INFORMATION

DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY
VBUS054DD-HF4	VBUS054DD-HF4-GS08	5000	5000

PACKAGE DATA

DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VBUS054DD-HF4	LLP1010-5L	C	1.07 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C

ABSOLUTE MAXIMUM RATINGS VBUS054DD-HF4

PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT
Peak pulse current	Pin 1, 2, 3 or 4 to pin 5 acc. IEC 61000-4-5; $t_p = 8/20$ μ s; single shot	I_{PPM}	3	A
Peak pulse power	Pin 1, 2, 3 or 4 to pin 5 acc. IEC 61000-4-5; $t_p = 8/20$ μ s; single shot	P_{PP}	57	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V_{ESD}	± 15	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses		± 15	kV
Operating temperature	Junction temperature	T_J	-40 to +125	°C
Storage temperature		T_{STG}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS VBUS054DD-HF4 (Pin 1, 2, 3, or 4 to pin 5)						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N_{channel}	-	-	4	lines
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	-	5	V
Reverse voltage	at $I_{\text{R}} = 0.1 \mu\text{A}$	V_{R}	5	-	-	V
Reverse current	at $V_{\text{IN}} = V_{\text{RWM}} = 5 \text{ V}$	I_{R}	-	< 0.01	0.1	μA
Reverse breakdown voltage	at $I_{\text{R}} = 1 \text{ mA}$	V_{BR}	6.9	8	8.7	V
Reverse clamping voltage	at $I_{\text{PP}} = 3 \text{ A}$ acc. IEC 61000-4-5	V_{C}	-	16	19	V
Forward clamping voltage	at $I_{\text{F}} = 12 \text{ A}$ acc. IEC 61000-4-5	V_{F}	-	3.5	4.5	V
Capacitance	$V_{\text{IN}} = 0 \text{ V}$	C_{D}	-	0.8	1	pF
	$V_{\text{IN}} = 2.5 \text{ V}$	C_{D}	-	0.5	0.8	pF
Line symmetry	Difference of the line capacitances	dC_{D}	-	-	0.05	pF

Note

- $T_{\text{amb}} = 25 \text{ }^{\circ}\text{C}$, unless otherwise specified

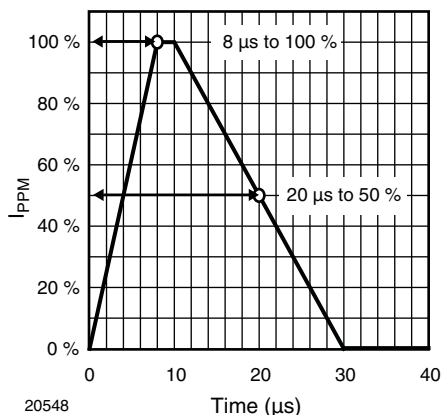
TYPICAL CHARACTERISTICS ($T_{\text{amb}} = 25 \text{ }^{\circ}\text{C}$, unless otherwise specified)


Fig. 1 - 8/20 μs Peak Pulse Current Wave Form
acc. IEC 61000-4-5

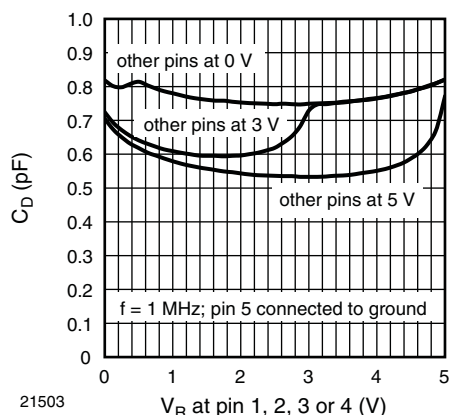


Fig. 3 - Typical Capacitance C_{D} vs. Reverse Voltage V_{R}

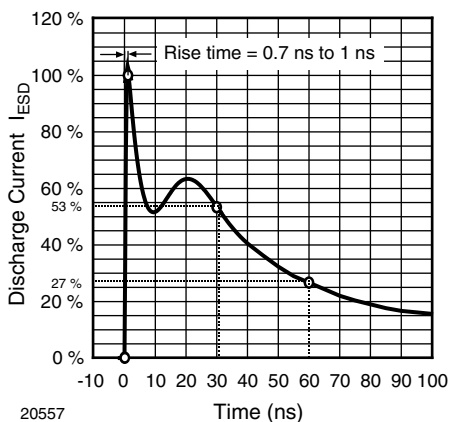


Fig. 2 - ESD Discharge Current Wave Form
acc. IEC 61000-4-2 (330 Ω /150 pF)

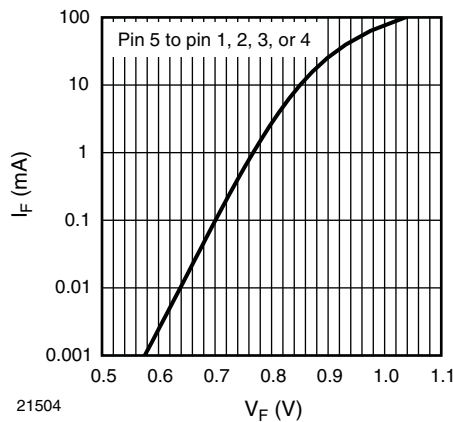


Fig. 4 - Typical Forward Current I_{F} vs. Forward Voltage V_{F}

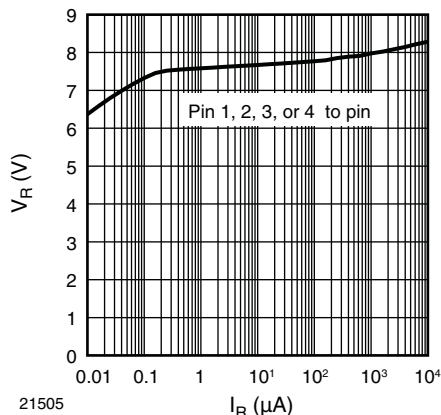


Fig. 5 - Typical Reverse Voltage V_R vs. Reverse Current I_R

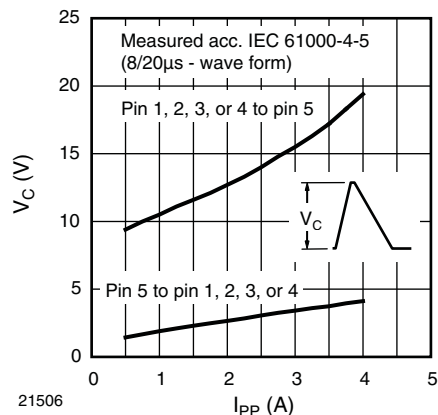
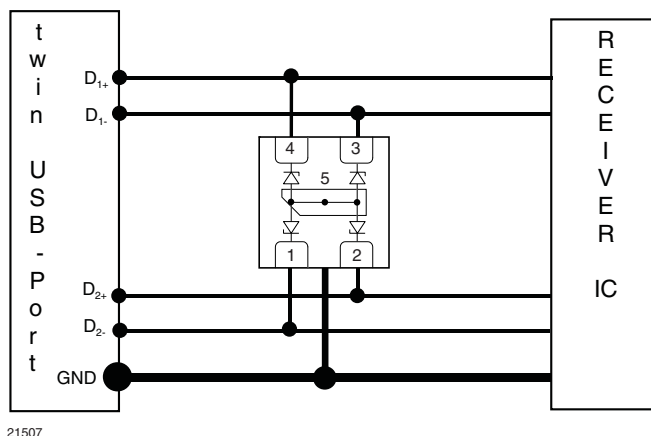


Fig. 6 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}

APPLICATION NOTE

With the VBUS054DD-HF4 a double, high speed USB-port or up to 4 other high speed signal or data lines can be protected against transient voltage signals. Negative transients will be clamped close below the ground level while positive transients will be clamped close above the 5 V working range. The high speed data lines, D_{1+} , D_{2+} , D_{1-} and D_{2-} , are connected to pin 1, 2, 3, and 4, pin 5 is connected to ground. As long as the signal voltage on the data lines is between the ground- and the breakthrough-level, the low input capacitance of each channel offer a very high isolation to ground and to the other data lines. But as soon as any transient signal exceeds this working range, the VBUS054DD-HF4 clamps the transient to ground or to the avalanche breakthrough voltage level.



BACKGROUND KNOWLEDGE

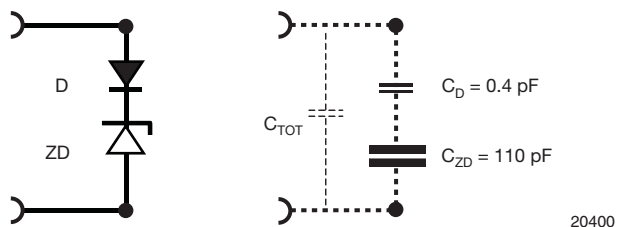
A zener- or avalanche diode is an ideal device for “cutting” or “clamping” voltage spikes or voltage transients down to low and uncritical voltage values. The breakthrough voltage can easily be adjusted by the chip-technology to any desired value within a wide range. Up to about 6 V the “zener-effect” (tunnel-effect) is responsible for the breakthrough characteristic. Above 6 V the so-called “avalanche-effect” is responsible. This is a more abrupt breakthrough phenomenon. Because of the typical “Z-shape” of the current-voltage-curve of such diodes, these diodes are generally called “Z-diode” (= zener or avalanche diodes). An equally important parameter for a protection diode is the ESD- and surge-power that allows the diode to short current in the pulse to ground without being destroyed.

This requirement can be adjusted by the size of the silicon chip (crystal). The bigger the active area the higher the current that the diode can short to ground.

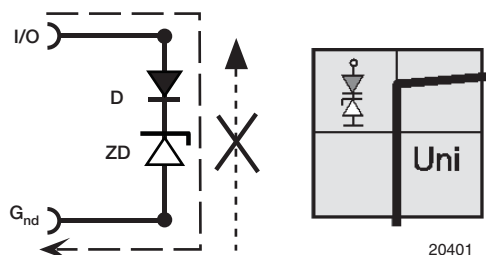
But the active area is also responsible for the diode capacitance - the bigger the area the higher the capacitance.

The dilemma is that a lot of applications require an effective protection against more than 8 kV ESD while the capacitance must be lower than 5 pF! This is well out of the normal range of a Z-diode. However, a protection diode with a low capacitance PN-diode (switching diode or junction diode) in series with a Z-diode, can fulfil both requirements simultaneously: low capacitance AND high ESD- and/or surge immunity become possible!

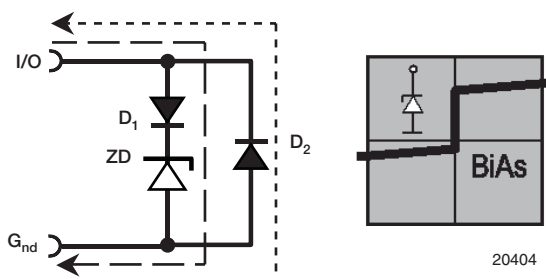
A small signal ($V_{pp} < 100$ mV) just sees the low capacitance of the PN-diode, while the big capacitance of the Z-diode in series remains “invisible”.



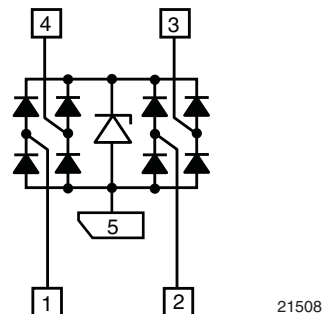
Such a constellation with a Z-diode and a small PN-diode (with low capacitance) in series (anti-serial) is a real unidirectional protection device. The clamping current can only flow in one direction (forward) in the PN-diode. The reverse path is blocked.



Another PN-diode “opens” the back path so that the protection device becomes bidirectional! Because the clamping voltage levels in forward and reverse directions are different, such a protection device has a **B**idirectional and **A**symmetrical clamping behaviour (**BiAs**) just like a single Z-diode.

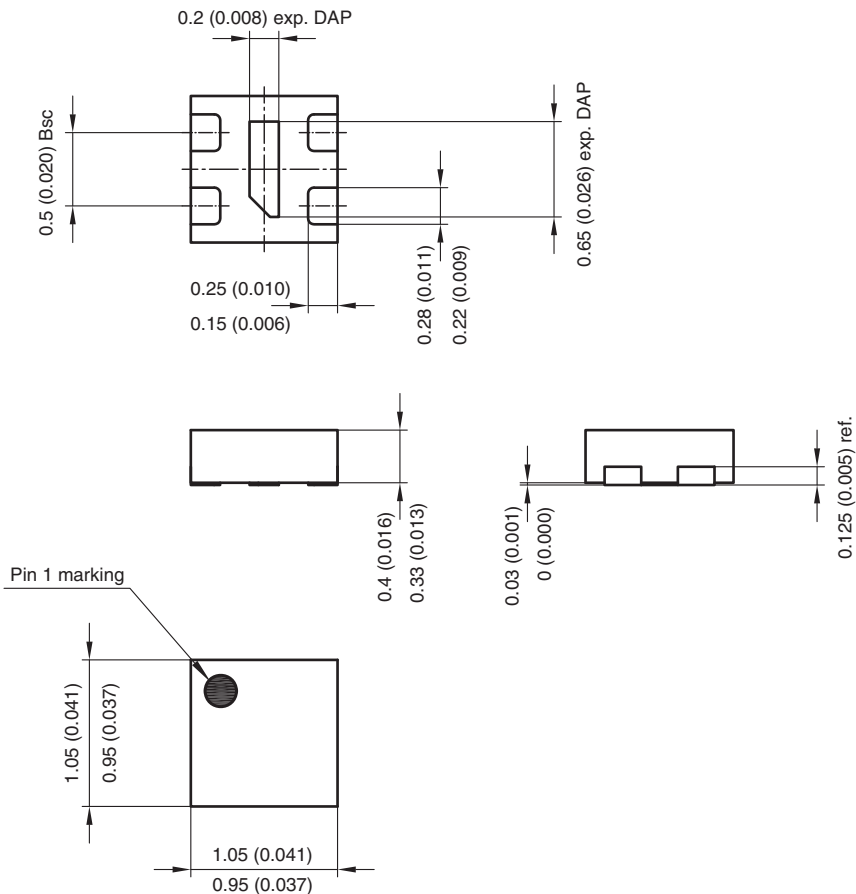


The VBUS054DD-HF4 offers four inputs with such protection circuit inside.

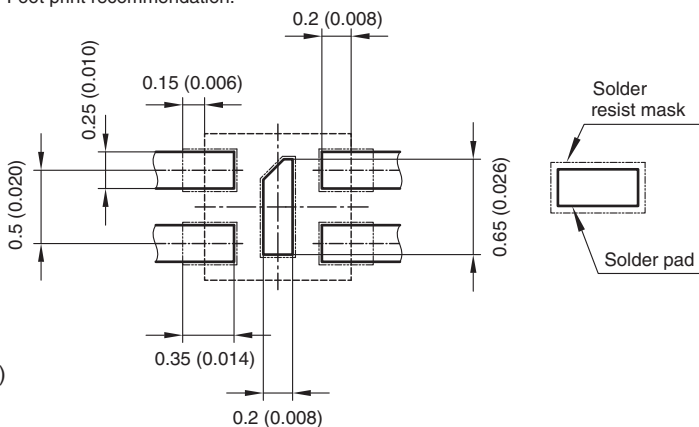




PACKAGE DIMENSIONS in millimeters (inches): **LLP1010-5L**



Foot print recommendation:



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Created - Date: 15. April. 2008
Rev. 3 - Date: 11. May. 2016
21380



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