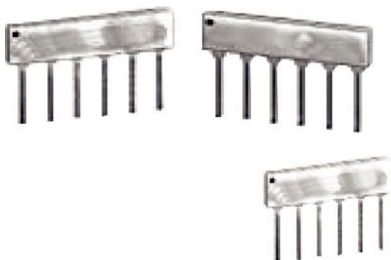


Ceramic Sandwich, Single-In-Line Thin Film Resistor, Through Hole Network (Low Profile 0.20 Custom)



Actual Size

Vishay Dale Thin Film presents a design concept in precision thin film resistor networks. The essence of this new concept is the marriage of two principle design elements . . . a unique resistive film, having electrical properties comparable to those of wire-wound resistors, and a rugged, low cost, ceramic package and an almost limitless variety of sizes and configurations.

FEATURES

- Gold-to-gold terminations. External leads are attached directly to gold pads on the ceramic substrate by thermo-compression bonding (no internal solder)
- Low profile (0.200 min.)
- Custom pin-outs available
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition



RoHS*
COMPLIANT
HALOGEN
FREE

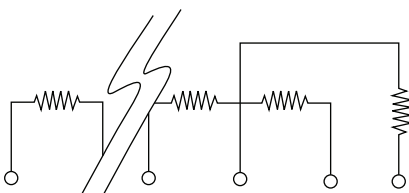
Note

* Pb containing terminations are not RoHS compliant, exemptions may apply

TYPICAL PERFORMANCE

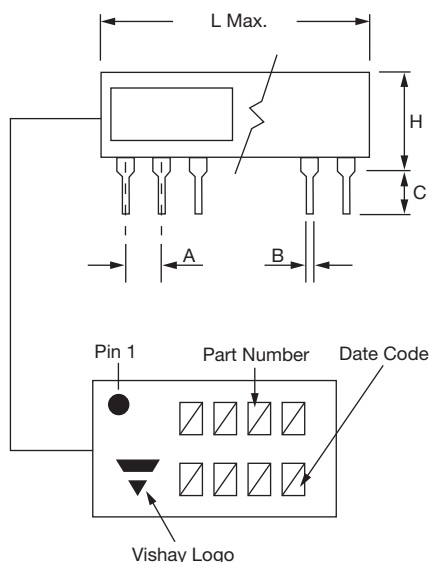
	ABSOLUTE	TRACKING
TCR	25	2
	ABSOLUTE	RATIO
TOL.	0.05	0.01

SCHEMATIC



Custom schematics available.
Please consult factory.

STANDARD ELECTRICAL SPECIFICATIONS			
TEST	SPECIFICATIONS		CONDITIONS
Material	Passivated nichrome	Tantalum nitride	-
Pin/Lead Number	3 to 10		-
Resistance Range	20 Ω to 2000 k Ω (total)	20 Ω to 500 k Ω (total)	-
TCR: Absolute	± 10 ppm/ $^{\circ}\text{C}$ to ± 25 ppm/ $^{\circ}\text{C}$	± 50 ppm/ $^{\circ}\text{C}$ to ± 100 ppm/ $^{\circ}\text{C}$	- 55 $^{\circ}\text{C}$ to + 125 $^{\circ}\text{C}$
TCR: Tracking	± 2 ppm/ $^{\circ}\text{C}$	± 5 ppm/ $^{\circ}\text{C}$	- 55 $^{\circ}\text{C}$ to + 125 $^{\circ}\text{C}$
Tolerance: Absolute	± 0.05 % to ± 1.0 %		+ 25 $^{\circ}\text{C}$
Tolerance: Ratio	± 0.01 % to ± 0.5 %	± 0.02 % to ± 0.5 %	+ 25 $^{\circ}\text{C}$
Power Rating: Resistor	100 mW (per element)		Typical at + 25 $^{\circ}\text{C}$
Power Rating: Package	-		-
Stability: Absolute	$\Delta R \pm 0.05$ %	$\Delta R \pm 0.1$ %	2000 h at + 70 $^{\circ}\text{C}$
Stability: Ratio	$\Delta R \pm 0.015$ %	$\Delta R \pm 0.02$ %	2000 h at + 70 $^{\circ}\text{C}$
Voltage Coefficient	< 0.1 ppm/V	< 0.1 ppm/V	-
Working Voltage	100 V		-
Operating Temperature Range	- 55 $^{\circ}\text{C}$ to + 125 $^{\circ}\text{C}$		-
Storage Temperature Range	- 55 $^{\circ}\text{C}$ to + 150 $^{\circ}\text{C}$		-
Noise	< - 30 dB		-
Thermal EMF	< 0.08 $\mu\text{V}/^{\circ}\text{C}$		-
Shelf Life Stability: Absolute	$\Delta R \pm 0.01$ %		1 year at + 25 $^{\circ}\text{C}$
Shelf Life Stability: Ratio	$\Delta R \pm 0.002$ %		1 year at + 25 $^{\circ}\text{C}$

DIMENSIONS AND IMPRINTING in inches and millimeters


DIMENSION	INCHES	MILLIMETERS
A	0.100 typ. ⁽¹⁾	2.54 typ.
B	0.020 ± 0.002 typ.	0.51 ± 0.05 typ.
C	0.125 min.	3.17 min.
D	0.100 max.	2.54 max.
E	0.010	0.25
L (3 Pins)	0.320	8.13
L (4 Pins)	0.420	10.67
L (5 Pins)	0.520	13.21
L (6 Pins)	0.620	15.75
L (7 Pins)	0.720	18.25
L (8 Pins)	0.820	20.83
L (9 Pins)	0.920	23.37
L (10 Pins)	1.020	25.91
H (3 Pins)	0.200 ⁽²⁾	7.11 ⁽²⁾
H (4 Pins)		
H (5 Pins)		
H (6 Pins)		
H (7 Pins)		
H (8 Pins)		
H (9 Pins)		
H (10 Pins)		

Notes
⁽¹⁾ Non-accum.

⁽²⁾ Resistance value and schematic dependent. By occupying more than one 0.100 inch square, higher values are available.

MECHANICAL SPECIFICATIONS

Resistive Element	Passivated nichrome or tantalum nitride
Substrate Material	Alumina
Body	Ceramic
Terminals	Copper alloy
Plating	Gold
Tin/Lead Option	Sn63
Lead (Pb)-free Option	Sn96.5, Ag3.0, Cu0.5
Tin/Lead and Lead (Pb)-free Finish	Hot solder dip

ORDERING INFORMATION CHECK LIST

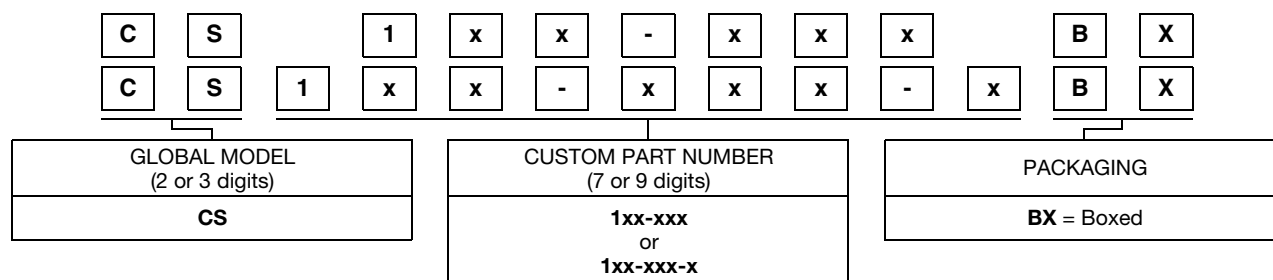
Special requirements should be identified in advance, but as a minimum, you should have the following information ready.

ELECTRICAL	MECHANICAL
<ol style="list-style-type: none"> Resistors, by value and tolerance Reference resistor(s) and matching of which resistors to which reference resistors Resistance by ratio Absolute temperature coefficient of resistivity Temperature tracking of subordinate resistors to reference resistor(s) Maximum operating voltage Resistor power ratings Operating temperature range 	<ol style="list-style-type: none"> Maximum allowable seated height (from PC board to top of network) Special marking concerns Schematic pin out of package Specify if lead (Pb)-free
For additional assistance refer to Vishay Dale Thin Film's guide to understanding Thin Film precision. Resistor networks or application engineering. All standard products may be ordered directly from Vishay Dale Thin Film.	

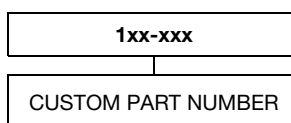


GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: CS1xx-xxxBX



Historical Part Number example: 1xx-xxx (for reference purposes only)





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