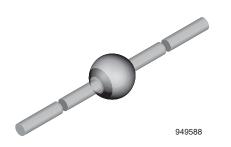


## Vishay Semiconductors

# **Standard Avalanche Sinterglass Diode**



#### **FEATURES**

- · Glass passivated junction
- Hermetically sealed package
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



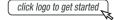


COMPLIANT HALOGEN FREE

### **APPLICATIONS**

- High voltage rectification
- Efficiency diode in horizontal deflection circuit

#### **DESIGN SUPPORT TOOLS**





#### **MECHANICAL DATA**

Case: SOD-64

Terminals: plated axial leads, solderable per MIL-STD-750,

method 2026

Polarity: color band denotes cathode end

Mounting position: any Weight: approx. 858 mg

| ORDERING INFORMATION (Example) |               |  |        |  |  |
|--------------------------------|---------------|--|--------|--|--|
| DEVICE NAME                    | ORDERING CODE | NG CODE TAPED UNITS MINIMUM ORDER QUANTITY |        |  |  |
| BY228                          | BY228TR       | 2500 per 10" tape and reel                 | 12 500 |  |  |
| BY228                          | BY228TAP      | 2500 per ammopack                          | 12 500 |  |  |

| PARTS TABLE |   |         |  |  |  |
|-------------|---|---------|--|--|--|
| PART        | TYPE DIFFERENTIATION                              | PACKAGE |  |  |  |
| BY228       | V <sub>R</sub> = 1500 V; I <sub>F(AV)</sub> = 3 A | SOD-64  |  |  |  |

| ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified) |  |       |                    |             |      |  |
|---|--|-------|--------------------|-------------|------|--|
| PARAMETER   | TEST CONDITION                         | PART  | SYMBOL             | VALUE       | UNIT |  |
| Reverse voltage   | See electrical characteristics         | BY228 | $V_R$              | 1500        | V    |  |
| Repetitive peak reverse voltage   | I <sub>R</sub> = 100 μA                |       | $V_{RRM}$          | 1650        | V    |  |
| Peak forward surge current  | t <sub>p</sub> = 10 ms, half sine wave |       | I <sub>FSM</sub>   | 50          | Α    |  |
| Average forward current   |  |       | I <sub>F(AV)</sub> | 3           | Α    |  |
| Junction temperature  |  |       | Tj                 | 140         | °C   |  |
| Storage temperature range   |  |       | T <sub>stg</sub>   | -55 to +175 | °C   |  |
| Non repetitive reverse avalanche energy   | I <sub>(BR)R</sub> = 0.4 A             |       | E <sub>R</sub>     | 10          | mJ   |  |

| MAXIMUM THERMAL RESISTANCE (T <sub>amb</sub> = 25 °C, unless otherwise specified) |                                |            |       |      |  |
|---|--------------------------------|------------|-------|------|--|
| PARAMETER   | TEST CONDITION SYMBOL          |            | VALUE | UNIT |  |
| Junction ambient  | On PC board with spacing 25 mm | $R_{thJA}$ | 70    | K/W  |  |



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| <b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified) |  |                 |      |      |      |      |
|--|--|-----------------|------|------|------|------|
| PARAMETER  | TEST CONDITION   | SYMBOL          | MIN. | TYP. | MAX. | UNIT |
| Forward voltage  | I <sub>F</sub> = 5 A   | V <sub>F</sub>  | -    | -    | 1.5  | V    |
| Reverse current  | V <sub>R</sub> = 1500 V  | I <sub>R</sub>  | -    | 2    | 5    | μA   |
| neverse current  | V <sub>R</sub> = 1500 V, T <sub>j</sub> = 140 °C               | I <sub>R</sub>  | -    | -    | 140  | μA   |
| Reverse recovery time  | $I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, i_R = 0.25 \text{ A}$ | t <sub>rr</sub> | -    | -    | 2    | μs   |
| Total reverse recovery time  | $I_F = 1 \text{ A}, - dI_F/dt = 0.05 \text{ A/}\mu\text{s}$    | t <sub>rr</sub> | -    | -    | 20   | μs   |

### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

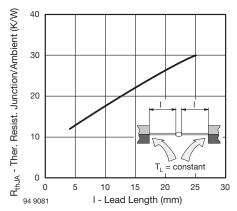


Fig. 1 - Typ. Thermal Resistance vs. Lead Length

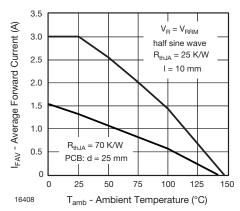


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

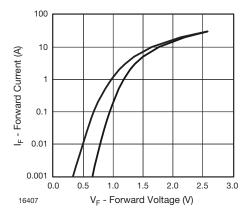


Fig. 2 - Forward Current vs. Forward Voltage

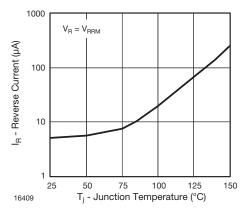


Fig. 4 - Reverse Current vs. Junction Temperature



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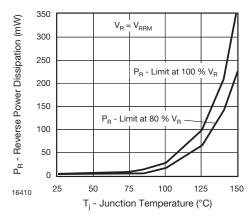


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

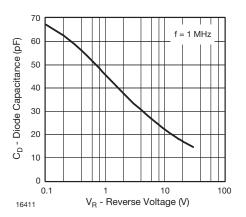
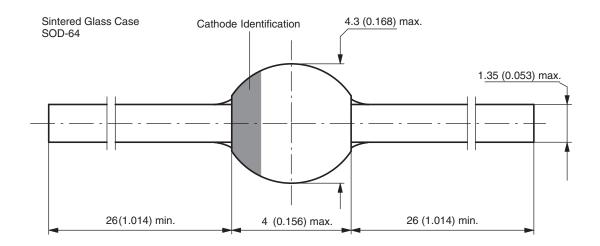


Fig. 6 - Diode Capacitance vs. Reverse Voltage

### PACKAGE DIMENSIONS in millimeters (inches): SOD-64



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