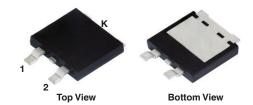
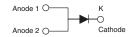


Ultrafast Rectifier, 16 A FRED Pt®

eSMP[®] Series SMPD (TO-263AC)





LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	16 A			
V _R	600 V			
V _F at I _F	0.91 V			
t _{rr}	55 ns			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Single			

FEATURES

 Ultrafast recovery time, reduced Q_{rr}, and soft recovery



RoHS

COMPLIANT

HALOGEN FREE

- 175 °C maximum operating junction temperature
- For PFC CRM, snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in PFC, boost, lighting, in the AC/DC section of SMPS, freewheeling and clamp diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _{solder pad} = 141 °C	16	Λ
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C, 6 ms square pulse	160	A

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN. TYP. MAX.		UNITS		
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	$I_R = 100 \mu A$	600	-	-	
Forward voltage V _F	V	I _F = 16 A	-	1.04	1.25	V
	VF	I _F = 16 A, T _J = 150 °C	-	0.91	1.1	
Reverse leakage current	1	$V_R = V_R$ rated	-	-	15	μA
neverse leakage current	I _R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	70	300	μΑ
Junction capacitance	C _T	V _R = 600 V	-	16	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt = 50 A$	/μs, V _R = 30 V	-	55	-	
Davida va a a a a a a a a a a a a a a a a a		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	55	no
Reverse recovery time	t _{rr}	T _J = 25 °C	I _F = 16 A, dI _F /dt = 500 A/μs, V _R = 400 V	-	100	-	ns
		T _J = 125 °C		-	150	-	
Peak recovery current	1	T _J = 25 °C		-	20	-	Α
reak recovery current	I _{RRM}	$T_{\rm J} = 125 ^{\circ}\text{C}$ $V_{\rm R} = 400 ^{\circ}$		-	27	-	_ A
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	1	-	μC
	T _J = 125 °C		-	2	-	μΟ	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	+175	°C
Thermal resistance, junction to mount	R_{thJM}		-	1.2	1.7	°C/W
Approximate weight				0.55		g
Marking device		Case style SMPD (TO-263AC)		16EI	DU06	

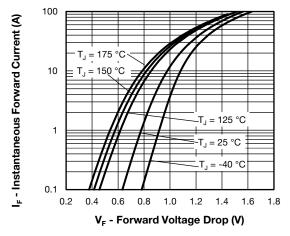


Fig. 1 - Typical Forward Voltage Drop Characteristics

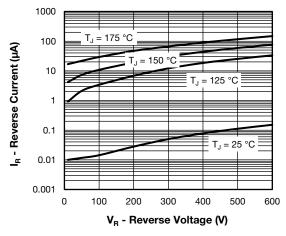


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



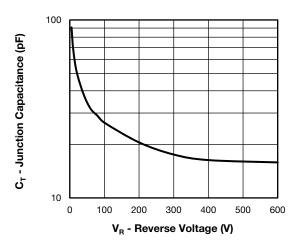


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

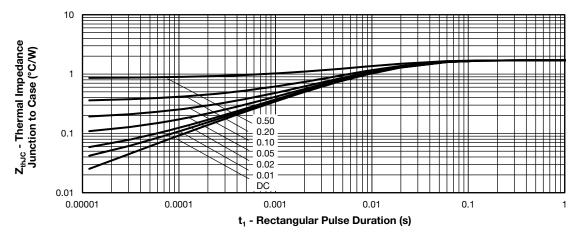


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

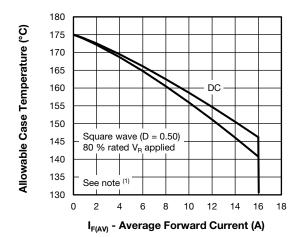


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

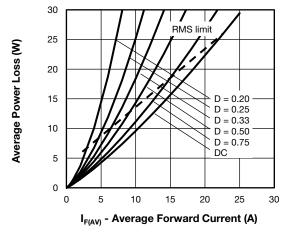


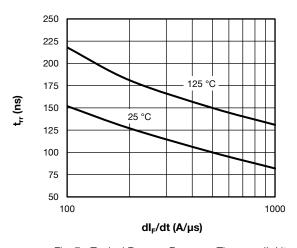
Fig. 6 - Forward Power Loss Characteristics

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 5); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = rated V_R$

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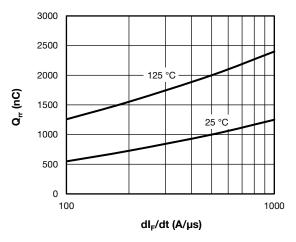
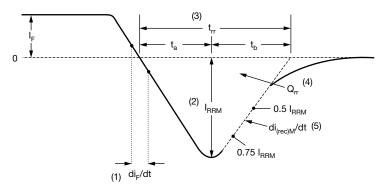


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) ${\rm I}_{\rm RRM}$ peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_F$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

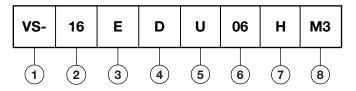
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (16 A)

3 - Circuit configuration:

E = single die

- D = SMPD package

5 - Process type,

U = ultrafast recovery

6 - Voltage code (06 = 600 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

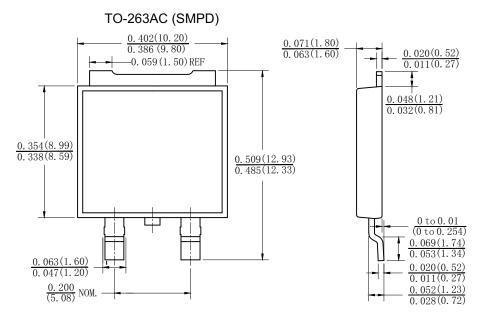
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-16EDU06HM3/I	2000	2000	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95604</u>				
Part marking information	www.vishay.com/doc?95566			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96771			

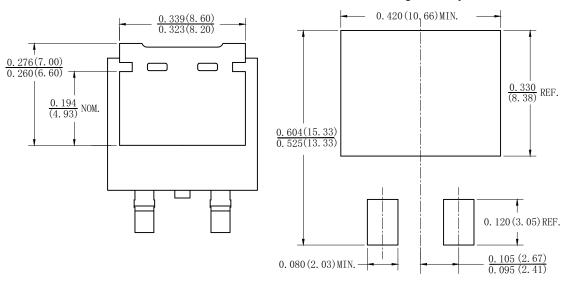


TO-263AC (SMPD)

DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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Vishay

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