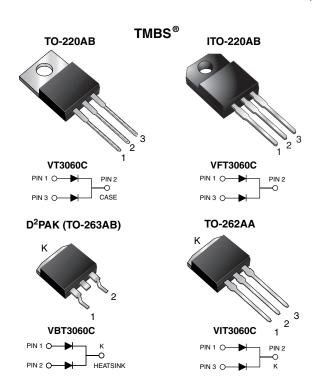
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## **Dual High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.38 \text{ V}$  at  $I_F = 5 \text{ A}$ 



#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 15 A					
$V_{RRM}$	60 V					
I <sub>FSM</sub>	170 A					
V <sub>F</sub> at I <sub>F</sub> = 15 A	0.57 V					
T <sub>J</sub> max.	150 °C					
Package	TO-220AB, ITO-220AB, D <sup>2</sup> PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

### **FEATURES**

Trench MOS Schottky technology



• Low forward voltage drop, low power losses

• High efficiency operation

m RoHS

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)

- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB, ITO-220AB and TO-262AA package)
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **TYPICAL APPLICATIONS**

For use in high frequency inverters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB, ITO-220AB,  $D^2PAK$  (TO-263AB), and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs max.

PARAMETER			VT3060C	VFT3060C	VBT3060C	VIT3060C	UNIT
Max. repetitive peak reverse voltage		V <sub>RRM</sub>	$V_{\text{RRM}}$ 60				V
Max. average forward rectified current	per device	I	30			А	
(fig. 1)	per diode	I <sub>F(AV)</sub>	15				
Peak forward surge current 8.3 ms single half sir superimposed on rated load per diode	k forward surge current 8.3 ms single half sine-wave erimposed on rated load per diode		170			Α	
Non-repetitive avalanche energy at $T_J = 25$ °C, L	= 60 mH per diode	E <sub>AS</sub>	180				mJ
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz per diode	$T_{J} = 38  ^{\circ}\text{C} \pm 2  ^{\circ}\text{C}$	C IR <sub>RM</sub> 1.0			Α		
Isolation voltage (ITO-220AB only) from terminal t	o heatsink t = 1 min	V <sub>AC</sub>	V <sub>AC</sub> 1500			V	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150			°C	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>A</sub> = 25 °C	V <sub>BR</sub>	60 (min.)	-	V		
Instantaneous forward voltage per diode (1)	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub>	0.47	-	V		
	I <sub>F</sub> = 7.5 A			0.51	-			
	I <sub>F</sub> = 15 A			0.60	0.70			
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.38	-			
	I <sub>F</sub> = 7.5 A			0.44	-			
	I <sub>F</sub> = 15 A			0.57	0.65			
Reverse current per diode (2)	V 60 V	T <sub>A</sub> = 25 °C	I <sub>R</sub>	=	1.2	mA		
	$V_R = 60 \text{ V}$ $T_A = 12$	T <sub>A</sub> = 125 °C		20	45			

#### **Notes**

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER		SYMBOL	VT3060C	VFT3060C	VBT3060C	VIT3060C	UNIT	
Typical thermal resistance	per diode	R <sub>eJC</sub>	2.5	6.0	2.5	2.5	°C/W	
	per device		1.7	4.8	1.7	1.7	C/VV	

ORDERING INFORMATION (Example)									
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
TO-220AB	VT3060C-E3/4W	1.89	4W	50/tube	Tube				
ITO-220AB	VFT3060C-E3/4W	1.76	4W	50/tube	Tube				
D <sup>2</sup> PAK (TO-263AB)	VBT3060C-E3/4W	1.39	4W	50/tube	Tube				
D <sup>2</sup> PAK (TO-263AB)	VBT3060C-E3/8W	1.39	8W	800/reel	Tape and reel				
TO-262AA	VIT3060C-E3/4W	1.46	4W	50/tube	Tube				

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### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

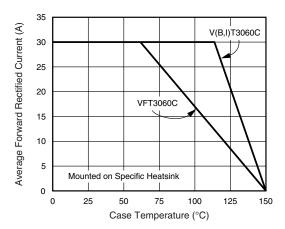


Fig. 1 - Maximum Forward Current Derating Curve

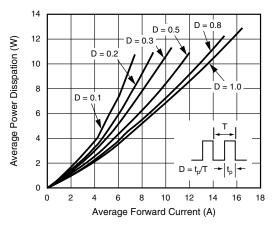


Fig. 2 - Forward Power Dissipation Characteristics Per Diode

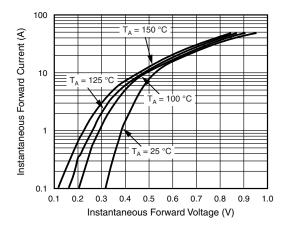


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

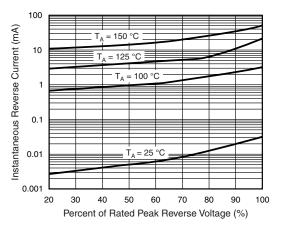


Fig. 4 - Typical Reverse Characteristics Per Diode

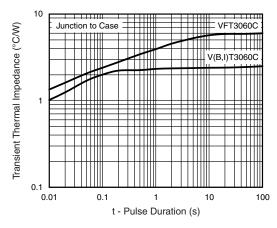


Fig. 5 - Typical Transient Thermal Impedance Per Diode

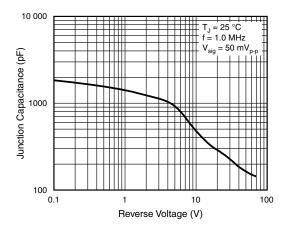
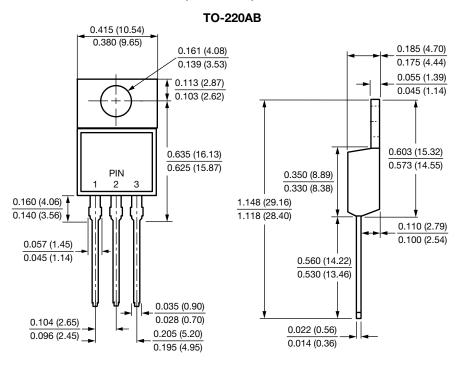


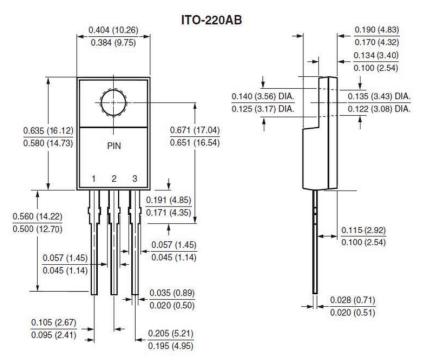
Fig. 6 - Typical Junction Capacitance Per Diode

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### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

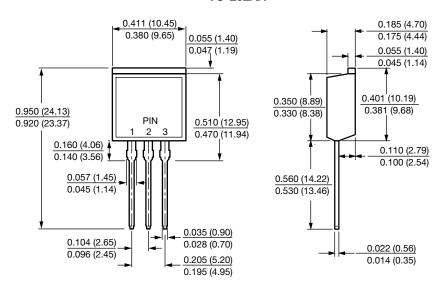




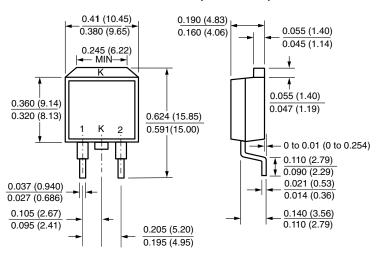
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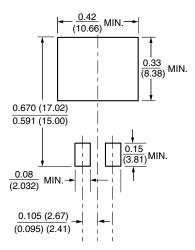
#### **TO-262AA**



### D<sup>2</sup>PAK (TO-263AB)



### **Mounting Pad Layout**





### **Legal Disclaimer Notice**

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