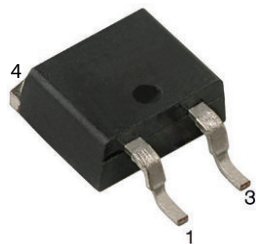
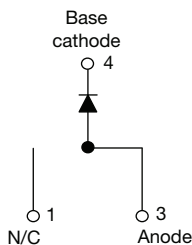


Hyperfast Rectifier, 15 A FRED Pt® G5


D²PAK 2L (TO-263AB 2L)


FEATURES

- Best in class forward voltage drop and switching losses trade off
- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Polyimide passivation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT
HALOGEN
FREE

LINKS TO ADDITIONAL RESOURCES



3D Models



Application Notes

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	15 A
V_R	600 V
V_F at I_F at 125 °C	1.3 V
t_{rr} (typ.)	19 ns
T_J max.	175 °C
Package	D ² PAK 2L (TO-263AB 2L)
Circuit configuration	Single

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for soft switched and resonant converters, as well as medium frequency hard switching converters. This device is specifically designed to improve efficiency of high speed LLC output rectification stages of EV / HEV battery charging stations and high frequency stages of UPS applications

MECHANICAL DATA

Case: D²PAK 2L (TO-263AB 2L)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V_{RRM}		600	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 129\text{ °C}$, $D = 0.50$	15	A
Repetitive peak forward current	I_{FRM}	$T_C = 129\text{ °C}$, $D = 0.50$, $f = 20\text{ kHz}$	30	
Non-repetitive peak surge current	I_{FSM}	$T_C = 25\text{ °C}$, $t_p = 10\text{ ms}$, sine wave	185	
Operating junction and storage temperature	T_J , T_{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_R	$I_R = 100\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	V_F	$I_F = 15\text{ A}$	-	1.6	2.1	
		$I_F = 15\text{ A}$, $T_J = 125\text{ °C}$	-	1.3	-	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	-	10	μA
		$T_J = 125\text{ °C}$, $V_R = V_R$ rated	-	-	500	
Junction capacitance	C_T	$V_R = 200\text{ V}$	-	25	-	pF
Series inductance	L_S	Measured to lead 5 mm from package body	-	8	-	nH

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1.0\text{ A}$, $dI_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	19	-	ns
		$T_J = 25\text{ }^{\circ}\text{C}$	-	23	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	36	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	12	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	20	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	180	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	472	-	
Reverse recovery time	t_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	33	-	ns
		$T_J = 125\text{ }^{\circ}\text{C}$	-	44	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	13	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	21	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	220	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	578	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	R_{thJC}		-	-	1.72	$^{\circ}\text{C}/\text{W}$
Weight			-	2.0	-	g
Maximum junction and storage temperature range	T_J, T_{Stg}		-55	-	175	$^{\circ}\text{C}$
Marking device		Case style D ² PAK 2L (TO-263AB 2L)	E5TX1506S			

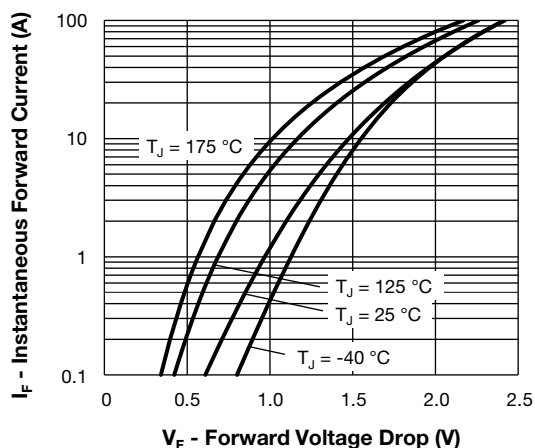


Fig. 1 - Forward Voltage Drop Characteristics, Per Leg

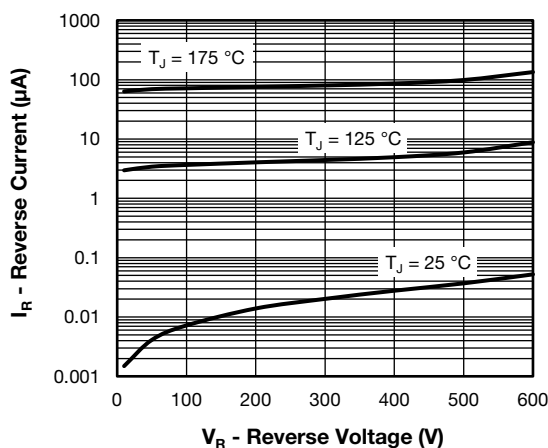


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage, Per Leg

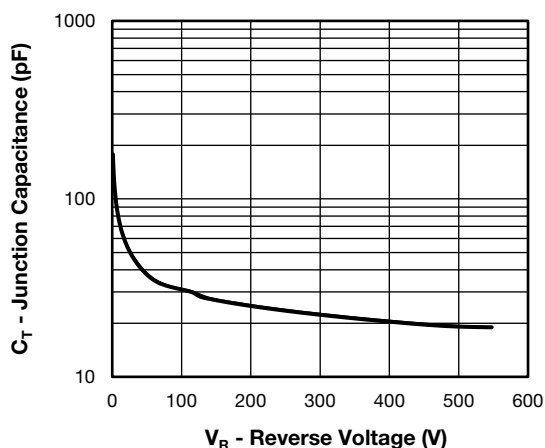


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage, Per Leg

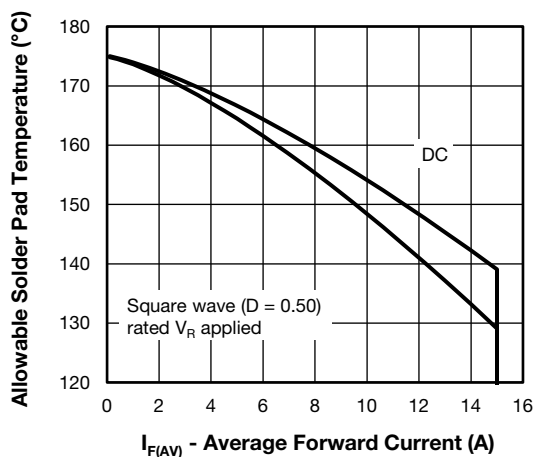


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current, Per Leg

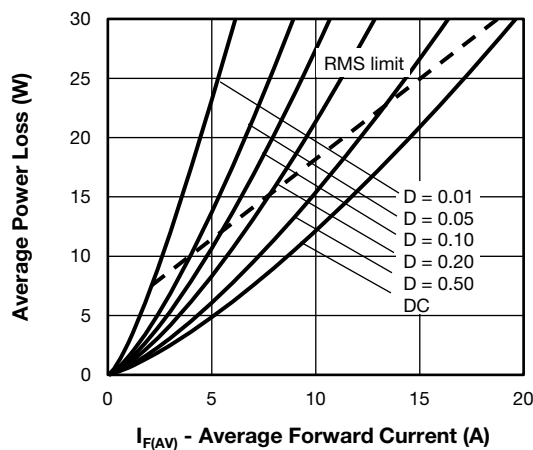


Fig. 5 - Forward Power Loss Characteristics, Per Leg

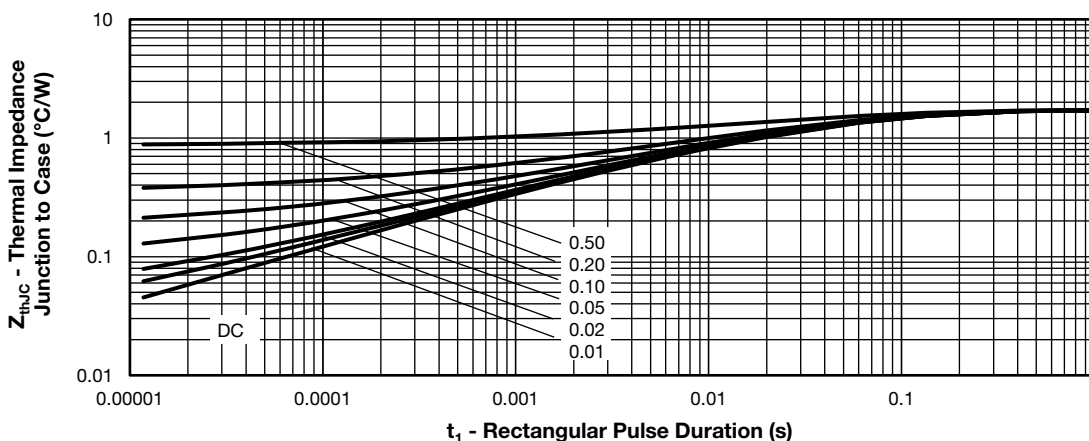
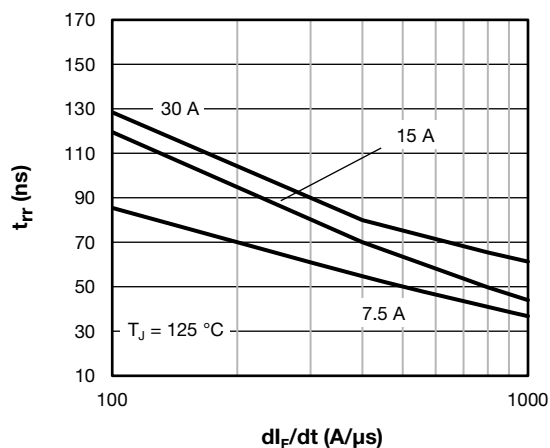
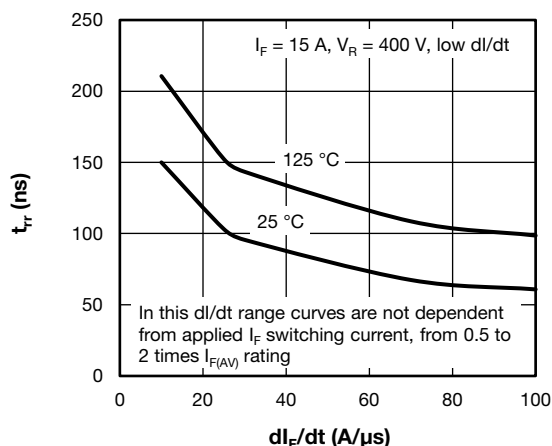
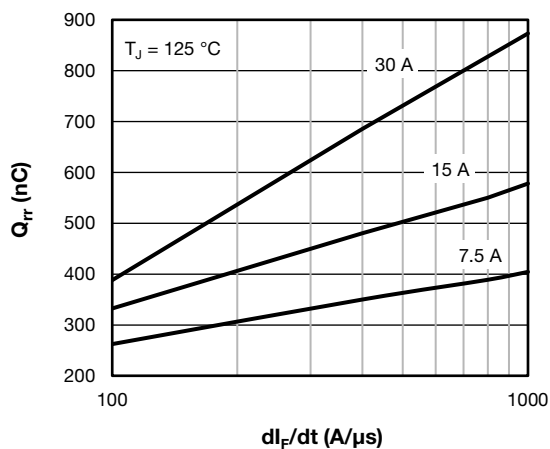
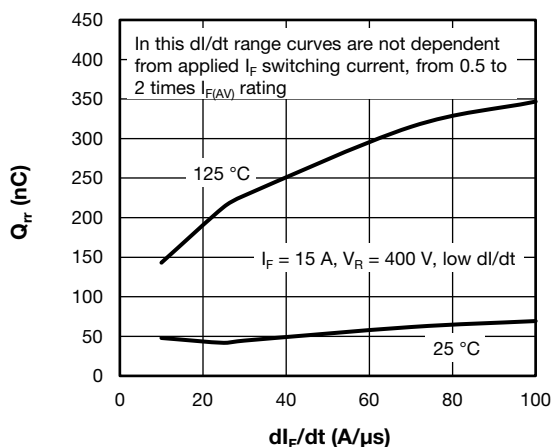
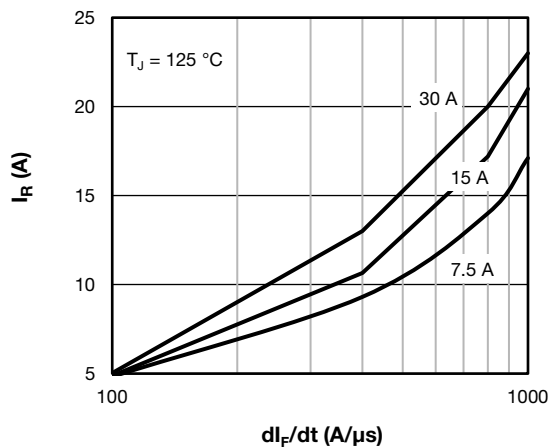
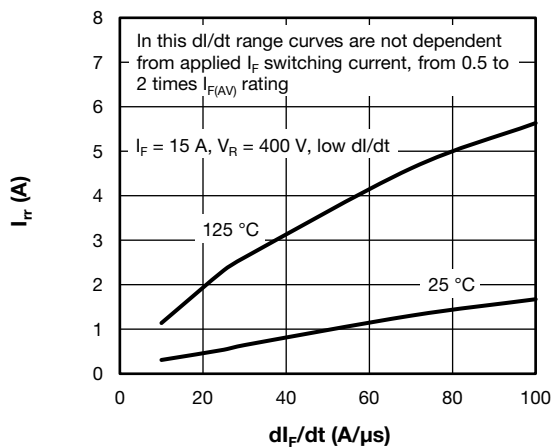


Fig. 6 - Transient Thermal Impedance, Junction to Case, Per Leg


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt , Per Leg

Fig. 10 - Typical Reverse Recovery Time vs. dI_F/dt , Per Leg

Fig. 8 - Typical Reverse Recovery Charge vs. dI_F/dt , Per Leg

Fig. 11 - Typical Reverse Recovery Charge vs. dI_F/dt , Per Leg

Fig. 9 - Typical Reverse Recovery Current vs. dI_F/dt , Per Leg

Fig. 12 - Typical Reverse Recovery Current vs. dI_F/dt , Per Leg

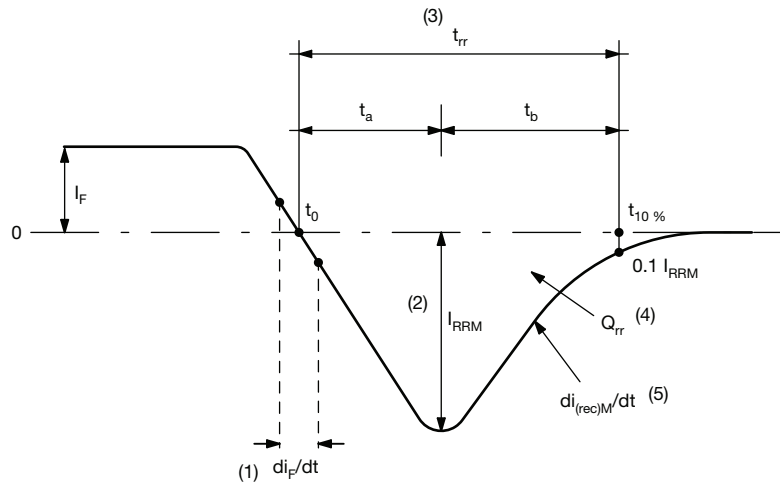


Fig. 13 - Reverse Recovery Waveform and Definitions

Notes

- (1) di_F/dt - rate of change of current through zero crossing
- (2) I_{RRM} - peak reverse recovery current
- (3) t_{rr} - reverse recovery time measured from t_0 , crossing point of negative going I_F , to point $t_{10\%}$, $0.1 I_{RRM}$
- (4) Q_{rr} - area under curve defined by t_0 and $t_{10\%}$

$$Q_{rr} = \int_{t_0}^{t_{10\%}} I(t) dt$$

- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}



ORDERING INFORMATION TABLE

Device code	VS-	E	5	T	X	15	06	S2	L	-M3
	1	2	3	4	5	6	7	8	9	10
1	-	Vishay Semiconductors product								
2	-	E = single diode								
3	-	5 = FRED generation 5								
4	-	Package: T = D ² PAK (TO-263) package								
5	-	X = hyperfast recovery								
6	-	Current rating (15 = 15 A)								
7	-	Voltage rating (12 = 1200 V)								
8	-	S2 = true 2 pin D ² PAK								
9	-	None = tube (50 pieces) • L = tape and reel (left oriented, for D ² PAK package) If needed different orientation/packaging, please contact factory								
10	-	Environmental digit: -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free								

ORDERING INFORMATION (Example)		
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-E5TX1506S2L-M3	800	13" diameter reel

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96683
Part marking information	www.vishay.com/doc?96693
Packaging information	www.vishay.com/doc?95032



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