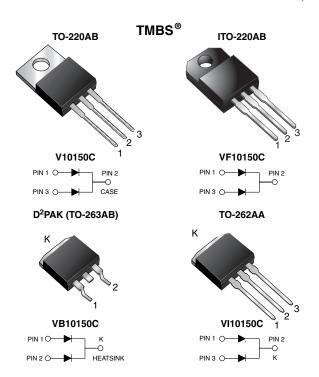
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## **Dual High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.63 \text{ V}$  at  $I_F = 3 \text{ A}$ 



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 5.0 A					
$V_{RRM}$	150 V					
I <sub>FSM</sub>	60 A					
$V_F$ at $I_F = 5$ A	0.69 V					
T <sub>J</sub> max.	150 °C					
Package	TO-220AB, ITO-220AB, D <sup>2</sup> PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

### **FEATURES**

Trench MOS Schottky technology



• Low forward voltage drop, low power losses

• High efficiency operation

(e3)

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)

 Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB, ITO-220AB and TO-262AA package)

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB, ITO-220AB,  $D^2PAK$  (TO-263AB), and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs max.

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER		SYMBOL	V10150C	VF10150C	VB10150C	VI10150C	UNIT	
Max. repetitive peak reverse voltage			150					
Max. average forward rectified current (fig. 1) per device		1	10				Α	
pe	er diode	IF(AV)	I <sub>F(AV)</sub>		5.0			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode	I <sub>FSM</sub>	60			Α			
Non-repetitive avalanche energy at T <sub>J</sub> = 25 °C, L = 60 mH per diode			23			mJ		
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C per diode	I <sub>RRM</sub>	0.5			Α			
Voltage rate of change (rated V <sub>R</sub> )			10 000			V/µs		
Isolation voltage (ITO-220AB only) from terminal to heatsink t = 1 min			1500			V		
Operating junction and storage temperature range				-55 to	+150		°C	

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
Breakdown voltage	$I_R = 1.0 \text{ mA}$	T <sub>A</sub> = 25 °C	$V_{BR}$	150 (min.)	-	V		
Instantaneous forward voltage per diode (1)	I <sub>F</sub> = 3 A	T <sub>A</sub> = 25 °C	V <sub>F</sub>	0.82	-	V		
	I <sub>F</sub> = 5 A			0.99	1.41			
	$I_F = 3 A$	T <sub>A</sub> = 125 °C		0.63	-			
	I <sub>F</sub> = 5 A			0.69	0.75			
Reverse current per diode (2)	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C		0.5	-	μA		
		T <sub>A</sub> = 125 °C	I <sub>R</sub>	0.5	-	mA		
	V <sub>R</sub> = 150 V	T <sub>A</sub> = 25 °C		-	100	μA		
		T <sub>A</sub> = 125 °C		1.0	10	mA		

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	V10150C	VF10150C	VB10150C	VI10150C	UNIT	
Typical thermal resistance per diode	$R_{\theta JC}$	4.0	6.5	4.0	4.0	°C/W	

ORDERING INFORMATION (Example)								
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
TO-220AB	V10150C-E3/4W	1.87	4W	50/tube	Tube			
ITO-220AB	VF10150C-E3/4W	1.74	4W	50/tube	Tube			
D <sup>2</sup> PAK (TO-263AB)	VB10150C-E3/4W	1.39	4W	50/tube	Tube			
D <sup>2</sup> PAK (TO-263AB)	VB10150C-E3/8W	1.38	8W	800/reel	Tape and reel			
D <sup>2</sup> PAK (TO-263AB)	VI10150C-E3/4W	1.45	4W	50/tube	Tube			

## **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

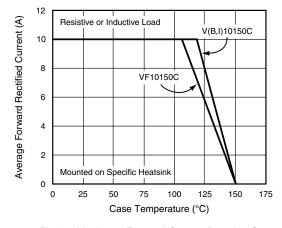


Fig. 1 - Maximum Forward Current Derating Curve

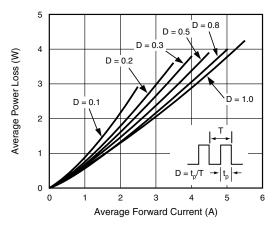


Fig. 2 - Forward Power Loss Characteristics Per Diode

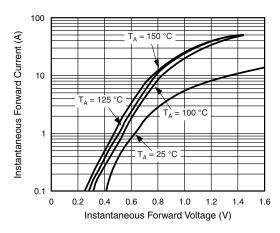


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

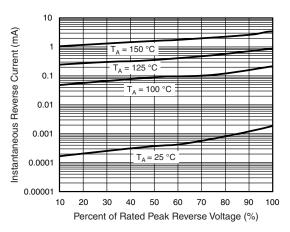


Fig. 4 - Typical Reverse Characteristics Per Diode

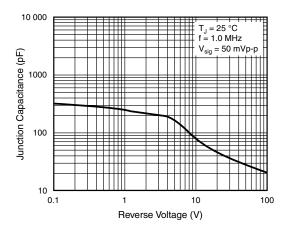


Fig. 5 - Typical Junction Capacitance Per Diode

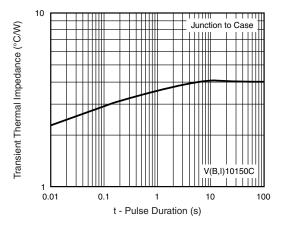


Fig. 6 - Typical Transient Thermal Impedance Per Diode

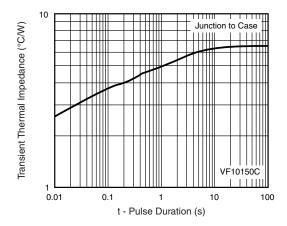
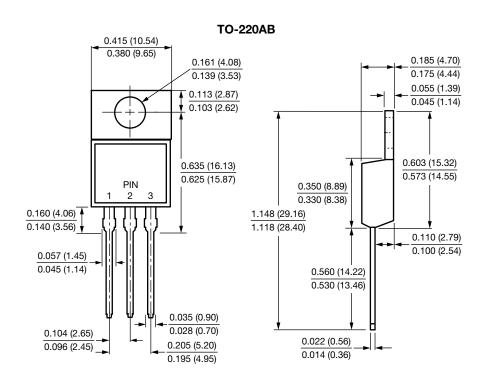


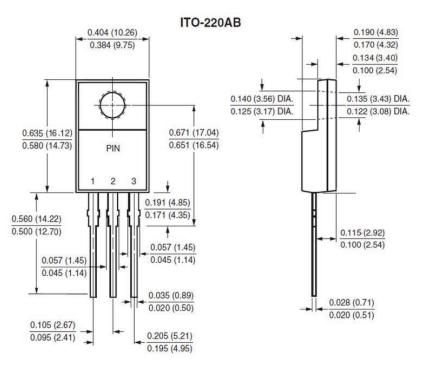
Fig. 7 - Typical Transient Thermal Impedance Per Diode

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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

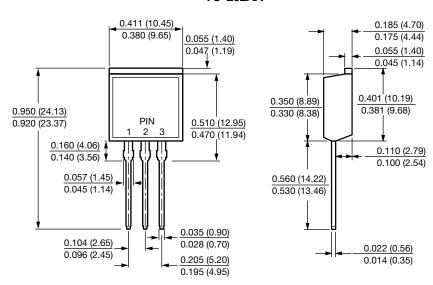




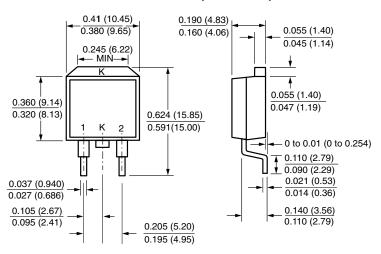
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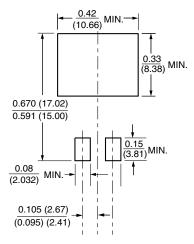
#### **TO-262AA**



### D<sup>2</sup>PAK (TO-263AB)



### **Mounting Pad Layout**





## **Legal Disclaimer Notice**

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