

# **High Performance Schottky Rectifier, 2 A**



SMA (DO-214AC)

### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub> 2 A			
$V_{R}$	100 V		
V <sub>F</sub> at I <sub>F</sub>	0.72 V		
I <sub>RM</sub> max.	1 mA at 125 °C		
T <sub>J</sub> max.	150 °C		
E <sub>AS</sub>	1.0 mJ		
Package SMA (DO-214AC)			
Circuit configuration	Single		

### **FEATURES**

- · Small foot print, surface mountable
- Low forward voltage drop



- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION**

The VS-20MQ100NTRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I <sub>F(AV)</sub>	Rectangular waveform	2	A	
V <sub>RRM</sub>		100	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	120	A	
V <sub>F</sub>	2 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.72	V	
TJ	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-20MQ100NTRPbF	UNITS	
Maximum DC reverse voltage	$V_{R}$	100	V	
Maximum working peak reverse voltage	$V_{RWM}$	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDIT	TIONS	VALUES	UNITS
Maximum average forward current		50 % duty cycle at T <sub>C</sub> = 113 °C, r On PC board 9 mm <sup>2</sup> island (0.013		2.1	А
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 116 °C, r On PC board 9 mm <sup>2</sup> island (0.013	_	2	A
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	120	
non-repetitive surge current See fig. 6	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	30	Α
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 0.5  \text{A},  L = 8  \text{mH}$		1.0	mJ
Repetitive avalanche current	I <sub>AR</sub>			0.5	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		2 A		0.91	V
		1.5 A	T <sub>J</sub> = 25 °C	0.85	
Maximum forward voltage drop	V (1)	1 A		0.78	
See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	2 A		0.72	
		1.5 A	T <sub>J</sub> = 125 °C	0.68	
		1 A		0.63	
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>B</sub>	, 0.1	A
See fig. 2	'RM '''	T <sub>J</sub> = 125 °C	V <sub>R</sub> = nated V <sub>R</sub>	1	mA
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.52	V
Forward slope resistance	r <sub>t</sub>			78.4	mΩ
Typical junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 10 V <sub>DC</sub> , T <sub>J</sub> = 25 °C, test signal = 1 MHz		38	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 1		10 000	V/µs

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	OZ.
Marking device		Case style SMA (DO-214AC) (similar D-64)	2	J

### Note

$$^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$$

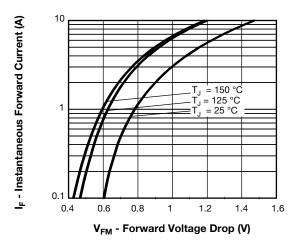


Fig. 1 - Maximum Forward Voltage Drop Characteristics

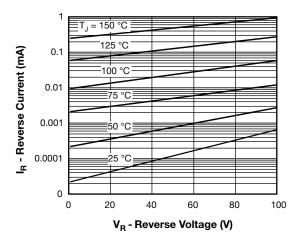


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

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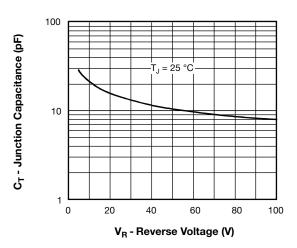


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

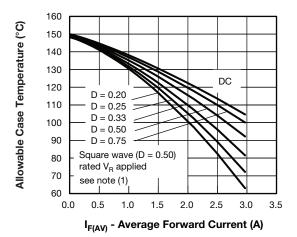


Fig. 4 - Maximum Average Forward Current vs.
Allowable Lead Temperature

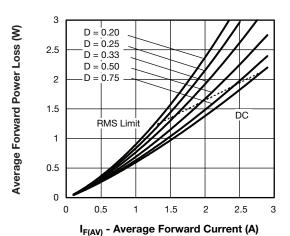


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

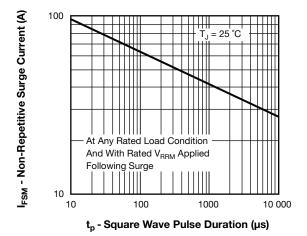


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

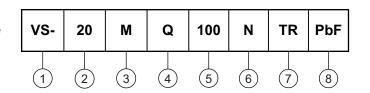
#### Note

 $^{(1)}$  Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$  Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} =$  inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1} = 80~\%$  rated  $V_R$ 



### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating

3 - M = SMA

4 - Q = Schottky "Q" series

5 - Voltage rating (100 = 100 V)

6 - N = new SMA

7 - TR = tape and reel (7500 pieces)

8 - PbF = lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-20MQ100NTRPbF	5AT	7500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?97121			



# **SMA**

## **DIMENSIONS** in inches (millimeters)

### **DO-214AC (SMA)**



### Mounting Pad Layout





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