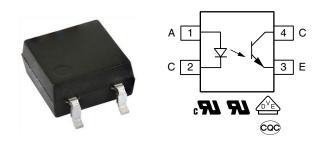


Optocoupler, Phototransistor Output, Low Input Current, SOP-4, Mini-Flat Package



LINKS TO ADDITIONAL RESOURCES







DESCRIPTION

The VOMA618A series has a GaAlAs infrared emitting diode, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 4-pin mini-flat package.

It features a high current transfer ratio at low input current, low coupling capacitance, and high isolation voltage.

The coupling device is designed for signal transmission between two electrically separated circuits, specifically for use in automotive, as well as high reliable industrial applications.

FEATURES

- AEC-Q101 qualified
- High CTR with low input current
- Low power consumption
- SOP-4 low profile package
- High collector emitter voltage, V_{CEO} = 80 V
- Isolation test voltage = 3750 V_{RMS}
- Low coupling capacitance
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS
COMPLIANT
HALOGEN
FREE
GREEN
(5-2008)

APPLICATIONS

- · Galvanic and noise isolation
- Signal transmission
- Hybrid / electric vehicle applications
- · Battery management
- 48 V board net
- · System control

AGENCY APPROVALS

- UL1577
- cUL 1577
- DIN EN 60747-5-5 (VDE 0884-5)
- CQC GB4943.1-2011

ORDERING INFORMATION					
V O M A 6 1 8 A - # X 0 0 1 T SOP-4					
PART NUMBER C			PACKAGE OPTIO	N TAPE AND REEL	≥ 5 mm
AGENCY CERTIFIED / CTR (%)					
PACKAGE	1 mA				
UL, cUL, VDE, CQC	50 to 600	63 to 125	100 to 200	160 to 320	130 to 260
SOP-4	VOMA618A-X001T	VOMA618A-2X001T	VOMA618A-3X001T	VOMA618A-4X001T	VOMA618A-8X001T

Note

• Additional options may be possible, please contact sales office

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V _R	5	V	
Power dissipation		P _{diss}	30	mW	
Forward current		l _F	20	mA	
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.5	Α	
Junction temperature		Tj	125	°C	
OUTPUT					
Collector emitter voltage		V _{CEO}	80	V	
Emitter collector voltage		V _{ECO}	7	V	
Collector current		I _C	50	mA	
Power dissipation		P _{diss}	150	mW	
Junction temperature		T _j	125	°C	
COUPLER					
Total power dissipation		P _{tot}	180	mW	
Storage temperature range		T _{stg}	-40 to +150	°C	
Ambient temperature range		T _{amb}	-40 to +110	°C	
Soldering temperature	t = 10 s	T _{sld}	260	°C	

Note

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

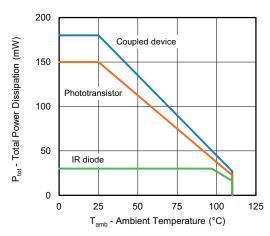


Fig. 1 - Power Dissipation vs. Ambient Temperature

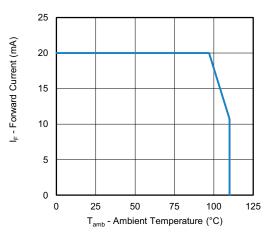


Fig. 2 - Maximum Forward Current vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I _F = 1 mA	V_{F}	-	1.28	1.4	V
Reverse current	V _R = 5 V	I _R	-	-	10	μA
Capacitance	$V_R = 0 V, f = 1 MHz$	Cı	-	40	-	pF
OUTPUT						
Collector emitter leakage current	V _{CE} = 50 V	I _{CEO}	-	1	100	nA
Collector emitter breakdown voltage	$I_C = 100 \mu A$	BV _{CEO}	80	-	-	V
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz	C _{CE}	-	7	-	pF
COUPLER						
Collector emitter saturation voltage	$I_F = 1 \text{ mA}, I_C = 0.25 \text{ mA}$	V _{CEsat}	-	0.12	0.4	V
Cut-off frequency	$I_F = 10$ mA, $V_{CC} = 5$ V, $R_L = 100$ Ω	f _{CTR}	-	155	-	kHz
Coupling capacitance	f = 1 MHz	C _{IO}	-	1.2	-	pF

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I _F = 1 mA, V _{CE} = 5 V	VOMA618A	CTR	50	-	600	%
		VOMA618A-2	CTR	63	-	125	%
I _C /I _F		VOMA618A-3	CTR	100	-	200	%
		VOMA618A-4	CTR	160	-	320	%
		VOMA618A-8	CTR	130	-	260	%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED						
Rise time		t _r	-	1.8	-	μs
Fall time	$I_C = 2 \text{ mA}, V_{CC} = 5 \text{ V},$	t _f	-	1.7	-	μs
Turn-on time	$R_L = 100 \Omega$	t _{on}	-	6.8	-	μs
Turn-off time		t _{off}	-	2.3	-	μs
SATURATED						
Rise time		t _r	-	5.4	-	μs
Fall time	$I_F = 1 \text{ mA}, V_{CC} = 5 \text{ V},$	t _f	-	5.8	-	μs
Turn-on time	$R_L = 1.9 \text{ k}\Omega$	t _{on}	-	10.0	-	μs
Turn-off time		t _{off}	-	6.6	=	μs

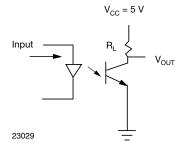


Fig. 3 - Test Circuit for Switching Characteristics

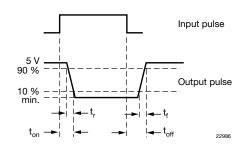


Fig. 4 - Parameter and Limit Definition



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SAFETY AND INSULATION RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification	According to IEC 68 part 1		40 / 110 / 21		
Pollution degree	According to DIN VDE 0109		2		
Comparative tracking index	Insulation group Illa	CTI	175		
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	3750	V_{RMS}	
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	6000	V_{peak}	
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	707	V_{peak}	
	$T_{amb} = 25 ^{\circ}C, V_{IO} = 500 V$	R _{IO}	≥ 10 ¹²	Ω	
Isolation resistance	$T_{amb} = 100 ^{\circ}\text{C}, V_{IO} = 500 \text{V}$	R _{IO}	≥ 10 ¹¹	Ω	
	$T_{amb} = T_{S}, V_{IO} = 500 \text{ V}$	R _{IO}	≥ 10 ⁹	Ω	
Output safety power		P _{SO}	550	mW	
Input safety current		I _{SI}	180	mA	
Input safety temperature		T _S	175	°C	
Creepage distance			≥ 5	mm	
Clearance distance			≥ 5	mm	

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

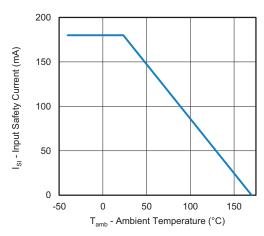


Fig. 5 - Input Safety Current vs. Ambient Temperature

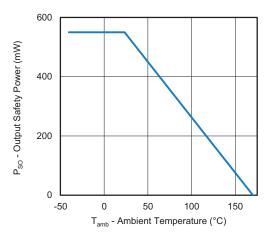


Fig. 6 - Output Safety Power vs. Ambient Temperature

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

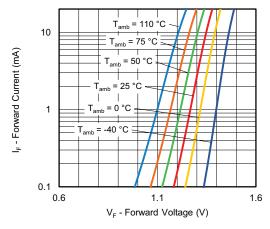


Fig. 7 - Forward Current vs. Forward Voltage

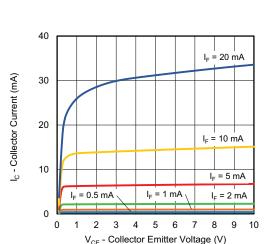


Fig. 8 - Collector Current vs. Collector Emitter Voltage (non-sat.)

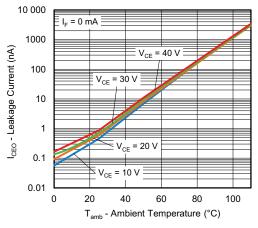


Fig. 9 - Leakage Current vs. Ambient Temperature

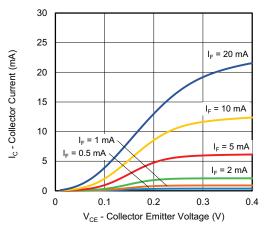


Fig. 10 - Collector Current vs. Collector Emitter Voltage (sat.)

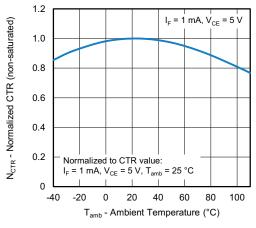


Fig. 11 - Normalized CTR (non-sat.) vs. Ambient Temperature

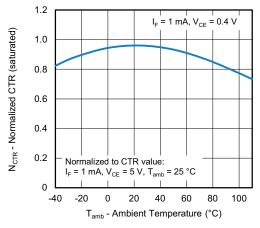


Fig. 12 - Normalized CTR (sat.) vs. Ambient Temperature

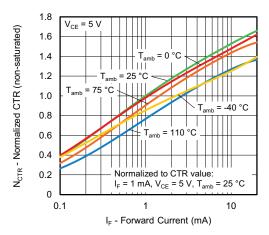


Fig. 13 - Normalized CTR (non-sat.) vs. Forward Current

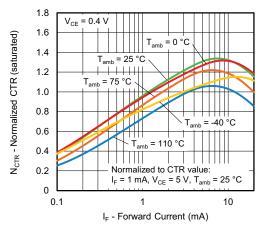


Fig. 14 - Normalized CTR (sat.) vs. Forward Current

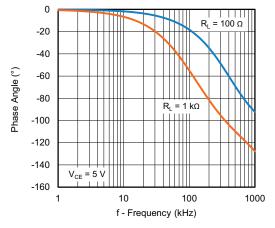


Fig. 15 - Phase Angle vs. Frequency

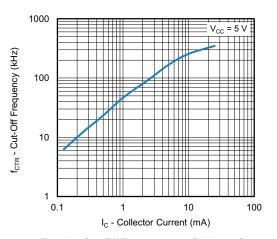


Fig. 16 - Cut-Off Frequency vs. Collector Current

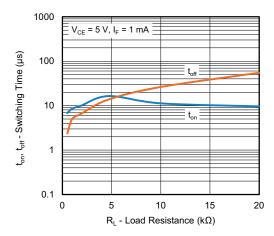


Fig. 17 - Switching Time vs. Load Resistance

PACKAGE DIMENSIONS (in millimeters)

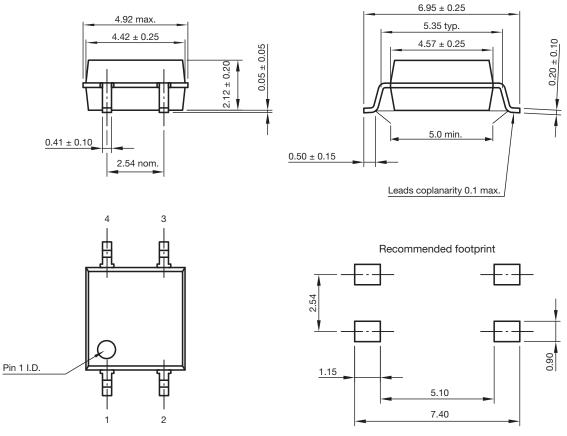


Fig. 18 - Package Drawing

PACKAGE MARKING

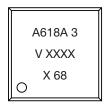


Fig. 19 - Example of VOMA618A-3X001T

Note

- Tape and reel suffix (T) is not part of the package marking
- XXXX = LMC (lot marking code)
- Option 1 is reflected with letter "X"

PACKAGING INFORMATION (in millimeters)

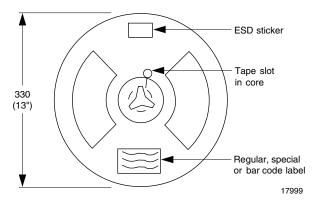


Fig. 20 - Tape and Reel Shipping Medium (EIA-481, revision A, and IEC 60286)

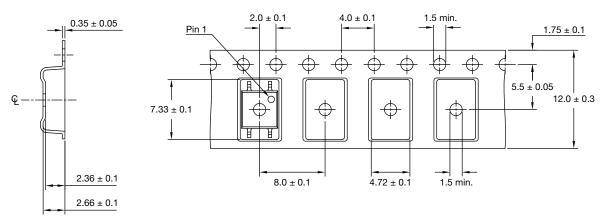


Fig. 21 - Tape and Reel Packing

Note

• Cummulative tolerance of 10 spocket holes is 0.20 mm

TAPE AND REEL PACKING				
TYPE	UNITS/REEL			
SOP-4	2000			



SOLDER PROFILES

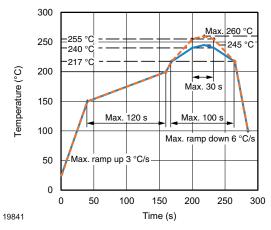


Fig. 22 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 168 h

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 3, according to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-033D.



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