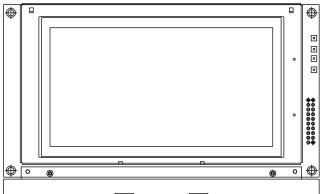
COMPLIANT



# 240 x 128 Graphic LCD



#### **FEATURES**

• Type: graphic

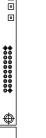
• Display format: 240 x 128 dots

• Built-in controller: RA6963

Duty cycle: 1/128Built-in N.V. (option)

• Material categorization: for definitions of compliance

please see www.vishay.com/doc?99912



ABSOLUTE MAXIMUM RATINGS						
ITEM	SYMBOL	STANDARD VALUE			UNIT	
I I EIVI	STIVIBUL	MIN.	MIN. TYP. MAX.		CIVIT	
Power supply	$V_{DD}$ to $V_{SS}$	4.75	5.0	5.25	V	
Input voltage	VI	-0.3	-	$V_{DD}$	] v	

#### Note

•  $V_{SS} = 0 \text{ V}, V_{DD} = 5.0 \text{ V}$ 

MECHANICAL DATA					
ITEM	STANDARD VALUE	UNIT			
Module dimension	170.0 x 93.4				
Viewing area	132.0 x 74.0				
Dot size	0.47 x 0.47	mm			
Dot pitch	0.50 x 0.50	1111111			
Mounting hole	162.0 x 85.0				
Character size	n/a				

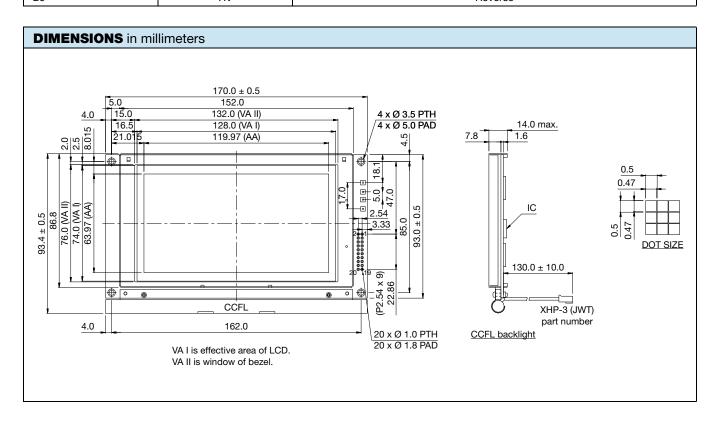
ELECTRICAL CHARACTERISTICS							
ITEM	SYMBOL	CONDITION	STA	STANDARD VALUE			
	STIVIBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Input voltage	$V_{DD}$	L level	0.7 V <sub>DD</sub>	'n	$V_{DD}$	· V	
	V <sub>IO</sub>	H level	-	-	0.3 V <sub>DD</sub>	V	
Supply current	I <sub>DD</sub>	$V_{DD} = +5 \text{ V}$	-	23	-	mA	
D d. d.l. O		-20 °C	-	=.	-		
Recommended LC Driving		0 °C	19.1	19.5	20.1	V	
Voltage for Normal	$V_{DD}$ to $V_0$	25 °C	18.1	18.5	19.1		
Temperature Version Module		50 °C	17.1	17.5	18.1		
		70 °C	-	-	-		
LED Forward Voltage	V <sub>F</sub>	25 °C	-	=	-	V	
LED Forward Current	I <sub>F</sub>	25 °C	-	=	-	mA	
CCFL Forward Voltage	V <sub>F</sub>	25 °C	-	325	580	V <sub>RMS</sub>	
CCFL Forward Current	I <sub>F</sub>	25 °C	-	=	5.0	mA <sub>RMS</sub>	
EL Power Supply Current	I <sub>EL</sub>	V <sub>EL</sub> = 110 V <sub>AC</sub> , 400 Hz	-	1	5.0	mA	

OPTIONS									
PROCESS COLOR						BACKLIGHT			
TN	STN GRAY	STN YELLOW	STN BLUE	FSTN B&W	STN COLOR	NONE	LED	EL	CCFL
-	x	x	x	x	-	x	x	x	x

For detailed information, please see the "Product Numbering System" document.



INTERFACE PII	INTERFACE PIN FUNCTION				
PIN NO. SYMBOL		FUNCTION			
1	F <sub>GND</sub>	Frame GEN (connected to bezel)			
2	V <sub>SS</sub>	Ground			
3	$V_{DD}$	Power supply for logic circuit			
4	V <sub>0</sub>	Contrast adjustment			
5	WR	Data write			
6	DR	Data read			
7	CE	Chip enable			
8	C/D	Code / data			
9	NC / V <sub>EE</sub>	No connection / negative voltage output			
10	RST	Controller reset			
11	DB0	Data bus line			
12	DB1	Data bus line			
13	DB2	Data bus line			
14	DB3	Data bus line			
15	DB4	Data bus line			
16	DB5	Data bus line			
17	DB6	Data bus line			
18	DB7	Data bus line			
19	FS	Font selection: FS = "H", 6 x 8 character font, FS = "L", 8 x 8 character font			
20	RV	Reverse			





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Vishay

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