

COMPLIANT

HALOGEN FREE

GREEN

(5-2008)

### Standard Mini SMD LED



### **DESCRIPTION**

The new MiniLED series has been designed in a small white SMT package. The feature of the device is the very small package 2.3 mm x 1.3 mm x 1.4 mm. The MiniLED is an obvious solution for small-scale, high-power products that are expected to work reliably in an arduous environment. This is often the case in automotive and industrial application of course.

### PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD MiniLED
Product series: standard
Angle of half intensity: ± 60°

### **FEATURES**

- SMD LEDs with exceptional brightness
- · Luminous intensity categorized
- Compatible with automatic placement equipment
- EIA and ICE standard package
- IR reflow soldering
- Available in 8 mm tape
- · Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- ESD-withstand voltage: up to 2 kV (HBM) according to JESD22-A114-B
- Luminous intensity ratio in one packaging unit  $I_{Vmax.}/I_{Vmin.} \le 1.6$
- Preconditioning according to JEDEC® level 2a
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **APPLICATIONS**

- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- Indicator and backlight in office equipment
- · Flat backlight for LCDs, switches, and symbols
- General use

PARTS TABLE														
PART	COLOR		JMINO TENSI (mcd)		at I <sub>F</sub> (mA)	WAY	/ELEN( (nm)	GTH	at I <sub>F</sub> (mA)		ORWAF OLTAG (V)	iΕ	at I <sub>F</sub> (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMO2100-GS08	Soft orange	3.55	7.0	-	10	598	605	611	10	-	2.1	3	20	GaAsP on GaP
VLMY2100-GS08	Yellow	3.55	8.5	-	10	581	588	594	10	-	2.2	3	20	GaAsP on GaP



ABSOLUTE MAXIMUM RATII VLMO21, VLMY21	NGS (T <sub>amb</sub> = 25 °C, unless otherwise	se specified)		
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage (1)		V <sub>R</sub>	6	V
DC forward current	T <sub>amb</sub> ≤ 74 °C	I <sub>F</sub>	20	mA
Surge forward current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	0.34	Α
Power dissipation		P <sub>V</sub>	56	mW
Junction temperature		Tj	+100	°C
Operating temperature range		T <sub>amb</sub>	-40 to +100	°C
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C
Soldering temperature	t ≤ 5 s	T <sub>sd</sub>	260	°C
Thermal resistance junction to ambient	Mounted on PC board (pad size > 5 mm <sup>2</sup> )	R <sub>thJA</sub>	480	K/W

#### Note

<sup>(1)</sup> Driving the LED in reverse direction is suitable for a short term application

OPTICAL AND ELECT VLMO21, SOFT OR		RISTICS (T <sub>am</sub>	<sub>b</sub> = 25 °C, u	nless othe	rwise spe	cified)	
PARAMETER	TEST CONDITION	PARTS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity (1)	I <sub>F</sub> = 10 mA	VLMO2100	I <sub>V</sub>	3.55	7.0	-	mcd
Dominant wavelength	I <sub>F</sub> = 10 mA		$\lambda_d$	598	605	611	nm
Peak wavelength	I <sub>F</sub> = 10 mA		$\lambda_{p}$	-	605	-	nm
Angle of half intensity	I <sub>F</sub> = 10 mA		j	-	± 60	-	0
Forward voltage	I <sub>F</sub> = 20 mA		V <sub>F</sub>	-	2.1	3	V
Reverse voltage	I <sub>R</sub> = 10 μA		V <sub>R</sub>	6	15	-	V
Junction capacitance	$V_R = 0 V, f = 1 MHz$		Cj	-	15	-	pF

#### Note

 $<sup>^{(1)}~</sup>$  In one packing unit  $I_{Vmax.}/I_{Vmin.} \leq 1.6$ 

OPTICAL AND ELECT VLMY21, YELLOW	RICAL CHARACTER	RISTICS (T <sub>amb</sub>	= 25 °C, un	less other	wise spe	cified)	
PARAMETER	TEST CONDITION	PARTS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity (1)	I <sub>F</sub> = 10 mA	VLMY2100	I <sub>V</sub>	3.55	8.5	-	mcd
Dominant wavelength	I <sub>F</sub> = 10 mA		$\lambda_{d}$	581	588	594	nm
Peak wavelength	I <sub>F</sub> = 10 mA		$\lambda_{p}$	-	585	-	nm
Angle of half intensity	I <sub>F</sub> = 10 mA		φ	-	± 60	-	0
Forward voltage	I <sub>F</sub> = 20 mA		V <sub>F</sub>	-	2.2	3	V
Reverse voltage	I <sub>R</sub> = 10 μA		V <sub>R</sub>	6	15	-	V
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		C <sub>j</sub>	-	15	-	pF

#### Note

 $<sup>^{(1)}~</sup>$  In one packing unit  $I_{Vmax.}/I_{Vmin.} \leq 1.6$ 



### www.vishay.com

## Vishay Semiconductors

LUMINOUS	LUMINOUS INTENSITY CLASSIFICATION						
GROUP	LIGHT INTENSITY (mcd)						
STANDARD	OPTIONAL	MIN	MAX				
Н	1	2.8	3.55				
П	2	3.55	4.5				
J	1	4.5	5.6				
J	2	5.6	7.1				
K	1	7.1	9.0				
, r	2	9.0	11.2				
1	1	11.2	14.0				
_	2	14.0	18.0				
М	1	18.0	22.4				
IVI	2	22.4	28.0				
N	1	28.0	35.5				
IN	2	35.5	45.0				

 Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable

COLOR	COLOR CLASSIFICATION						
	YEL	LOW	SOFT O	RANGE			
GROUP	DOM. WAVELENGTH (nm)						
	MIN.	MAX.	MIN.	MAX.			
1	581	584	598	601			
2	583	586	600	603			
3	585	588	602	605			
4	587	590	604	607			
5	589	592	606	609			
6	591	594	608	611			

### Note

Wavelengths are tested at a current pulse duration of 25 ms

CROSSING TABLE	
VISHAY	OSRAM
VLMO2100	LOM670
VLMY2100	LYM670

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

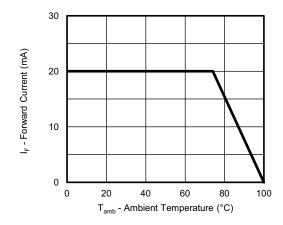


Fig. 1 - Forward Current vs. Ambient Temperature

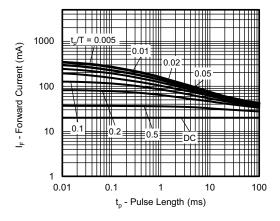


Fig. 2 - Pulse Forward Current vs. Pulse Duration

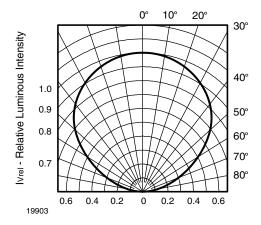


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

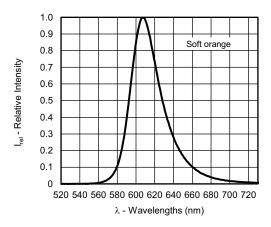


Fig. 4 - Relative Intensity vs. Wavelength

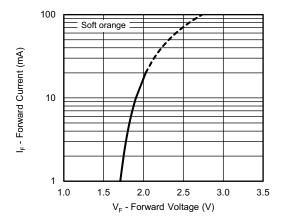


Fig. 5 - Forward Current vs. Forward Voltage

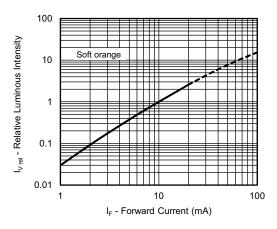


Fig. 6 - Relative Luminous Intensity vs. Forward Current

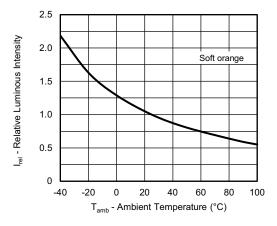


Fig. 7 - Relative Luminous Intensity vs. Ambient Temperature

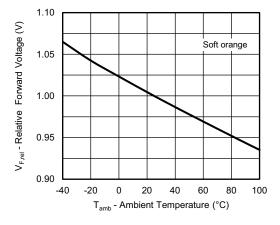


Fig. 8 - Relative Forward Voltage vs. Ambient Temperature



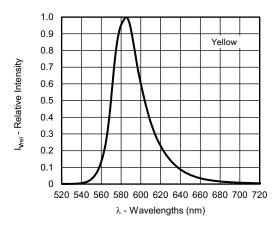


Fig. 9 - Relative Intensity vs. Wavelength

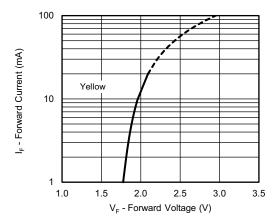


Fig. 10 - Forward Current vs. Forward Voltage

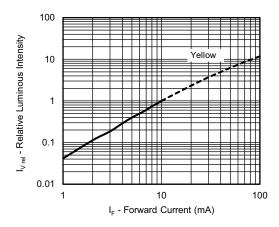


Fig. 11 - Relative Luminous Intensity vs. Forward Current

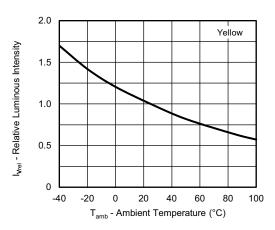


Fig. 12 - Relative Luminous Intensity vs. Ambient Temperature

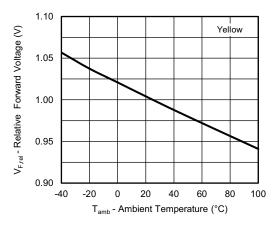


Fig. 13 - Forward Voltage vs. Ambient Temperature

### **SOLDERING PROFILE**

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC level 2a

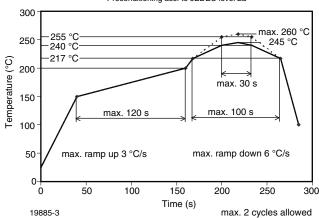
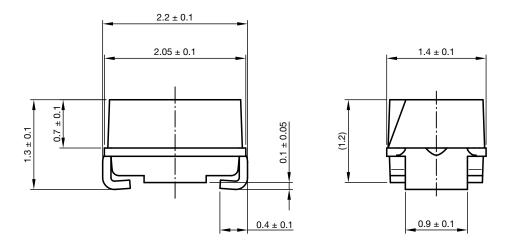
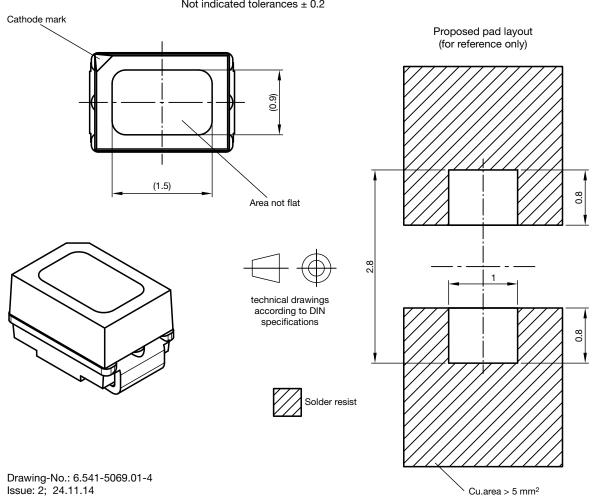


Fig. 14 - Vishay Lead (Pb)-Free Reflow Soldering Profile (according to J-STD-020)

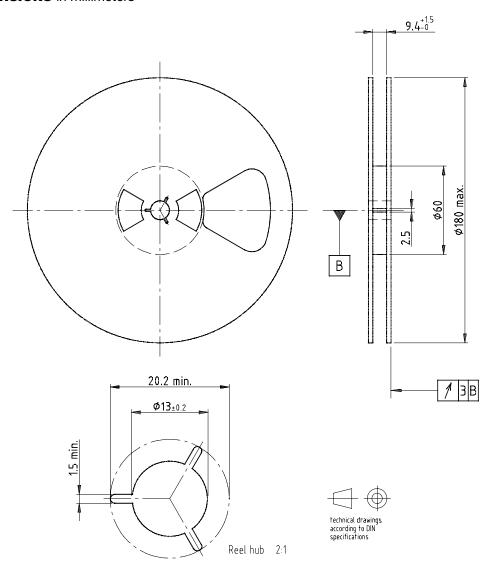
### **PACKAGE DIMENSIONS** in millimeters



Not indicated tolerances ± 0.2



### **REEL DIMENSIONS** in millimeters

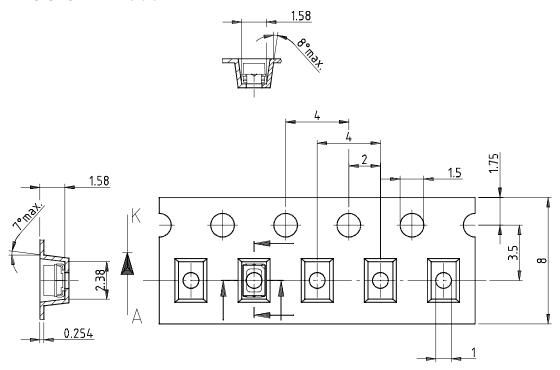


Drawing-No.: 9.800-5051.V5-4

Issue: 1; 25.07.02

16938

### **TAPE DIMENSIONS** in millimeters

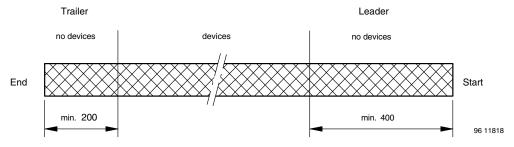


Drawing-No.: 9.700-5266.01-4

Issue: 1; 05.06.02

16939

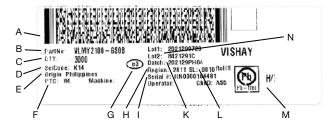
### **LEADER AND TRAILER DIMENSIONS** in millimeters



### Note

• GS08 = 3000 pcs

### **BAR CODE PRODUCT LABEL**



A. 2D barcode

B. Part No: Vishay part number

C. QTY: quantity

D. SelCode: selection bin code

E. Country of origin

F. PTC: production plant code

G. Termination finish

H. Region code

I. Serial#: serial number

K. Batch Number: year, week, country code, plant code

L. SL: storage location

M. Environmental Symbols: RoHS, lead (Pb)-free, halogen

free

N. Lot numbers

### **COVER TAPE PEEL STRENGTH**

According to DIN EN 60286-3 0.1 N to 1.3 N 300 mm/min  $\pm$  10 mm/min 165° to 180° peel angle

### **LABEL**

### Standard bar code labels for finished goods

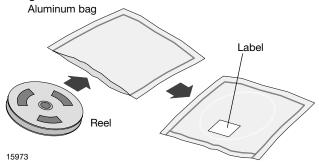
The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

PLAIN WRITTING	ABBREVIATION	LENGTH
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by:	ACC	-
Packed by:	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxxx+	Company logo
LONG BAR CODE TOP	TYPE	LENGTH
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
SHORT BAR CODE BOTTOM	TYPE	LENGTH
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17



### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



### **FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

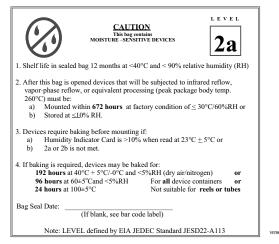
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at  $60~^{\circ}\text{C}$  +  $5~^{\circ}\text{C}$  and  $< 5~^{\circ}\text{RH}$  for all device containers

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

# VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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