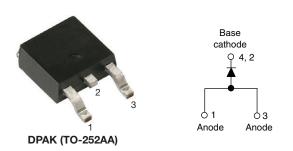


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High Performance Schottky Rectifier, 5.5 A



PRIMARY CHARACTERISTICS					
I _{F(AV)} 5.5 A					
V_{R}	60 V				
V _F at I _F	See Electrical table				
I _{RM}	35 mA at 125 °C				
T_J max.	150 °C				
E _{AS}	7 mJ				
Circuit configuration	Single				
Package	DPAK (TO-252AA)				

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- · Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



DESCRIPTION

The VS-50WQ06FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Rectangular waveform	5.5	A				
V _{RRM}		60	V				
I _{FSM}	t _p = 5 μs sine	320	Α				
V _F	5 A _{pk} , T _J = 125 °C	0.54	V				
TJ	Range	-40 to +150	°C				

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-50WQ06FNHM3	UNITS		
Maximum DC reverse voltage	V_{R}	60	V		
Maximum working peak reverse voltage	V_{RWM}	60	V		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS			
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 132 °C, rectangular waveform		5.5		
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	320	Α	
non-repetitive surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	105		
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}$, $I_{AS} = 1.2 \text{A}$, $L = 10 \text{mH}$		7	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.8	Α	



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		5 A	T _{.1} = 25 °C	0.57		
Maximum forward voltage drop	V (1)	10 A	1J=25 C	0.74	V	
See fig. 1	V _{FM} ⁽¹⁾	5 A	T 105 °C	0.54		
		10 A	T _J = 125 °C	0.68		
Maximum reverse leakage current	. (1)	T _J = 25 °C		3	^	
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	35	- mA	
Threshold voltage	V _{F(TO)}	T T manyimayan		0.35	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum		25.5	mΩ	
Typical junction capacitance	C _T	V _R = 5 V _{DC} (test signal range	360	pF		
Typical series inductance	L _S	Measured lead to lead 5 n	nm from package body	5.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

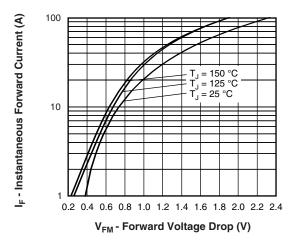
THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to 150	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W	
Approximate weight			0.3	g	
Approximate weight			0.01	OZ.	
Marking device		Case style DPAK	50WQ	D6FNH	

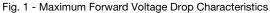
Note

(1)
$$\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$$
 thermal runaway condition for a diode on its own heatsink









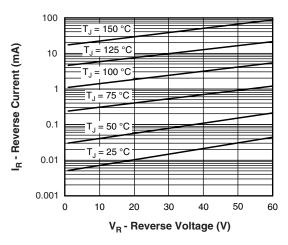


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

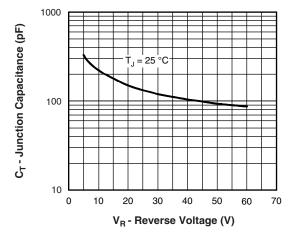


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

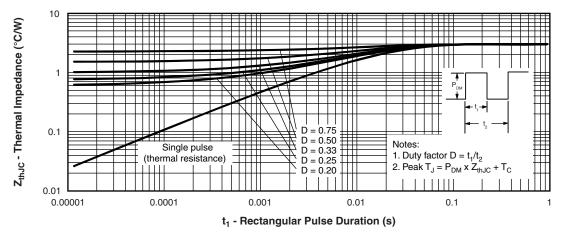


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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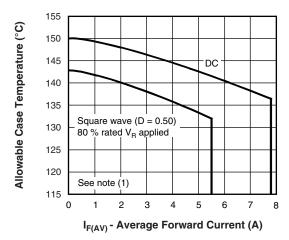


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

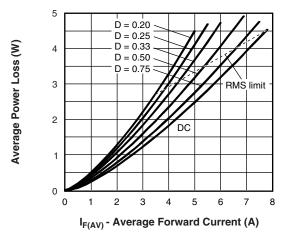


Fig. 6 - Forward Power Loss Characteristics

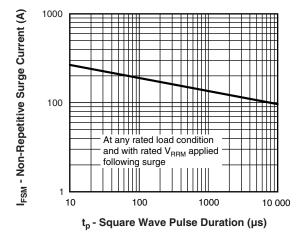


Fig. 7 - Maximum Non-Repetitive Surge Current

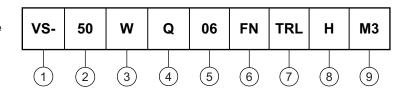
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (5.5 A)

3 - Package identifier:

W = DPAK

4 - Schottky "Q" series

5 - Voltage rating (06 = 60 V)

6 - FN = TO-252AA (DPAK)

7 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

8 - H = AEC-Q101 qualified

9 - Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-50WQ06FNHM3	75	3000	Antistatic plastic tube		
VS-50WQ06FNTRHM3	2000	2000	13" diameter reel		
VS-50WQ06FNTRRHM3	3000	3000	13" diameter reel		
VS-50WQ06FNTRLHM3	3000	3000	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			



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DPAK (TO-252AA)



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	1	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	4.93	-	0.194	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29 BSC		0.090	BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108	REF.	
L2	0.51 BSC		0.020	BSC	
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
	•	•		•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Outline conforms to JEDEC® outline TO-252AA, except for D1 dimension



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