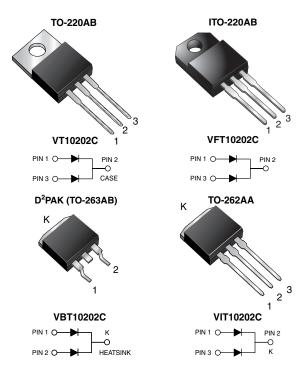
# VT10202C, VFT10202C, VBT10202C, VIT10202C

Vishay General Semiconductor

# Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.58 \text{ V}$  at  $I_F = 2.5 \text{ A}$ 



### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 5 A				
$V_{RRM}$	200 V				
I <sub>FSM</sub>	100 A				
$V_F$ at $I_F = 5$ A $(T_A = 125  ^{\circ}C)$	0.65 V				
T <sub>J</sub> max.	175 °C				
Package	TO-220AB, ITO-220AB, D <sup>2</sup> PAK (TO-263AB), TO-262AA				
Circuit configuration	Common cathode				

#### **FEATURES**

- Trench MOS Schottky technology Gen 2
- · Low forward voltage drop, low power losses

• High efficiency operation

RoHS COMPLIANT HALOGEN FREE

- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB, ITO-220AB, and TO-262AA package)
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

### **MECHANICAL DATA**

Case: TO-220AB, ITO-220AB,  $D^2PAK$  (TO-263AB), and TO-262AA

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs max.

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	VT10202C	VFT10202C	VBT10202C	VIT10202C	UNIT		
Maximum repetitive peak reverse voltage	$V_{RRM}$	200			V			
Maximum average forward rectified current per devi		10				А		
(fig. 1) per dioc	le I <sub>F(AV)</sub>							
Maximum DC reverse voltage	$V_{DC}$	160			V			
Peak forward surge current 8.3 ms single half sine-way superimposed on rated load per diode	re I <sub>FSM</sub>	100		А				
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000			V/µs			
erating junction and storage temperature range T <sub>J</sub> , T <sub>STG</sub>		-40 to +175				°C		



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)									
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT			
Instantaneous forward voltage per diode (1)	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub>	0.74	-	V			
	I <sub>F</sub> = 5.0 A			0.80	0.88				
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.58	-				
	$I_F = 5.0 \text{ A}$			0.65	0.73				
Reverse current (2)	V <sub>R</sub> = 160 V	T <sub>A</sub> = 25 °C	. I <sub>R</sub>	0.2	-	μΑ			
		T <sub>A</sub> = 125 °C		0.4	-	mA			
	V <sub>R</sub> = 200 V	T <sub>A</sub> = 25 °C		-	150	μΑ			
		T <sub>A</sub> = 125 °C		1.0	5	mA			

#### Notes

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width  $\leq 5 \text{ ms}$ 

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER		SYMBOL	VT10202C	VFT10202C	VBT10202C	VIT10202C	UNIT	
	per diode	$R_{ heta JC}$	3.4	6.8	3.4	3.4	°C/W	
Typical thermal resistance	per device	$R_{ heta JC}$	2.2	4.4	2.2	2.2		
	per device	R <sub>0JA</sub> (1)(2)	52	60	52	52		

#### **Notes**

(1) The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>θJA</sub>

(2) Free air, without heatsink

ORDERING INFORMATION (Example)								
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
TO-220AB	VT10202C-M3/4W	1.88	4W	50/tube	Tube			
D <sup>2</sup> PAK (TO-263AB)	VBT10202C-M3/4W	1.37	4W	50/tube	Tube			
D <sup>2</sup> PAK (TO-263AB)	VBT10202C-M3/8W	1.37	8W	800/reel	Tape and reel			
TO-262AA	VIT10202C-M3/4W	1.44	4W	50/tube	Tube			
ITO-220AB	VFT10202C-M3/4W	1.72	4W	50/tube	Tube			

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### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

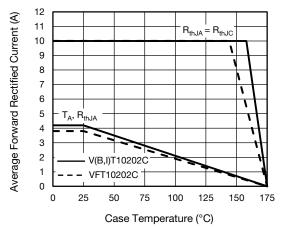


Fig. 1 - Maximum Forward Current Derating Curve (D = Duty Cycle = 0.5)

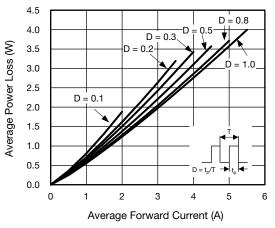


Fig. 2 - Forward Power Loss Characteristics Per Diode

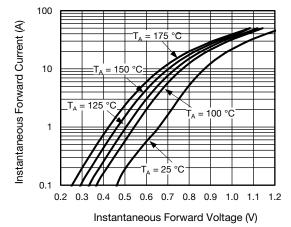


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

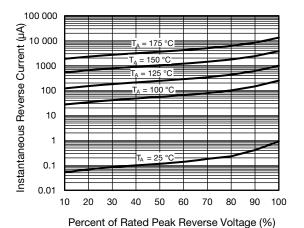


Fig. 4 - Typical Reverse Characteristics Per Diode

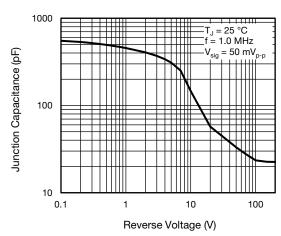


Fig. 5 - Typical Junction Capacitance Per Diode

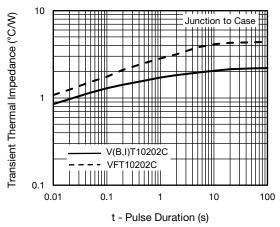


Fig. 6 - Typical Transient Thermal Impedance Per Device

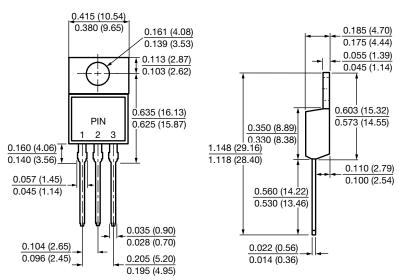


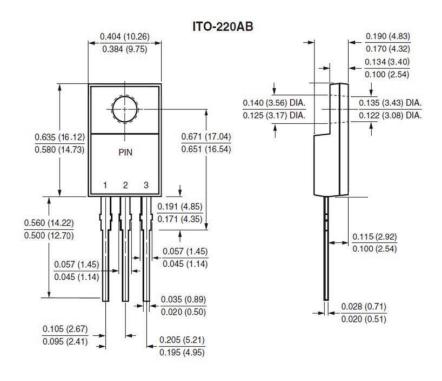
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

### TO-220AB

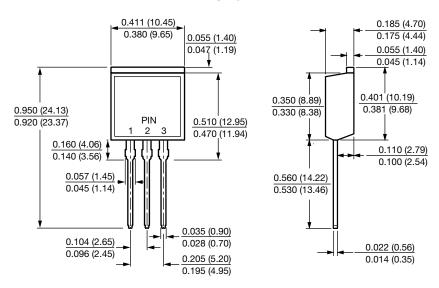




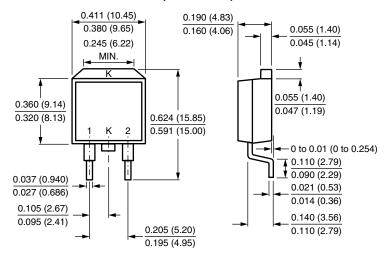


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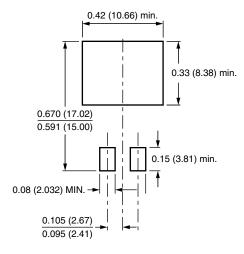
#### **TO-262AA**



### D<sup>2</sup>PAK (TO-263AB)



### **Mounting Pad Layout**





## **Legal Disclaimer Notice**

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