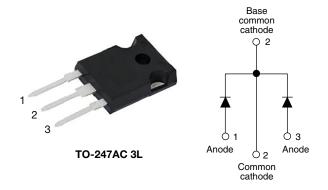


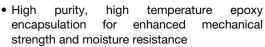
High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V _R	80 V, 100 V				
V _F at I _F	0.61 V				
I _{RM} max.	15 mA at 125 °C				
T _J max.	175 °C				
E _{AS}	11.25 mJ				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-40CPQ... center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	40	А			
V_{RRM}		80/100	V			
I _{FSM}	t _p = 5 μs sine	2950	А			
V _F	20 A _{pk} , T _J = 125 °C (per leg)	0.61	V			
TJ		-55 to +175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-40CPQ080-N3	VS-40CPQ100-N3	UNITS		
Maximum DC reverse voltage	V_R	80	100	W		
Maximum working peak reverse voltage	V_{RWM}	80	100	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 145 °C,	40				
Maximum peak one cycle non-repetitive surge current per leg	_	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	2950	Α		
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	300			
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 5.6 \text{mH}$		11.25	mJ		
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.75	А		



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
		20 A	T _{.1} = 25 °C	0.77	V	
Maximum forward voltage drop per leg	V (1)	40 A	1j=25 C	0.91		
See fig. 1	V _{FM} ⁽¹⁾	20 A	T 105 °C	0.61		
		40 A	T _J = 125 °C	0.75		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1.25	mA	
See fig. 2		T _J = 125 °C	v _R = nateu v _R	15	IIIA	
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25		600	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and s temperature range	torage	T _J , T _{Stg}		-55 to 175	°C	
Maximum thermal resista junction to case per leg	ance,	В	DC operation See fig. 4	1.25		
Maximum thermal resistance, junction to case per package		- R _{thJC}	DC operation	peration 0.63		
Typical thermal resistance case to heatsink	e,	R _{thCS}	Mounting surface, smooth and greased	0.24		
Annyayinasta waisht				6	g	
Approximate weight				0.21	OZ.	
minimum			Non-lubricated threads	6 (5)	kgf ⋅ cm	
Mounting torque	maximum		Non-jublicated tilleads	12 (10)	(lbf · in)	
Marking device			Coop et de TO 247AC 21	40CP	Q080	
			Case style TO-247AC 3L	40CP	40CPQ100	

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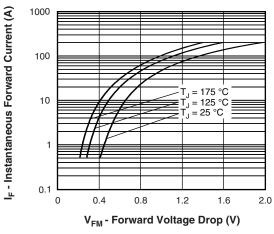


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

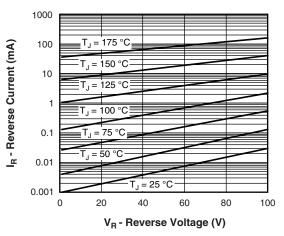


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

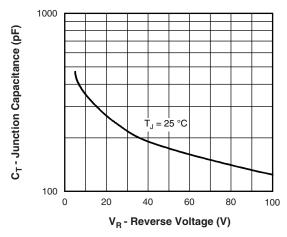


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

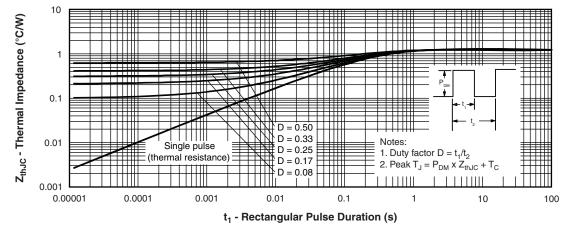


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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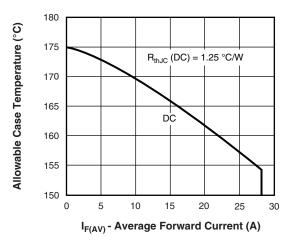


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

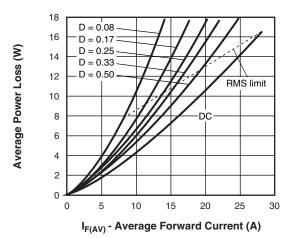


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

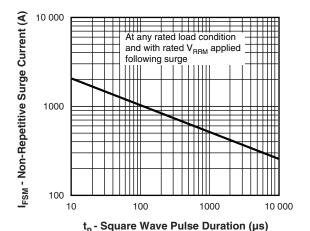


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

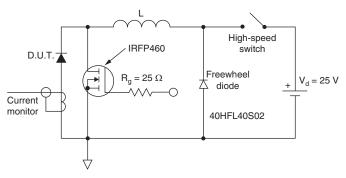
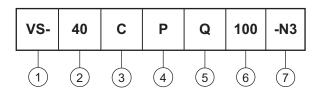


Fig. 8 - Unclamped Inductive Test Circuit

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 = 40 A)

3 - Circuit configuration:

C = common cathode

Environmental digit

4 - Package:

P = TO-247

5 - Schottky "Q" series

080 = 80 V 100 = 100 V

6 - Voltage code

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-40CPQ080-N3	25	500	Antistatic plastic tube			
VS-40CPQ100-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96138			
Part marking information	www.vishay.com/doc?95007			



TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS		HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.35	0.020	0.053	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØK	0.2	254	0.0)10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	7.39	-	0.291	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51	BSC	0.217	BSC	
	·		·	·	·

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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Vishay

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