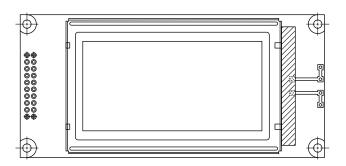


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# 128 x 64 Graphic LCD



#### **FEATURES**

• Type: graphic

• Display format: 128 x 64 dots

• Built-in controller: NT7107, NT7108

Duty cycle: 1/64+5 V power supply

• N.V. built-in

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>



COMPLIANT

MECHANICAL DATA					
ITEM	STANDARD VALUE	UNIT			
Module dimension	113.0 x 53.0				
Viewing area	72.0 x 40.0				
Dot size	0.48 x 0.48	mm			
Dot pitch	0.52 x 0.52	mm			
Mounting hole	108.0 x 46.0				
Character size	n/a				

ABSOLUTE MAXIMUM RATINGS						
ITEM	SYMBOL	STAN	UNIT			
I I EIVI	STIVIBUL	MIN.	TYP.	MAX.	UNIT	
Power supply	V <sub>DD</sub> to V <sub>SS</sub>	4.75	5.0	5.25	V	
Input voltage	$V_{l}$	- 0.3	1	$V_{DD}$	V	

#### Note

•  $V_{SS} = 0 \text{ V}, V_{DD} = 5.0 \text{ V}$ 

ELECTRICAL CHARACTERISTICS							
ITEM	SYMBOL	CONDITION	STANDARD VALUE			UNIT	
IIEM	STIVIDUL	CONDITION	MIN.	TYP.	MAX.	UNII	
Input voltage	$V_{DD}$	L level	0.7 V <sub>DD</sub>	-	$V_{DD}$	V	
Input voltage	V <sub>IO</sub>	H level	0	-	0.3 V <sub>DD</sub>	V	
Supply current	I <sub>DD</sub>	$V_{DD} = +5 \text{ V}$	-	2.5	7.5	mA	
	V <sub>DD</sub> to V <sub>0</sub>	-20 °C	9.9	10.4	10.9		
Recommended LC driving voltage for normal temperature version module		0 °C	9.7	10.2	10.7		
		25 °C	8.9	9.4	9.9	V	
		50 °C	8.6	9.1	9.6		
		70 °C	8.4	8.9	9.4		
LED forward voltage	VF	25 °C	-	4.2	4.6	V	
LED forward current - array		05.00	-	330	660	A	
LED forward current - edge	- I <sub>F</sub>	25 °C	-	120	240	mA	
EL power supply current	I <sub>EL</sub>	V <sub>EL</sub> = 110 V <sub>AC</sub> , 400 Hz	-	-	5.0	mA	

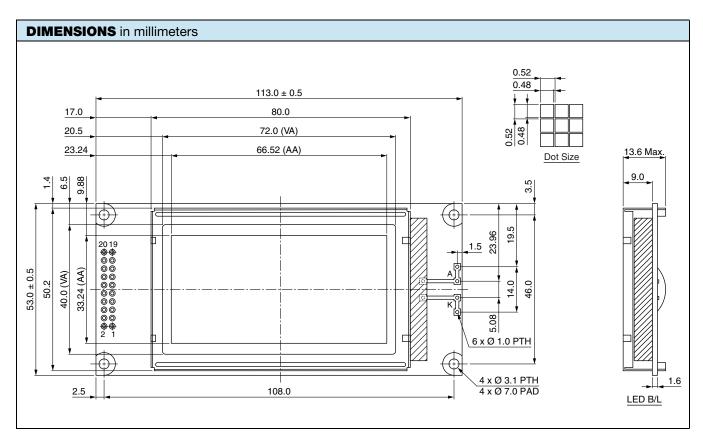
OPTIONS	OPTIONS								
	PROCESS COLOR					BACKLIGHT			
TN	STN GRAY	STN YELLOW	STN BLUE	FSTN B&W	STN COLOR	NONE	LED	EL	CCFL
-	х	х	х	х	-	х	х	х	-

For detailed information, please see the "Product Numbering System" document.



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INTERFACE PIN FUNCTION						
PIN NO.	SYMBOL	FUNCTION				
1	V <sub>SS</sub>	Ground				
2	$V_{DD}$	Power supply for logic				
3	V <sub>0</sub>	Operating voltage LCD driving				
4	D/I	Date / instruction				
5	R/W	H / L read / write signal				
6	E	H → L enable signal				
7	DB0	Data bus line				
8	DB1	Data bus line				
9	DB2	Data bus line				
10	DB3	Data bus line				
11	DB4	Data bus line				
12	DB5	Data bus line				
13	DB6	Data bus line				
14	DB7	Data bus line				
15	CS1	H → chip 1 enable				
16	CS2	$ extsf{H}  ightarrow  ext{chip 2 enable}$				
17	RES	Reset				
18	V <sub>OUT</sub>	Negative voltage output				
19	A	Power supply for backlight				
20	К	Power supply for backlight				





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