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\*May 22, 2023

\*DD Vishay Semiconductor Italiana SPA

\*File Name: VS-E5TX2112.txt

\*This document is intended as a SPICE modeling guideline and does not

\*constitute a commercial product datasheet. Designers should refer to the

\*appropriate datasheet of the same number for guaranteed specification

\*limits.

\*Model fit device at 25°C 125°C

\*DEVICE=VS-E5TX2112,D

.MODEL E5TX2112 D

+ IS=704E-6

+ N=6.081

+ RS=12.4E-3

+ IKF=.6268

+ CJO=144.58E-12

+ M=.3990

+ VJ=.3905

+ ISR=10.010E-21

+ NR=4.9950

+ BV=1.470E3

+ IBV=.0259

+ TT=29.5E-9

+ XTI=30

+ Tikf=0.048

+ Trs1=0.0005

\*May 22, 2023

\*DD Vishay Semiconductor Italiana SPA

\*File Name: Zth-VS-E5TX2112.txt

\*This document is intended as a SPICE modeling guideline and does not

\*constitute a commercial product datasheet. Designers should refer to the

\*appropriate datasheet of the same number for guaranteed specification

\*limits.

\*Thermal model

.SUBCKT ZTH-VS-E5TX2112 Tj Ta

R THERM1	Tj	1	1.749e-02
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R THERM2	1	2	6.905e-02
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R THERM3	2	3	4.065e-01
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R THERM4	3	Ta	8.879e-01
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C THERM1	Tj	1	9.753e-05
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C THERM2	1	2	1.195e-03
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C THERM3	2	3	4.785e-03
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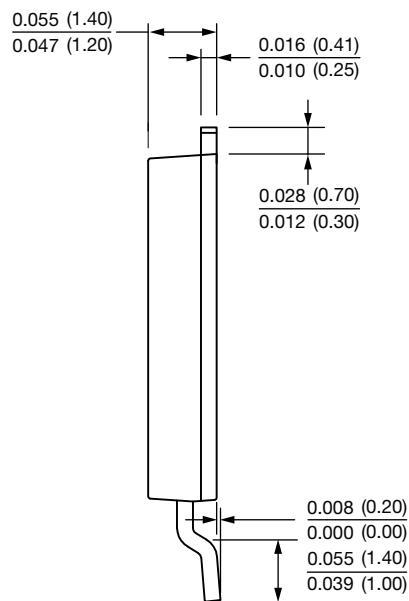
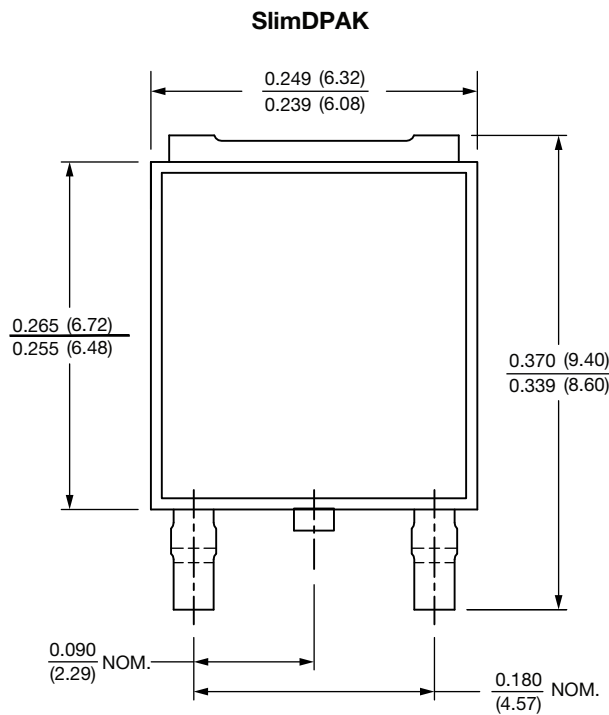
C THERM4	3	Ta	2.426e-02
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.ENDS ZTH-VS-E5TX2112

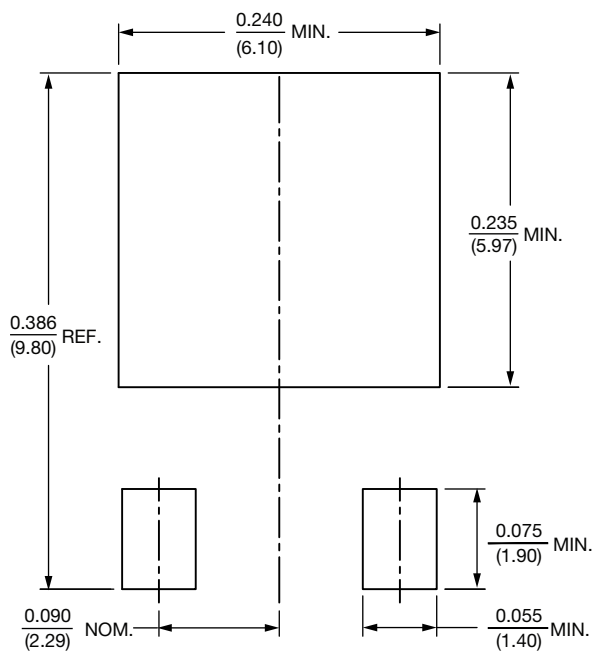
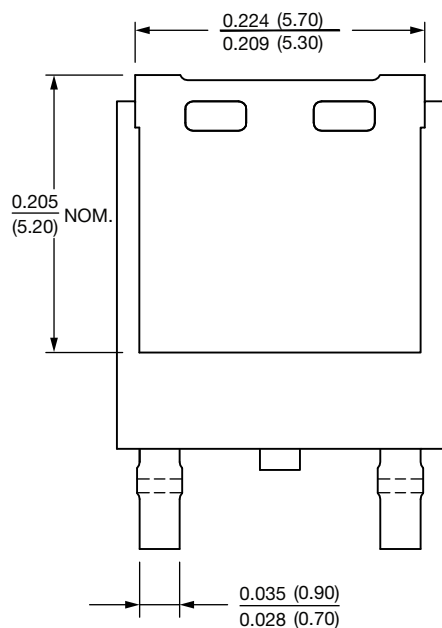


## SlimDPAK

**DIMENSIONS** in inches (millimeters)



### Mounting Pad Layout





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