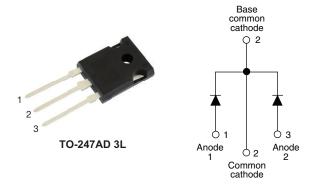


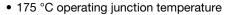
Hyperfast Soft Recovery Diode, 2 x 15 A FRED Pt[®] Gen 4

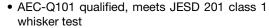


PRODUCT SUMMARY					
Package	TO-247AD 3L				
I _{F(AV)}	2 x 15 A				
V _R	600 V				
V _F at I _F	1.28 V				
t _{rr} typ.	See Recovery table				
T _J max.	175 °C				
Diode variation	Common cathode				

FEATURES

- Gen 4 FRED Pt® technology
- Low I_{RRM} and reverse recovery charge
- · Very low forward voltage drop
- Polyimide passivated chip for high reliability standard











ROHS COMPLIANT HALOGEN FREE

DESCRIPTION

Gen 4 Fred Pt technology, state of the art, ultralow V_F , soft switching optimized for Discontinuous (Critical) Mode (DCM) and IGBT F/W diode.

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Peak repetitive reverse voltage	V_{RRM}		600	V			
Average rectified forward current	I _{F(AV)}	T _C = 142 °C	15	^			
Non-repetitive peak surge current, per leg	I _{FSM}	$T_C = 25$ °C, $t_p = 8.3$ ms half sine wave	200	A			
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	$I_R = 100 \mu A$	600	-	-			
		I _F = 15 A	-	1.6	1.9	V		
	V _F	I _F = 30 A	-	1.87	-			
Forward voltage		I _F = 15 A, T _J = 125 °C	-	1.35	-			
		I _F = 30 A, T _J = 125 °C	-	1.67	-			
		I _F = 15 A, T _J = 150 °C	-	1.28	1.52			
		$I_F = 30 \text{ A}, T_J = 150 ^{\circ}\text{C}$	-	1.61	-			
Poverne legização eurrent	I _R	$V_R = V_R$ rated	-	-	15			
Reverse leakage current		$T_J = 125$ °C, $V_R = V_R$ rated	-	-	500	μA		
Junction capacitance	C _T	V _R = 600 V	_	16	-	pF		



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	MBOL TEST CONDITIONS			TYP.	MAX.	UNITS	
Reverse recovery time		T _J = 25 °C		-	50	-	20	
	t _{rr}	T _J = 125 °C	$I_F = 15 \text{ A}$ $dI_F/dt = 1000 \text{ A/}\mu\text{s}$ $V_R = 400 \text{ V}$	-	70	-	ns	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	8.5	-	Α	
		T _J = 125 °C		-	16	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	250	-	nC	
		T _J = 125 °C		-	600	-	IIC	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Thermal resistance, junction to case	R _{thJC}		-	-	1.4	°C/W		
Thermal resistance, case to heat sink	R _{thCS}		-	0.4	-			
Weight			-	6.0	-	g		
vveignt			-	0.21	-	oz.		
Mounting torque			6.0	_	12	kgf · cm		
Modifiling torque			(5)	_	(10)	(lbf · in)		
Marking device		Case style TO-247AD 3L	C4PH3006LH			•		

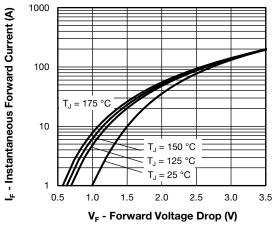


Fig. 1 - Typical Forward Voltage Drop Characteristics

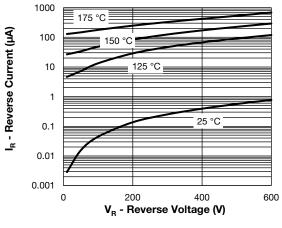


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

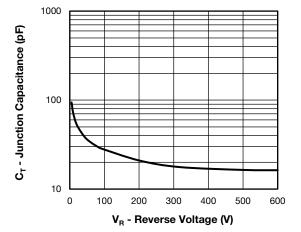


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

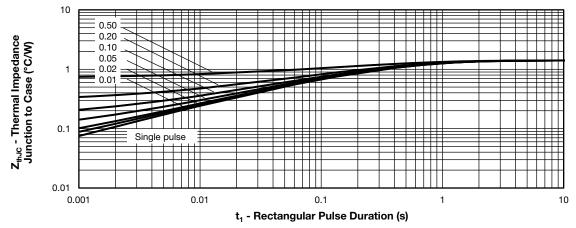


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

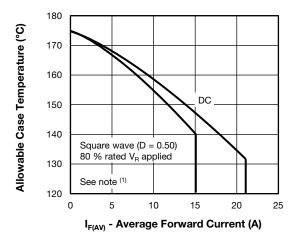


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

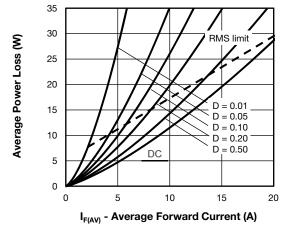


Fig. 6 - Forward Power Loss Characteristics

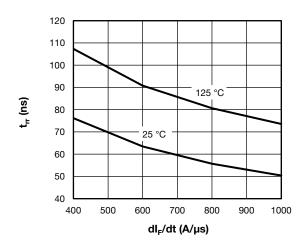


Fig. 7 - Typical Reverse Recovery Time vs. dI_{F}/dt

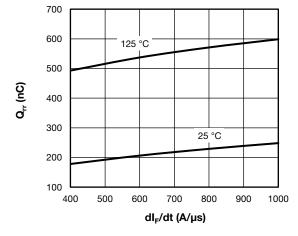


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see Fig. 5)} \\ P_{dREV} = \text{inverse power loss} = V_{R1} \times I_R \ \text{(1 - D); } I_R \ \text{at } V_R = \text{rated } V_R \\ \end{array}$

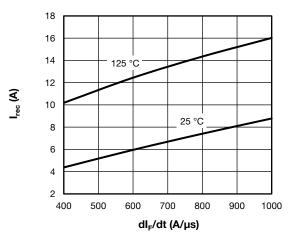


Fig. 9 - Typical Reverse Current vs. dl_F/dt

ORDERING INFORMATION TABLE

Device code VS-C P 30 06 **N3** 4 Н Н (2) (4)(5) (6) (8) (9) (10)(3) Vishay Semiconductors product

Circuit configuration:

C = common cathode

3 - FRED Pt Gen 4

P = TO-247 package

5 - Process type:

H = hyperfast recovery

6 - Current rating (30 = 2 x 15 A)

7 - Voltage rating (06 = 600 V)

8 - Package: L = long lead

9 - H = AEC-Q101 qualified

10 - Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-C4PH3006LHN3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-247AD 3L	www.vishay.com/doc?95626			
Part marking information	TO-247AD 3L	www.vishay.com/doc?95007			



TO-247AD 3L

DIMENSIONS in millimeters and inches



View B

	MILLIMETERS INCHES					
SYMBOL	IVIILLIIV	IETEKS	INC	пЕЭ	NOTES	
01111202	MIN.	MAX.	MIN.	MAX.		
Α	4.65	5.31	0.183	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.50	2.49	0.059	0.098		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.65	2.39	0.065	0.094		
b3	1.65	2.34	0.065	0.092		
b4	2.59	3.43	0.102	0.135		
b5	2.59	3.38	0.102	0.133		
С	0.38	0.89	0.015	0.035		
c1	0.38	0.84	0.015	0.033		
D	19.71	20.70	0.776	0.815	3	
D1	13.08	-	0.515	-	4	

Section C - C, D - D, E - E

CVMPOL	SYMBOL MILLIMETERS INCHES		HES	NOTES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØΚ	0.2	0.254		0.010	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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