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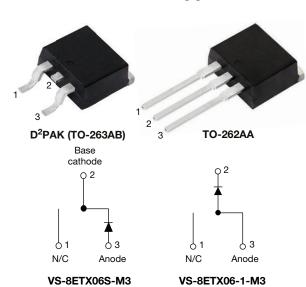
Vishay Semiconductors

COMPLIANT

HALOGEN

FREE

Hyperfast Rectifier, 8 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS							
I _{F(AV)}	8 A						
V_R	600 V						
V _F at I _F	1.4 V						
t _{rr} typ.	15 ns						
T _J max.	175 °C						
Package	D ² PAK (TO-263AB), TO-262AA						
Circuit configuration	Single						

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: D²PAK (TO-263AB), TO-262AA

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Peak repetitive reverse voltage	V_{RRM}		600	V			
Average rectified forward current	I _{F(AV)}	T _C = 143 °C	8				
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	110	Α			
Peak repetitive forward current	I _{FM}		18				
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	600	-	-			
Forward voltage	V _F	I _F = 8 A	-	2.3	3.0	V		
		I _F = 8 A, T _J = 150 °C	-	1.4	1.7			
Developed legisland account	,	V _R = V _R rated	-	0.03	50			
Reverse leakage current	I _R	T _J = 150 °C, V _R = V _R rated	-	35	500	μA		
Junction capacitance	C _T	V _R = 600 V	-	17	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm	-	8.0	-	nH		



DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MAX. U					
Reverse recovery time		$I_F = 1 A, dI_F/dt = 1$	00 A/μs, V _R = 30 V	-	15	19	
	+	$I_F = 8 A, dI_F/dt = 1$	00 A/μs, V _R = 30 V	-	16	24	ne
	t _{rr}	T _J = 25 °C		-	17	-	ns -
		T _J = 125 °C	I _F = 8 A dI _F /dt = 200 A/µs	-	40	1	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2.3	-	A
reak recovery current		T _J = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	4.5	1	
Reverse recovery charge	Q _{rr}	T _J = 25 °C	NH - 000 V	-	20	1	nC
neverse recovery charge		T _J = 125 °C		-	100	-	110
Reverse recovery time	t _{rr}		I _F = 8 A	-	31	-	ns
Peak recovery current	I _{RRM}	T _J = 125 °C	$dI_F/dt = 600 A/\mu s$	-	12	1	Α
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	195	ı	nC

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C		
Thermal resistance, junction-to-case per leg	R _{thJC}		-	1.4	2			
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W		
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-			
Weight			-	2.0	-	g		
Weight			-	0.07	-	OZ.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Marking daying		Case style D ² PAK (TO-263AB)		8ETX	K06S			
Marking device		Case style TO-262AA	8ETX06-1					

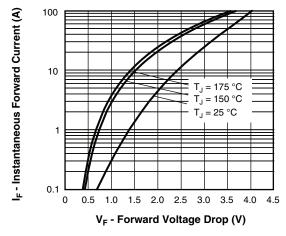


Fig. 1 - Typical Forward Voltage Drop Characteristics

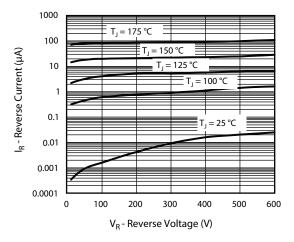


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

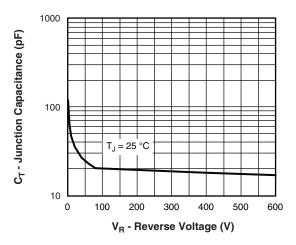


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

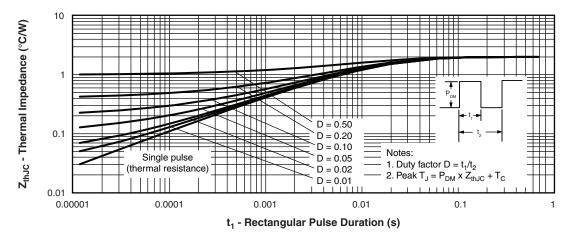


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

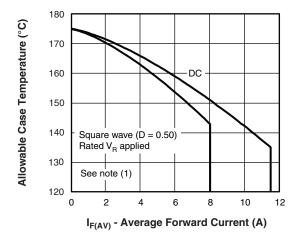


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

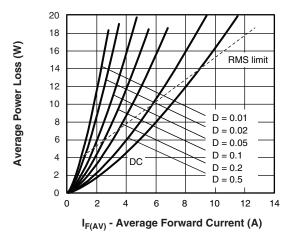
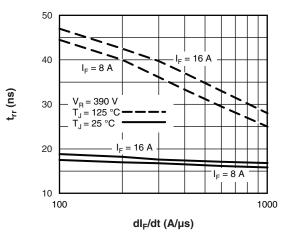


Fig. 6 - Forward Power Loss Characteristics



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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

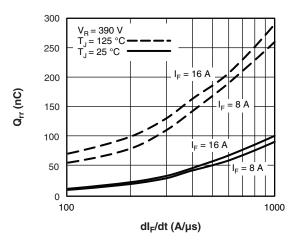
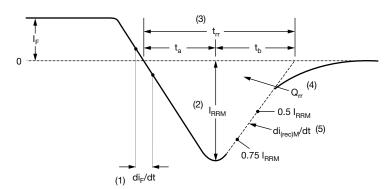


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

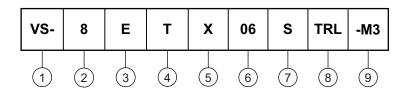
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (8 A)

3 - E = single diode

4 - T = TO-220, D²PAK (TO-263AB)

5 - X = hyperfast rectifier

6 - Voltage rating (06 = 600 V)

- • $S = D^2PAK (TO-263AB)$

• -1 = TO-262AA

None = tube (50 pieces)

• TRL = tape and reel (left oriented, for D²PAK (TO-263AB) package)

• TRR = tape and reel (right oriented, for D²PAK (TO-263AB) package)

9 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION					
VS-8ETX06S-M3	50	Antistatic plastic tubes					
VS-8ETX06STRL-M3	800	13" diameter plastic tape and reel					
VS-8ETX06STRR-M3	800	13" diameter plastic tape and reel					
VS-8ETX06-1-M3	50	Antistatic plastic tubes					

	LINKS TO RELATED DOCUMENTS						
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164					
Differsions	TO-262AA	www.vishay.com/doc?96165					
Ded and the defendable	D ² PAK (TO-263AB)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information		www.vishay.com/doc?96424					
SPICE model		www.vishay.com/doc?95393					



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	ETERS	INC	HES	NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 13-Jul-17 Document Number: 96164



TO-262AA

DIMENSIONS in millimeters and inches

Modified JEDEC® outline TO-262







⊕ 0.010 **M** A**M** B

Lead assignments



Diodes 1. - Anode (two die)/open (one die)

2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

SYMBOL	MILLIM	IETERS	INC	INCHES				
OTHIBOL	MIN.	MAX.	MIN.	MAX.	NOTES			
Α	4.06	4.83	0.160	0.190				
A1	2.03	3.02	0.080	0.119				
b	0.51	0.99	0.020	0.039				
b1	0.51	0.89	0.020	0.035	4			
b2	1.14	1.78	0.045	0.070				
b3	1.14	1.73	0.045	0.068	4			
С	0.38	0.74	0.015	0.029				
c1	0.38	0.58	0.015	0.023	4			
c2	1.14	1.65	0.045	0.065				
D	8.51	9.65	0.335	0.380	2			
D1	6.86	8.00	0.270	0.315	3			
E	9.65	10.67	0.380	0.420	2, 3			
E1	7.90	8.80	0.311	0.346	3			
е	2.54 BSC		0.100	BSC				
L	13.46	14.10	0.530	0.555				
L1	-	1.65	-	0.065	3			
L2	3.56	3.71	0.140	0.146				

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
 (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- Controlling dimension: inches
- Outline conform to JEDEC® TO-262 except A1 (max.), b (min., max.), b1 (min.), b2 (max.), c (min.), c1(min.), c2 (max.), D (min.), E (max.), L1 (max.), L2 (min., max.)



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Vishay

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