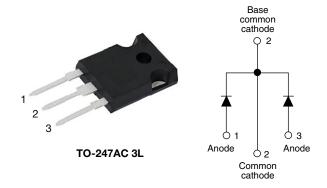


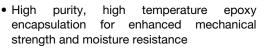
High Performance Schottky Rectifier, 2 x 20 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 20 A				
V_{R}	45 V				
V _F at I _F	0.49 V				
I _{RM} max.	80 mA at 100 °C				
T _J max.	150 °C				
E _{AS}	20 mJ				
Package	TO-247AC 3L				
Circuit configuration	Common cathode				

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- · High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-STPS40L45CW... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	40	Α				
V _{RRM}		45	V				
I _{FSM}	$t_p = 5 \mu s sine$	1240	Α				
V _F	20 A _{pk} , T _J = 125 °C (per leg, typical)	0.42	V				
T _J		-55 to +150	°C				

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-STPS40L45CW-N3	UNITS
Maximum DC reverse voltage	V _R	AE.	
Maximum working peak reverse voltage	V_{RWM}	45	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward per device			FO 0/ duty avalant T 100 °C ventangular varyafava		40		
current, see fig. 5	per leg	I _{F(AV)}	50 % duty cycle at T _C = 122 °C, rectangular waveform		20	Α	
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1240		
			10 ms sine or 6 ms rect. pulse	V _{RRM} applied	350		
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.4 mH		20	mJ	
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3	А	



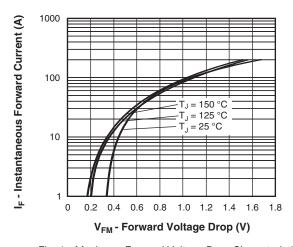
ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDIT	TIONS	TYP.	MAX.	UNITS	
		20 A	T. ₁ = 25 °C	0.48	0.53	V	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	40 A	1j = 25 O	0.61	0.69		
See fig. 1	VFM (*)	20 A	T. ₁ = 125 °C	0.42	0.49		
		40 A	1) = 123 0	0.60	0.70		
Reverse leakage current per leg	I _{RM} (1)	T _J = 25 °C	V _B = Rated V _B	ı	1.5	mA	
See fig. 2		T _J = 100 °C	VR = nateu VR	20	80		
Threshold voltage	V _{F(TO)}	0.27		27	V		
Forward slope resistance	r _t	$T_J = T_J$ maximum		8.	72	mΩ	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	1500	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	-	nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			000	V/µs	

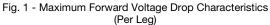
Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to 150	50 °C	
Maximum thermal resistan junction to case per leg	ce,	В	DC operation See fig. 4	1.6		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.8 °C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24		
Annyayimata waight				6	g	
Approximate weight				0.21	OZ.	
minimu				6 (5)	kgf · cm	
Mounting torque	maximu m		Non-lubricated threads	12 (10)	(lbf · in)	
Marking device			Case style TO-247AC 3L	STPS40	L45CW	







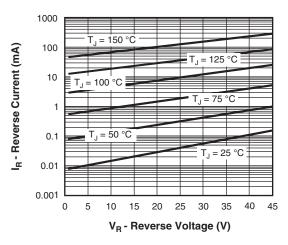


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

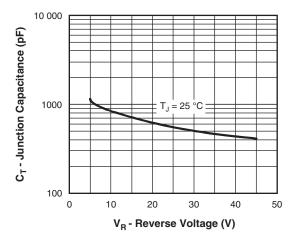


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

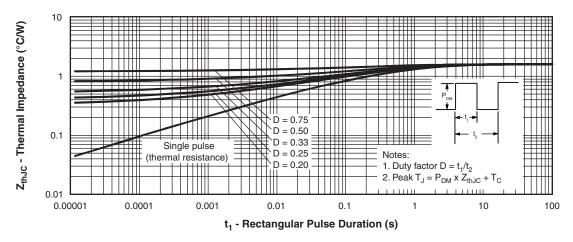


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

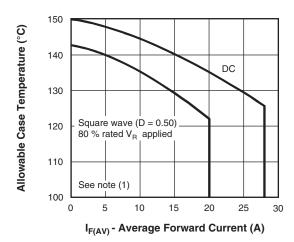


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

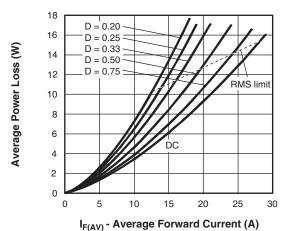


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

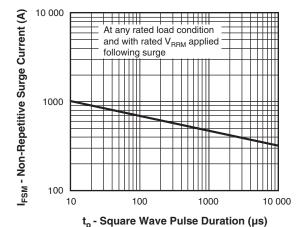


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

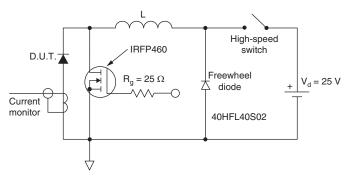


Fig. 8 - Unclamped Inductive Test Circuit

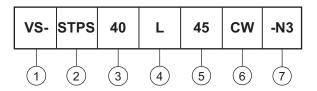
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Schottky STPS series

3 - Current ratings (40 = 40 A)

L = low forward voltage

5 - Voltage code (45 = 45 V)

6 - Package:

CW = TO-247

7 - Environmental digit

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-STPS40L45CW-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?96138			
Part marking information	www.vishay.com/doc?95007			



TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	MILLIMETERS		INCHES		
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.65	5.31	0.183	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.17	1.37	0.046	0.054		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.65	2.39	0.065	0.094		
b3	1.65	2.34	0.065	0.092		
b4	2.59	3.43	0.102	0.135		
b5	2.59	3.38	0.102	0.133		
С	0.38	0.89	0.015	0.035		
c1	0.38	0.84	0.015	0.033		
D	19.71	20.70	0.776	0.815	3	
D1	13.08	-	0.515	-	4	

SYMBOL	MILLIMETERS		INC	INCHES		
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	
D2	0.51	1.35	0.020	0.053		
E	15.29	15.87	0.602	0.625	3	
E1	13.46	-	0.53	-		
е	5.46	BSC	0.215	BSC		
ØK	0.254		0.010			
L	14.20	16.10	0.559	0.634		
L1	3.71	4.29	0.146	0.169		
ØΡ	3.56	3.66	0.14	0.144		
Ø P1	-	7.39	-	0.291		
Q	5.31	5.69	0.209	0.224		
R	4.52	5.49	0.178	0.216		
S	5.51 BSC		0.217	BSC		
	·		·	·	·	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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Vishay

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