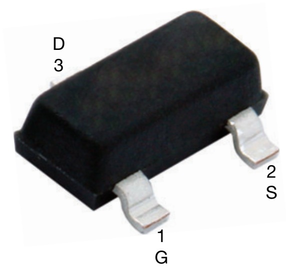
# N-Channel 100 V (D-S) MOSFET

**SOT-23 (TO-236)**




Top View

**Marking code:** H1

## FEATURES

- TrenchFET® Gen IV power MOSFET
- 100 % $R_g$ and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

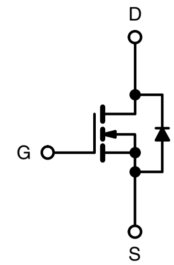
Pb-free

RoHS COMPLIANT

HALOGEN FREE

## APPLICATIONS

- DC/DC converters / boost converters
- Load switch
- LED backlighting in LCD TVs
- Power management for mobile computing




N-Channel MOSFET

| PRODUCT SUMMARY | |
|---|---|
| $V_{DS}$ (V) | 100 |
| $R_{DS(on)}$ max. (Ω) at $V_{GS}$ = 10 V | 0.149 |
| $R_{DS(on)}$ max. (Ω) at $V_{GS}$ = 4.5 V | 0.180 |
| $Q_g$ typ. (nC) | 2.2 |
| $I_D$ (A) [a] | 2.3 |
| Configuration | Single |

| ORDERING INFORMATION | |
|---|---|
| Package | SOT-23 |
| Lead (Pb)-free and halogen-free | Si2392BDS-T1-GE3 |

**ABSOLUTE MAXIMUM RATINGS** ($T_A$ = 25 °C, unless otherwise noted)

| PARAMETER | | SYMBOL | LIMIT | UNIT |
|---|---|---|---|---|
| Drain-source voltage | | $V_{DS}$ | 100 | V |
| Gate-source voltage | | $V_{GS}$ | ± 20 | |
| Continuous drain current ($T_J$ = 150 °C) | $T_C$ = 25 °C | $I_D$ | 2.3 | A |
| | $T_C$ = 70 °C | | 1.8 | |
| | $T_A$ = 25 °C | | 2.0 [b, c] | |
| | $T_A$ = 70 °C | | 1.6 [b, c] | |
| Pulsed drain current (t = 300 μs) | | $I_{DM}$ | 6 | |
| Continuous source-drain diode current | $T_C$ = 25 °C | $I_S$ | 1.4 | |
| | $T_A$ = 25 °C | | 1 [b, c] | |
| Single pulse avalanche current | L = 0.1 mH | $I_{AS}$ | 4 | |
| Single pulse avalanche energy | | $E_{AS}$ | 0.8 | mJ |
| Maximum power dissipation | $T_C$ = 25 °C | $P_D$ | 1.7 | W |
| | $T_C$ = 70 °C | | 1.1 | |
| | $T_A$ = 25 °C | | 1.25 [b, c] | |
| | $T_A$ = 70 °C | | 0.8 [b, c] | |
| Operating junction and storage temperature range | | $T_J$, $T_{stg}$ | -55 to +150 | °C |

**THERMAL RESISTANCE RATINGS**

| PARAMETER | | SYMBOL | TYPICAL | MAXIMUM | UNIT |
|---|---|---|---|---|---|
| Maximum junction-to-ambient [b, d] | t ≤ 5 s | $R_{thJA}$ | 75 | 100 | °C/W |
| Maximum junction-to-foot (drain) | Steady state | $R_{thJF}$ | 40 | 75 | |

**Notes**

a. Based on $T_C$ = 25 °C
b. Surface mounted on 1" x 1" FR4 board
c. t = 5 s
d. Maximum under steady state conditions is 166 °C/W

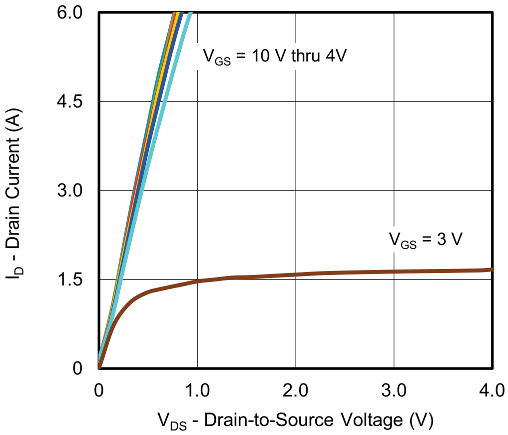
| **SPECIFICATIONS** ($T_J$ = 25 °C, unless otherwise noted) | | | | | | |
|---|---|---|---|---|---|---|
| **PARAMETER** | **SYMBOL** | **TEST CONDITIONS** | **MIN.** | **TYP.** | **MAX.** | **UNIT** |
| **Static** | | | | | | |
| Drain-source breakdown voltage | $V_{DS}$ | $V_{GS}$ = 0 V, $I_D$ = 250 μA | 100 | - | - | V |
| $V_{DS}$ temperature coefficient | $\Delta V_{DS}/T_J$ | $I_D$ =10 mA | - | 80 | - | mV/°C |
| $V_{GS(th)}$ temperature coefficient | $\Delta V_{GS(th)}/T_J$ | $I_D$ = 250 μA | - | -5 | - | |
| Gate-source threshold voltage | $V_{GS(th)}$ | $V_{DS}$ = $V_{GS}$, $I_D$ = 250 μA | 1.2 | - | 3 | V |
| Gate-source leakage | $I_{GSS}$ | $V_{DS}$ = 0 V, $V_{GS}$ = ± 20 V | - | - | ± 100 | nA |
| Zero gate voltage drain current | $I_{DSS}$ | $V_{DS}$ = 100 V, $V_{GS}$ = 0 V | - | - | 1 | μA |
| | | $V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_J$ = 55 °C | - | - | 10 | |
| On-state drain current [a] | $I_{D(on)}$ | $V_{DS} \geq$ 5 V, $V_{GS}$ = 10 V | 3 | - | - | A |
| Drain-source on-state resistance [a] | $R_{DS(on)}$ | $V_{GS}$ = 10 V, $I_D$ = 2 A | - | 0.124 | 0.149 | Ω |
| | | $V_{GS}$ = 4.5 V, $I_D$ = 1 A | - | 0.138 | 0.180 | |
| Forward transconductance [a] | $g_{fs}$ | $V_{DS}$ = 20 V, $I_D$ = 2 A | - | 12 | - | S |
| **Dynamic [b]** | | | | | | |
| Input capacitance | $C_{iss}$ | $V_{DS}$ = 50 V, $V_{GS}$ = 0 V, f = 1 MHz | - | 290 | - | pF |
| Output capacitance | $C_{oss}$ | | - | 26 | - | |
| Reverse transfer capacitance | $C_{rss}$ | | - | 5 | - | |
| Total gate charge | $Q_g$ | $V_{DS}$ = 50 V, $V_{GS}$ = 10 V, $I_D$ = 0.5 A | - | 4.7 | 7.1 | nC |
| | | $V_{DS}$ = 50 V, $V_{GS}$ = 4.5 V, $I_D$ = 0.5 A | - | 2.2 | 3.3 | |
| Gate-source charge | $Q_{gs}$ | | - | 1 | - | |
| Gate-drain charge | $Q_{gd}$ | | - | 0.5 | - | |
| Gate resistance | $R_g$ | f = 1 MHz | 0.3 | 1.5 | 3 | Ω |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD}$ = 50 V, $R_L$ = 31.25 Ω<br>$I_D$ = 1.6 A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 Ω | - | 15 | 30 | ns |
| Rise time | $t_r$ | | - | 15 | 30 | |
| Turn-off delay time | $t_{d(off)}$ | | - | 13 | 26 | |
| Fall time | $t_f$ | | - | 8 | 16 | |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD}$ = 50 V, $R_L$ = 31.25 Ω<br>$I_D$ = 1.6 A, $V_{GEN}$ = 10 V, $R_g$ = 1 Ω | - | 8 | 16 | |
| Rise time | $t_r$ | | - | 5 | 10 | |
| Turn-off delay time | $t_{d(off)}$ | | - | 15 | 30 | |
| Fall time | $t_f$ | | - | 5 | 10 | |
| **Drain-Source Body Diode Characteristics** | | | | | | |
| Continuous source-drain diode current | $I_S$ | $T_C$ = 25 °C | - | - | 1.4 | A |
| Pulse diode forward current [a] | $I_{SM}$ | | - | - | 6 | |
| Body diode voltage | $V_{SD}$ | $I_S$ = 1.6 A | - | 0.8 | 1.2 | V |
| Body diode reverse recovery time | $t_{rr}$ | $I_F$ = 1.6 A, di/dt = 100 A/μs,<br>$T_J$ = 25 °C | - | 21 | 32 | ns |
| Body diode reverse recovery charge | $Q_{rr}$ | | - | 21 | 32 | nC |
| Reverse recovery fall time | $t_a$ | | - | 19 | - | ns |
| Reverse recovery rise time | $t_b$ | | - | 2 | - | |

**Notes**

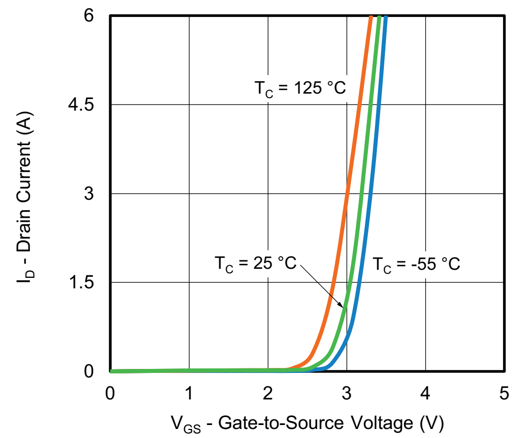
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %
b. Guaranteed by design, not subject to production testing

*Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.*

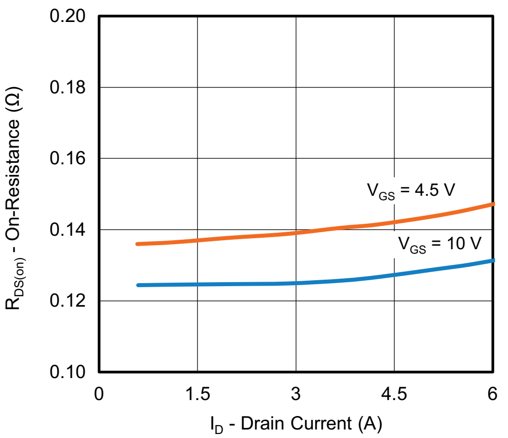
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



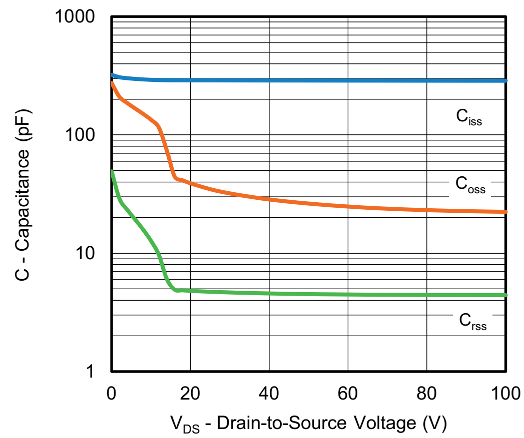

**Output Characteristics**



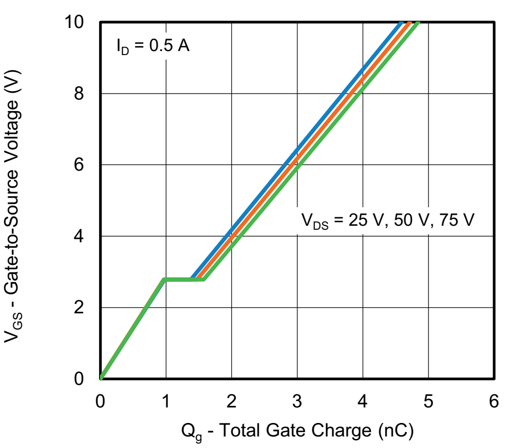

**Transfer Characteristics**



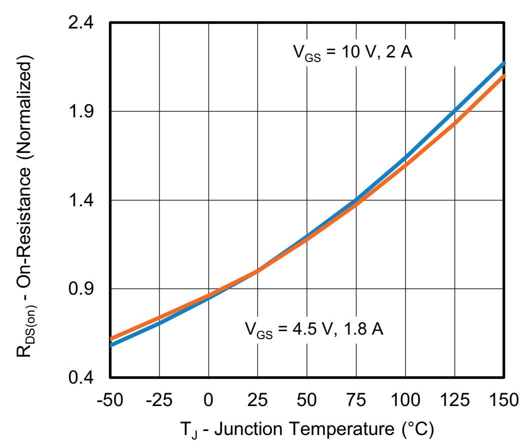

**On-Resistance vs. Drain Current**




**Capacitance**

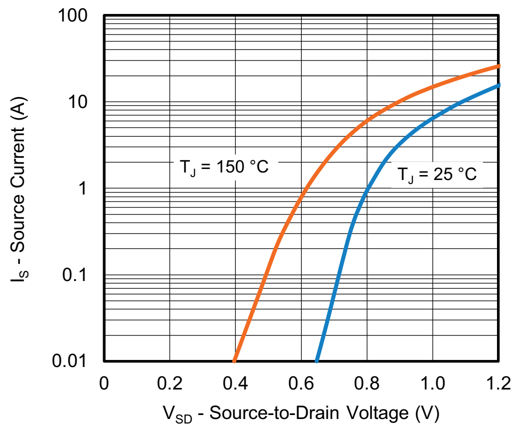



**Gate Charge**

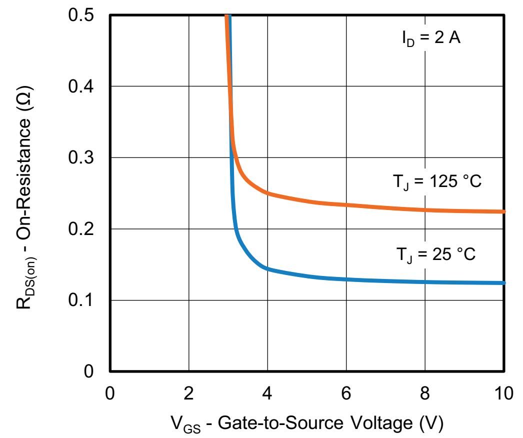



**On-Resistance vs. Junction Temperature**

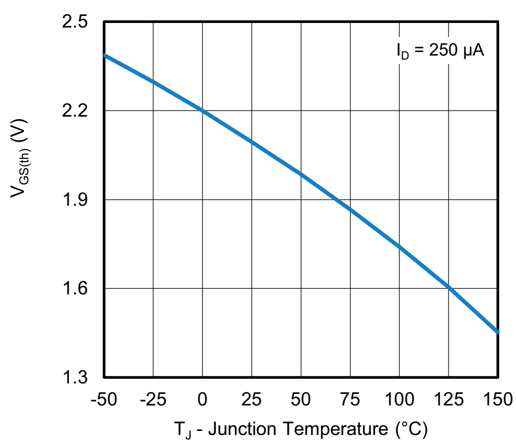
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



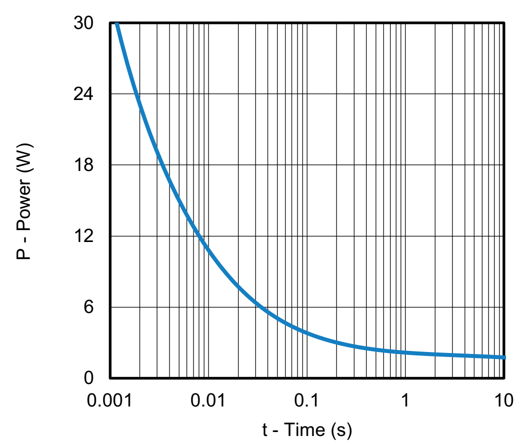

**Source-Drain Diode Forward Voltage**



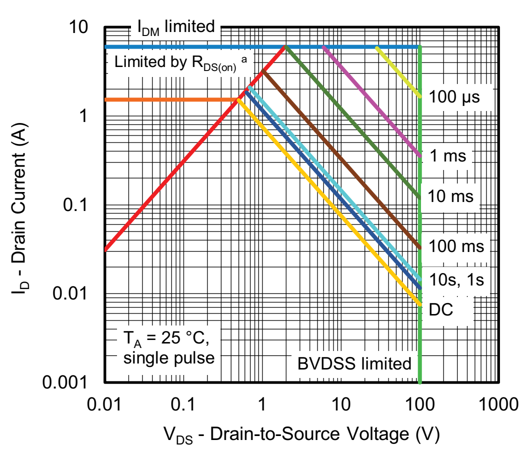

**On-Resistance vs. Gate-to-Source Voltage**




**Threshold Voltage**

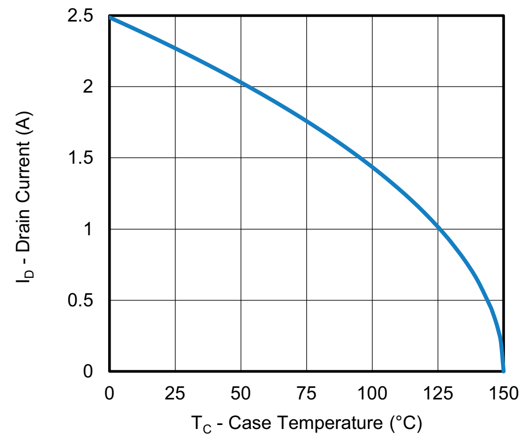



**Single Pulse Power**

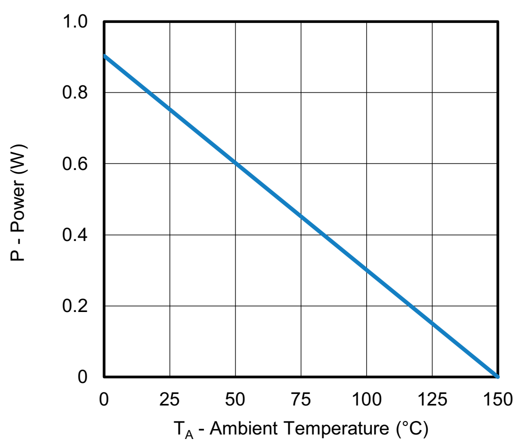



**Safe Operating Area**

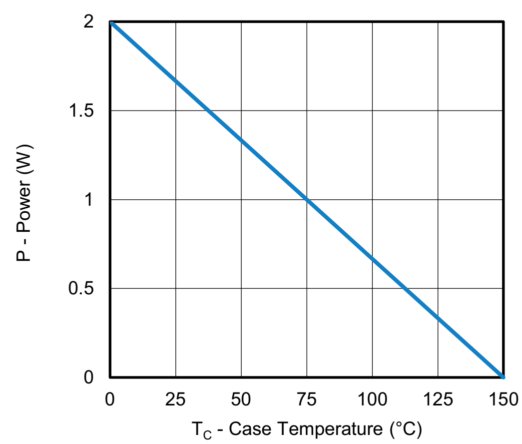
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)




**Current Derating** [a]




**Power, Junction-to-Foot**

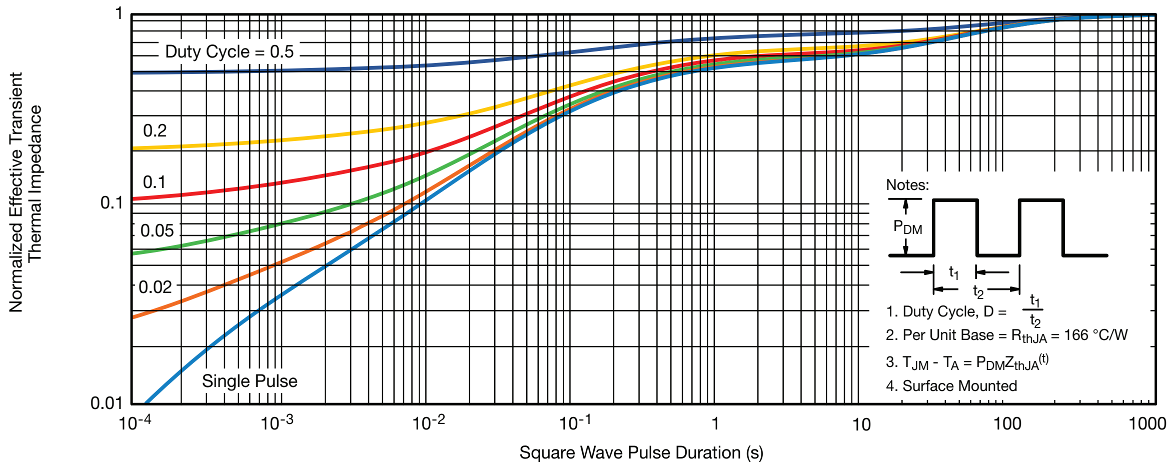



**Power, Junction-to-Ambient**

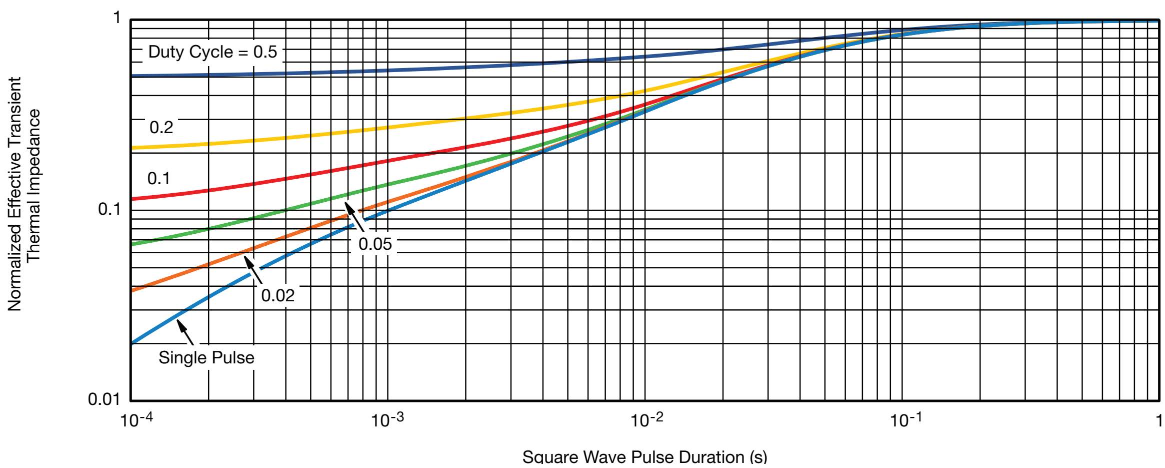
**Note**

a. The power dissipation $P_D$ is based on $T_J$ max.= 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)




**Normalized Thermal Transient Impedance, Junction-to-Ambient**




**Normalized Thermal Transient Impedance, Junction-to-Foot**

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