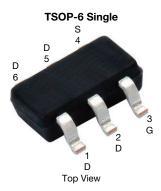


Vishay Siliconix

# N-Channel 150 V (D-S) MOSFET



#### Marking code: BS

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	150					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 10 \text{ V}$	0.380					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	0.432					
Q <sub>g</sub> typ. (nC)	1.65					
I <sub>D</sub> (A) <sup>d</sup>	2.2					
Configuration	Single					

#### **FEATURES**

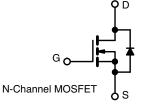
- ThunderFET® power MOSFET
- 100 % R<sub>g</sub> tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



ROHS COMPLIANT HALOGEN FREE

#### **APPLICATIONS**

- DC/DC converters
- Boost converters
- · LED backlighting
- PD switch
- · Load switch



ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	Si3440ADV-T1-GE3

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		$V_{DS}$	150		
Gate-source voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		2.2		
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C		1.7		
	T <sub>A</sub> = 25 °C	I <sub>D</sub>	1.6 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		1.3 <sup>a, b</sup>		
Pulsed drain current (t = 100 µs)		I <sub>DM</sub>	4	A	
	T <sub>C</sub> = 25 °C		3		
Continuous source-drain diode current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1.7 <sup>a, b</sup>		
Single pulse avalanche current	1 0.1 ml l	I <sub>AS</sub>	3		
Single pulse avalanche energy  L = 0.1 mH		E <sub>AS</sub>	0.45	mJ	
	T <sub>C</sub> = 25 °C		3.6		
Maximum power dissipation	T <sub>C</sub> = 70 °C		2.3	10/	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2 <sup>a, b</sup>	W	
	T <sub>A</sub> = 70 °C		1.3 <sup>a, b</sup>		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

THERMAL RESISTANCE RATINGS							
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient a, c	t ≤ 10 s	R <sub>thJA</sub>	50	62.5	°C/W		
Maximum junction-to-foot (drain)	Steady state	$R_{thJF}$	28	35	C/VV		

#### Notes

- a. Surface mounted on 1" x 1" FR4 board
- b. t = 10 s
- c. Maximum under steady state conditions is 110 °C/W
- d.  $T_C = 25$  °C



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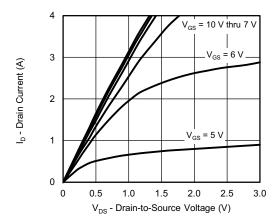
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static				•		
Drain-source breakdown voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	150	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	J 050 A	-	135	-	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	1	-5.6	-	mV/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2	-	4	V
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
Zana anta malta an aluain annuant	I <sub>DSS</sub>	V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V	-	-	1	μΑ
Zero gate voltage drain current		V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C	-	-	10	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 10 \text{ V}, V_{GS} = 10 \text{ V}$	4	-	-	Α
Delta de la contra del contra de la contra del la contra de la contra de la contra de la contra del la contra de la contra de la contra del la contra de la contra de la contra de la contra del la contra del la contra de la contra de la contra del la c		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A	ī	0.316	0.380	1 -
Drain-source on-state resistance a	R <sub>DS(on)</sub>	V <sub>GS</sub> = 7.5 V, I <sub>D</sub> = 1 A	-	0.345	0.432	Ω
Forward transconductance a	9 <sub>fs</sub>	$V_{DS} = 50 \text{ V}, I_D = 1.5 \text{ A}$	-	2.4	-	S
Dynamic <sup>b</sup>						
Input capacitance	C <sub>iss</sub>		-	80	-	pF
Output capacitance	C <sub>oss</sub>	$V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	26	-	
Reverse transfer capacitance	C <sub>rss</sub>		ī	3	-	
Total gate charge	Qg	V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A	ī	2	4	nC
			-	1.65	3	
Gate-source charge	Q <sub>gs</sub>	$V_{DS} = 75 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 0.5 \text{ A}$	ī	0.5	-	
Gate-drain charge	Q <sub>gd</sub>		1	0.7	-	
Gate resistance	$R_g$	f = 1 MHz	0.7	3.5	7	Ω
Turn-on delay time	t <sub>d(on)</sub>		ī	8	16	
Rise time	t <sub>r</sub>	$V_{DD} = 75 \text{ V}, R_L = 57.7 \Omega, I_D \cong 1.3 \text{ A},$	1	22	35	
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	9	18	1
Fall time	t <sub>f</sub>		-	22	35	]
Turn-on delay time	t <sub>d(on)</sub>		-	10	20	ns -
Rise time	t <sub>r</sub>	$V_{DD} = 75 \text{ V}, R_L = 57.7 \Omega, I_D \cong 1.3 \text{ A},$	-	25	40	
Turn-off delay time	t <sub>d(off)</sub>	$V_{GEN} = 7.5 \text{ V}, R_g = 1 \Omega$	1	10	20	
Fall time	t <sub>f</sub>		-	24	50	
Drain-Source Body Diode Characteristi	cs					
Continuous source-drain diode current	Is	T <sub>C</sub> = 25 °C	-	-	1.7	_
Pulse diode forward current	I <sub>SM</sub>		-	-	4	A
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = 1.3 A, V <sub>GS</sub> = 0 V	-	0.85	1.2	V
Body diode reverse recovery time	t <sub>rr</sub>		-	44	66	ns
Body diode reverse recovery charge	Q <sub>rr</sub>		-	53	80	nC
Reverse recovery fall time	ta	$I_F = 1.3 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 ^{\circ}\text{C}$	-	27	-	
Reverse recovery rise time	t <sub>b</sub>		-	17	-	ns

#### Notes

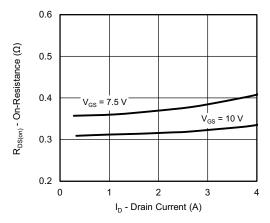
- a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

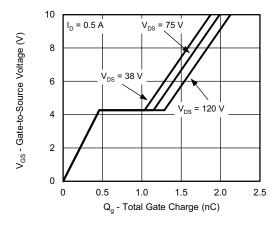




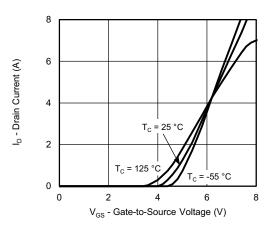
#### **Output Characteristics**



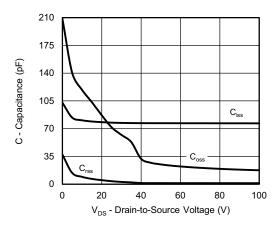
On-Resistance vs. Drain Current and Gate Voltage



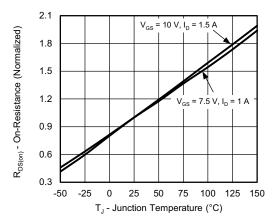
**Gate Charge** 



**Transfer Characteristics** 

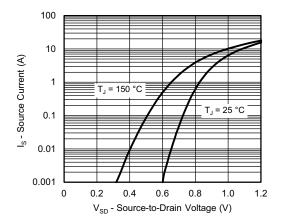


Capacitance

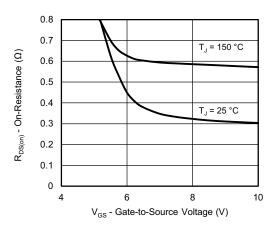


On-Resistance vs. Junction Temperature

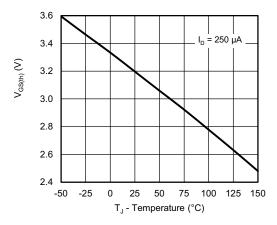




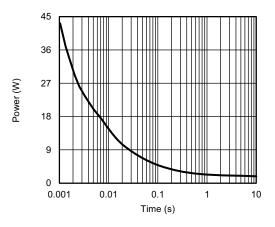
Source-Drain Diode Forward Voltage



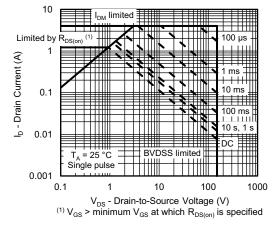
On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 

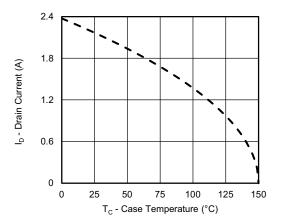


Single Pulse Power, Junction-to-Ambient

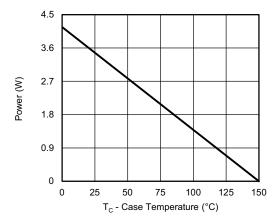


Safe Operating Area, Junction-to-Ambient

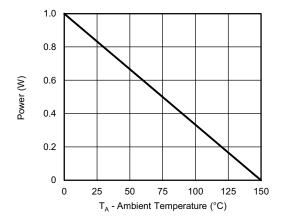




#### Current Derating a





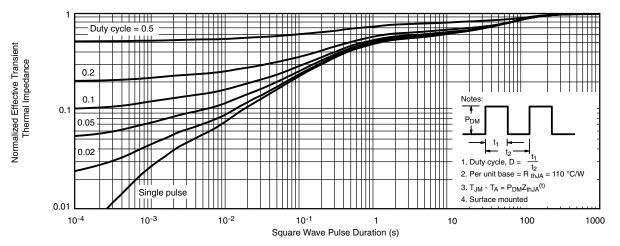


Power, Junction-to-Ambient

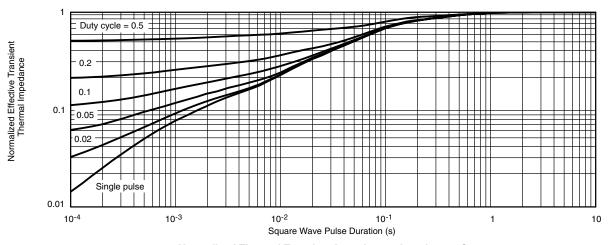
#### Note

a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?75594">www.vishay.com/ppg?75594</a>.





TSOP: 5/6-LEAD

**JEDEC Part Number: MO-193C** 





**5-LEAD TSOP** 







	MIL	LIMETER	RS	INCHES				
Dim	Min	Nom	Max	Min	Nom	Max		
Α	0.91	-	1.10	0.036	-	0.043		
A <sub>1</sub>	0.01	-	0.10	0.0004	-	0.004		
A <sub>2</sub>	0.90	-	1.00	0.035	0.038	0.039		
b	0.30	0.32	0.45	0.012	0.013	0.018		
С	0.10	0.15	0.20	0.004	0.006	0.008		
D	2.95	3.05	3.10	0.116	0.120	0.122		
E	2.70	2.85	2.98	0.106	0.112	0.117		
E <sub>1</sub>	1.55	1.65	1.70	0.061	0.065	0.067		
е		0.95 BSC		0.0374 BSC				
e <sub>1</sub>	1.80	1.90	2.00	0.071	0.075	0.079		
L	0.32	-	0.50	0.012	-	0.020		
L <sub>1</sub>		0.60 Ref			0.024 Ref			
L <sub>2</sub>	0.25 BSC			0.010 BSC				
R	0.10	-	-	0.004	-	-		
θ	0°	4°	8°	0°	4°	8°		
θ1	7° Nom			7° Nom				
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540								

DWG: 5540

Document Number: 71200 18-Dec-06



# Recommended Land Pattern For TSOP-5L / TSOP-6L



#### Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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