Vishay Siliconix

# N-Channel 20 V (D-S) MOSFET



Marking code: P5

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	20					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	0.0318					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 2.5 \text{ V}$	0.0356					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 1.8 \text{ V}$	0.0414					
Q <sub>g</sub> typ. (nC)	8.8					
I <sub>D</sub> (A) a, e	6					
Configuration	Single					

#### **FEATURES**

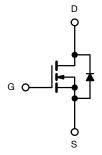
- TrenchFET® power MOSFET
- 100% R<sub>g</sub> tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



ROHS COMPLIANT HALOGEN FREE

### **APPLICATIONS**

- DC/DC converters
- Load switch for portable applications



N-Channel MOSFET

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	Si2312CDS-T1-GE3

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	20	V	
Gate-source voltage		V <sub>GS</sub>	± 8	V	
	T <sub>C</sub> = 25 °C		6 <sup>a</sup>		
Continuous durin surrent (T. 150 °C)	T <sub>C</sub> = 70 °C	1 .	5.1		
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	l <sub>D</sub>	5 b, c		
	T <sub>A</sub> = 70 °C		4 b, c	Α	
Pulsed drain current		I <sub>DM</sub>	20		
	T <sub>C</sub> = 25 °C		1.75		
Continuous source-drain diode current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	1.04 b, c		
	T <sub>C</sub> = 25 °C		2.1		
Maximum power dissipation	T <sub>C</sub> = 70 °C	1 .	1.3	14/	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.25 <sup>b, c</sup>	W	
	T <sub>A</sub> = 70 °C	1	0.8 <sup>b, c</sup>		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150		
Soldering recommendations (peak temperature)			260	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient b, d	t ≤ 5 s	R <sub>thJA</sub>	80	100	°C/W	
Maximum junction-to-foot (drain)	Steady state	$R_{thJF}$	40	60	C/VV	

#### Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 5 s
- d. Maximum under steady state conditions is 125 °C/W
- e. Based on T<sub>C</sub> = 25 °C

# Vishay Siliconix

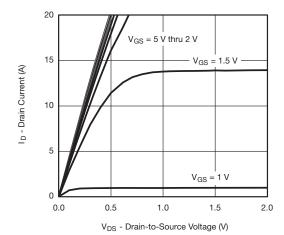
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static	1		1	1	1		
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	20	-	_	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	•	-	25	-		
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	_	-2.6	-	mV/°C	
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.45	-	1	V	
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	_	-	± 100	nA	
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	-	-	1	μΑ	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C	-	-	10		
On-state drain current a	I <sub>D(on)</sub>	$V_{DS} \le 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	20	-	=.	Α	
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	$V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$	-	0.0265	0.0318		
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 4.7 \text{ A}$	-	0.0296	0.0356	Ω	
		$V_{GS} = 1.8 \text{ V}, I_D = 4.3 \text{ A}$	-	0.0345	0.0414		
Forward transconductance a	9 <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	-	24	-	S	
Dynamic <sup>b</sup>					•		
Input capacitance	C <sub>iss</sub>		-	865	-	pF	
Output capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	105	-		
Reverse transfer capacitance	C <sub>rss</sub>		-	55	=.		
		$V_{DS} = 10 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 5 \text{ A}$	-	12	18		
Total gate charge	Qg	20 / 00 / 0	-	8.8	14	nC	
Gate-source charge	Q <sub>as</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5 \text{ A}$	_	1.1	-		
Gate-drain charge	Q <sub>gd</sub>		_	0.7	-		
Gate resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.4	4.8	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	8	16		
Rise time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, R_1 = 2.2 \Omega$	_	17	26	1	
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong 4 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$	-	31	47		
Fall time	t <sub>f</sub>		-	8	16		
Turn-on delay time	t <sub>d(on)</sub>		-	5	10	ns	
Rise time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, R_1 = 2.2 \Omega$	-	13	20	<u> </u>	
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong 4 \text{ A}, V_{GEN} = 5 \text{ V}, R_g = 1 \Omega$	_	21	32		
Fall time	t <sub>f</sub>		-	6	12		
<b>Drain-Source Body Diode Characteristic</b>	s		<u> </u>	L	1	l	
Continuous source-drain diode current	Is	T <sub>C</sub> = 25 °C	_	-	1.75	_	
Pulse diode forward current	I <sub>SM</sub>	-	-	-	20	Α	
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = 4 A, V <sub>GS</sub> = 0 V	-	0.75	1.2	V	
Body diode reverse recovery time	t <sub>rr</sub>	<i>5 ,</i> 45	_	12	20	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	$I_F = 4 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s,}$	_	5	10	nC	
Reverse recovery fall time	t <sub>a</sub>	$T_J = 25  ^{\circ}\text{C}$	_	7	-		
	t <sub>b</sub>	·	-	5		ns	

## Notes

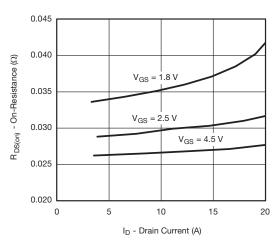
- a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

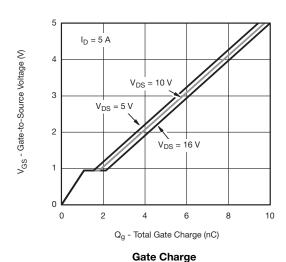


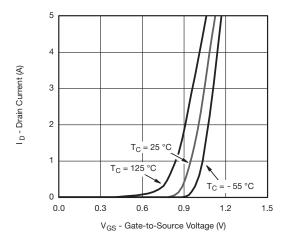


#### **Output Characteristics**

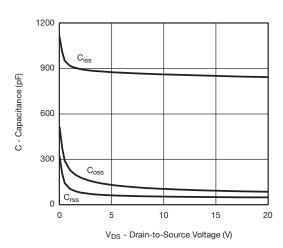


### On-Resistance vs. Drain Current and Gate Voltage

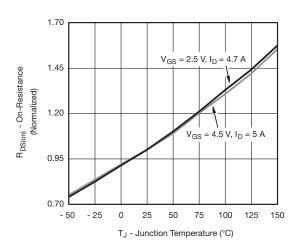




#### **Transfer Characteristics**

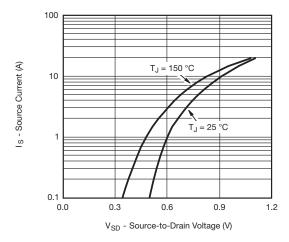


### Capacitance

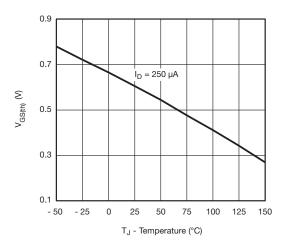


On-Resistance vs. Junction Temperature

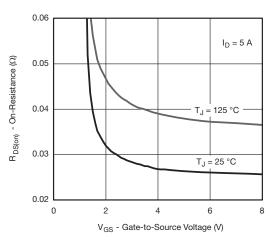




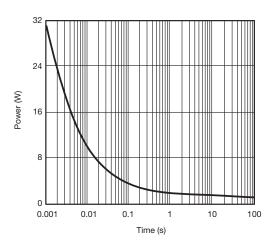
#### Source-Drain Diode Forward Voltage



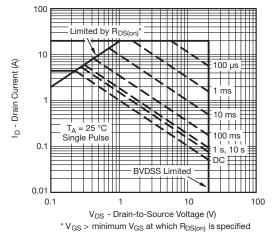
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage

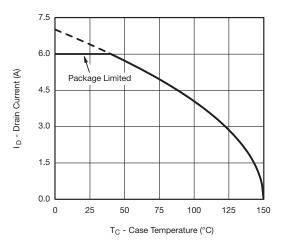


Single Pulse Power (Junction-to-Ambient)

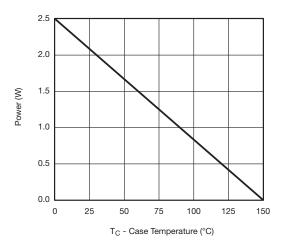


Safe Operating Area, Junction-to-Ambient

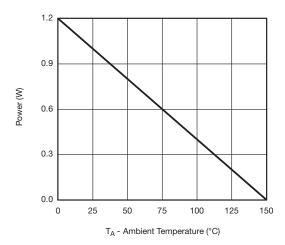




#### Current Derating a





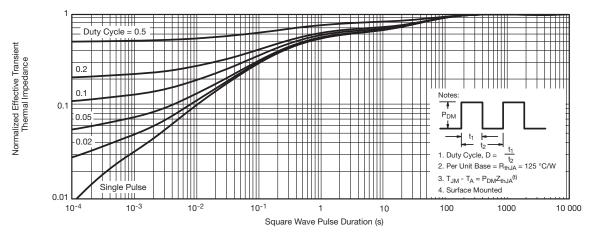


Power Derating, Junction-to-Ambient

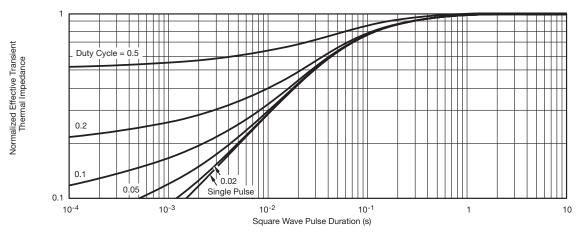
#### Note

a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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## SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K. 09-	Jul-01				

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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