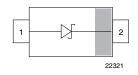


#### www.vishay.com

## Vishay Semiconductors

# **Small Signal Schottky Diode**





#### **LINKS TO ADDITIONAL RESOURCES**



#### **MECHANICAL DATA**

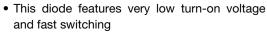
Case: SOD-523

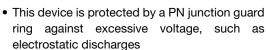
Weight: approx. 1.4 mg

Molding compound flammability rating: UL 94 V-0 **Terminals:** high temperature soldering guaranteed:

260 °C/10 s at terminals **Packaging codes / options:**08/8K per 7" reel (8 mm tape)

#### **FEATURES**

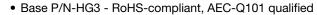






• Space saving SOD-523 package

Base P/N-G3 - RoHS-compliant, commercial grade



 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>







HALOGEN FREE

<u>(5-2008)</u>

PARTS TABLE						
PART	ORDERING CODE	AEC-Q101 QUALIFIED	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS	
BAS70-02V	BAS70-02V-G3-08 no Since		Single	:X	Tape and reel	
	BAS70-02V-HG3-08	yes	Single	.^	rape and reel	

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Repetitive peak reverse voltage		$V_{RRM}$	70	V	
Forward continuous current		I <sub>F</sub>	100	mA	
Surge forward current	$t_p$ = 10 ms square wave, $T_j$ = 25 °C prior to surge	I <sub>FSM</sub>	600	mA	
Power dissipation on FR-4 board with recommended soldering footprin		P <sub>tot</sub>	150	mW	

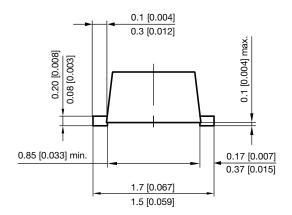
THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	on FR-4 board according to JEDEC® 51-3 with recommended soldering footprint	R <sub>thJA</sub> 680		K/W		
Thermal resistance junction to lead		R <sub>thJL</sub>	480	K/W		
Junction temperature		Tj	125	°C		
Operating temperature range		T <sub>op</sub>	-55 to +125	°C		
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C		

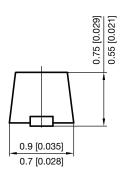
<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reserve breakdown voltage	I <sub>R</sub> = 10 μA (pulsed)	V <sub>(BR)</sub>	70			V
Leakage current	$V_R = 50 \text{ V}, t_p < 300 \mu\text{s}$	I <sub>R</sub>		20	100	nA
Forward voltage	$t_p < 300 \ \mu s, \ I_F = 1.0 \ mA$	V <sub>F</sub>			410	mV
Forward voitage	$t_p < 300 \ \mu s, \ I_F = 15 \ mA$	V <sub>F</sub>			1000	mV
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>D</sub>		1.5	2	pF
Reserve recovery time	$I_F$ = 10 mA, $I_R$ = 10 mA, $I_R$ = 1 mA, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			5	ns

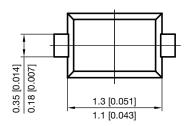


# Vishay Semiconductors

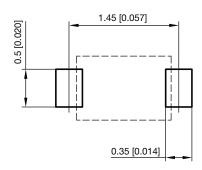
### PACKAGE DIMENSIONS in millimeters [inches]: SOD-523







Footprint recommendation:



Document no.: S8-V-3880.02-003 (4) Created - Date: 04. April 2017 Rev. 4 - Date: 03. Aug. 2020

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