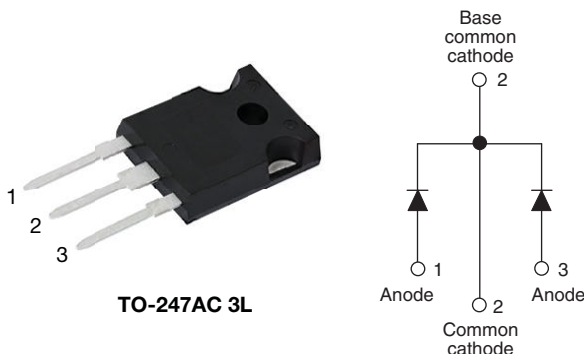


High Performance Schottky Rectifier, 2 x 30 A


TO-247AC 3L

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT
HALOGEN
FREE

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 30 A
V_R	45 V
V_F at I_F	0.55 V
I_{RM} max.	150 mA at 125 °C
T_J max.	150 °C
E_{AS}	27 mJ
Package	TO-247AC 3L
Circuit configuration	Common cathode

DESCRIPTION

The VS-MBR6045WT... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	60	A
V_{RRM}		45	V
I_{FSM}	$t_p = 5 \mu s$ sine	2900	A
V_F	30 A _{pk} , $T_J = 125$ °C (per leg)	0.55	V
T_J		-55 to +150	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-MBR6045WT-N3	UNITS
Maximum DC reverse voltage	V_R	45	V
Maximum working peak reverse voltage	V_{RWM}		

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current, see fig. 5	$I_{F(AV)}$	50 % duty cycle at $T_C = 122$ °C, rectangular waveform	30	A
			60	
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse	2900	
		10 ms sine or 6 ms rect. pulse	360	
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25$ °C, $I_{AS} = 4$ A, $L = 3.4$ mH	27	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical	6	A

**ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	30 A	T _J = 25 °C	0.62	V
		60 A		0.75	
		30 A	T _J = 125 °C	0.55	
Maximum reverse leakage current per leg See fig. 2	I _{RM} (1)	T _J = 25 °C	V _R = Rated V _R	1	mA
		T _J = 125 °C		150	
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.27	V
Forward slope resistance	r _t			7.3	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		1400	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/μs

Note(1) Pulse width < 300 μ s, duty cycle < 2 %**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation See fig. 4	1.0	°C/W
Maximum thermal resistance, junction to case per package		DC operation	0.5	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.24	
Approximate weight			6	g
			0.21	oz.
Mounting torque	minimum		6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-247AC 3L	MBR6045WT	

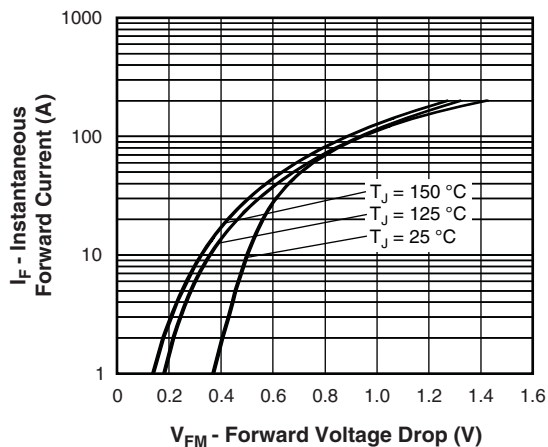


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

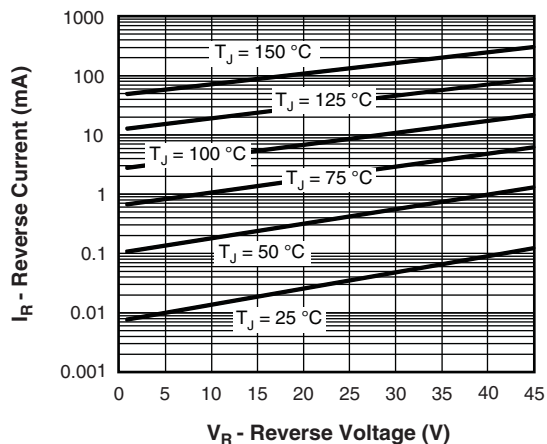


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

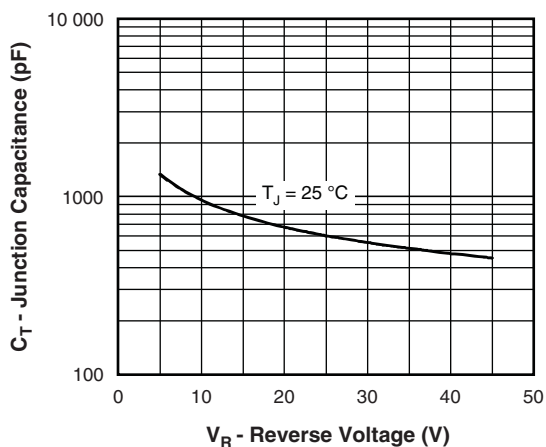


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

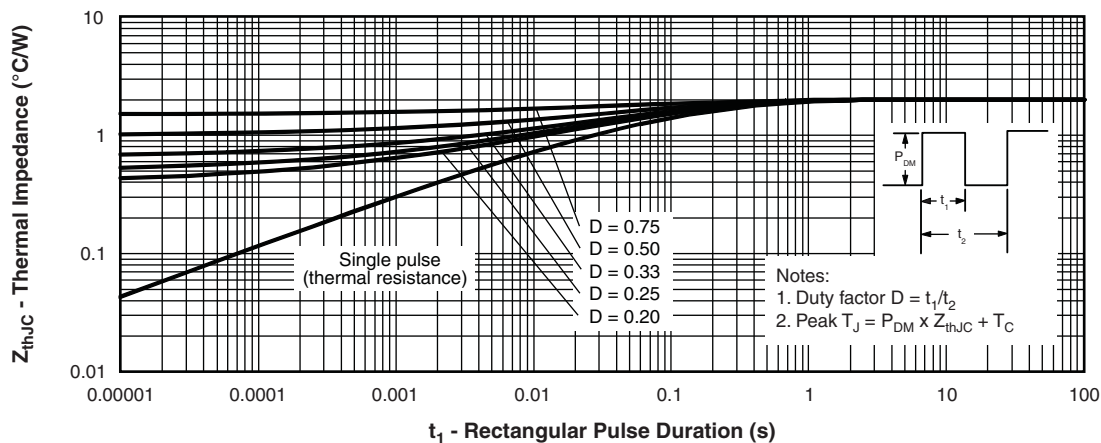


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

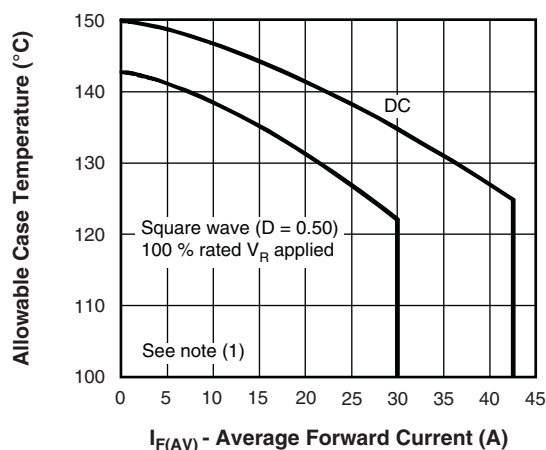


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

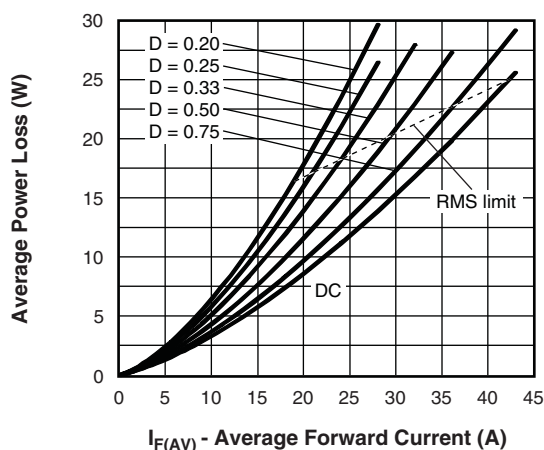


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

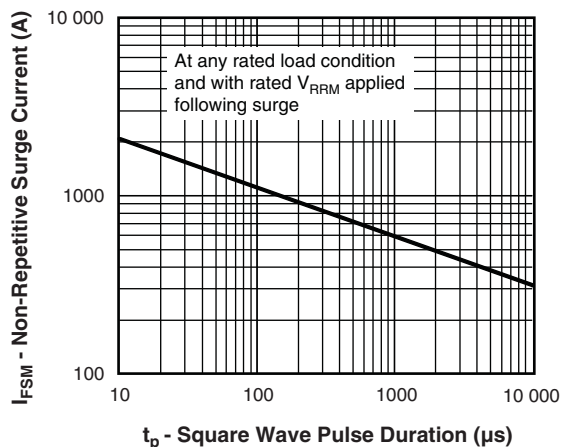


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

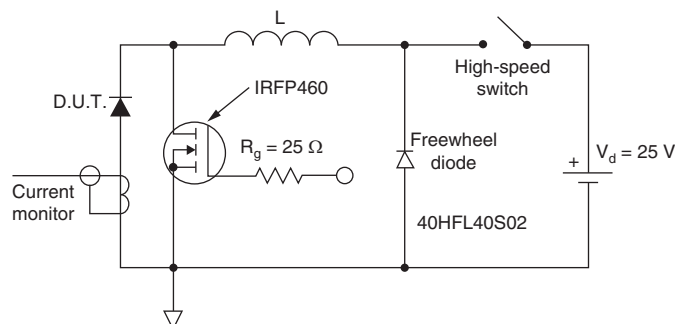


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 100\%$ rated V_R



ORDERING INFORMATION TABLE

Device code	VS-	MBR	60	45	WT	-N3
	1	2	3	4	5	6

- | | | |
|---|---|---------------------------------------------------------------------------------------|
| 1 | - | Vishay Semiconductors product |
| 2 | - | Schottky MBR series |
| 3 | - | Current rating (60 = 60 A) |
| 4 | - | Voltage rating (45 = 45 V) |
| 5 | - | Circuit configuration:
Center tap (dual) TO-247 |
| 6 | - | Environmental digit
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free |

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-MBR6045WT-N3	25	500	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96138
Part marking information	www.vishay.com/doc?95007



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