

# Bidirectional Symmetrical (BiSy) Low Capacitance, **Dual-Line ESD Protection Diode in DFN1110-3A**





### **MARKING** (example only)



Dot = pin marking X = date code

Y = type code (see table below)

## **LINKS TO ADDITIONAL RESOURCES**







#### **FEATURES**

- For CAN FD Bus applications
- Small DFN1110-3A
- 2-line ESD protection
- Working range ± 36 V
- Low leakage current I<sub>R</sub> < 0.05 μA</li>
- Low load capacitance C<sub>D</sub> < 6 pF (at V<sub>R</sub> = 5 V)
- ESD immunity acc. IEC 61000-4-2 ± 20 kV contact discharge
- ± 20 kV air discharge ESD capability according to AEC-Q101:
- human body model: class H3B: > 8 kV • e3 - pins side wall plated with tin (Sn)
- AOI capable
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912







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ORDERING INFORMATION							
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE			PACKAGING CODE			
	AEC-Q101 QUALIFIED	RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS	TIN PLATED	10K PER 7" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)		
	QUALIFIED	GREEN	PLATED	10K = MOQ			
VCAN36A2-HT5	-	G	3	-08	VCAN36A2-HT5-G3-08		
VCAN36A2-HT5	Н	G	3	-08	VCAN36A2-HT5HG3-08		

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VCAN36A2-HT5	DFN1110-3A	С	1.43 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	$T_A$ = 25 °C, acc. IEC 61000-4-5; $t_p$ = 8/20 $\mu$ s; single shot	$I_{PPM}$	1.6	Α		
Peak pulse power	$T_A = 25$ °C; pin 1 or 2 to pin 3; acc. IEC 61000-4-5; $t_p = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	92	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses, T <sub>A</sub> = 25 °C	V	± 20	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses, T <sub>A</sub> = 25 °C	V <sub>ESD</sub>	± 20	kV		
Operating temperature	Junction temperature	$T_J$	-55 to +150	°C		
Storage temperature		T <sub>STG</sub>	-55 to +150	°C		



<b>ELECTRICAL CHARACTERISTICS</b> (pin 1 to 3, 3 to 1, 2 to 3, or 3 to 2) (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	36	V		
Reverse voltage	At I <sub>R</sub> = 0.05 μA	V <sub>R</sub>	36	-	-	V		
Reverse current	At V <sub>RWM</sub> = 36 V	I <sub>R</sub>	-	-	0.05	μA		
Reverse breakdown voltage	At I <sub>R</sub> = 1 mA	V <sub>BR</sub>	39	42	45	V		
Reverse clamping voltage	At I <sub>PP</sub> 1 A; t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	-	54	V		
	At I <sub>PP</sub> = I <sub>PPM</sub> = 1.6 A; t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	53	58	V		
Capacitance	At $V_R = 0 V$ , $f = 1 MHz$	0	-	5.6	6.8	pF		
	At V <sub>R</sub> = 5 V, f = 1 MHz	- C <sub>D</sub>	-	3.8	4.6	pF		
	Diode capacitance matching at $V_R = 5 V$ , $C_{D13}$ vs. $C_{D23}$	dC <sub>D</sub>	=	-	0.12	pF		

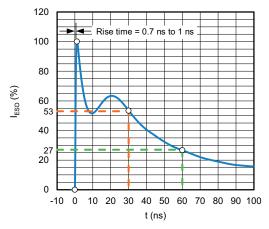


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega\,/150$  pF)

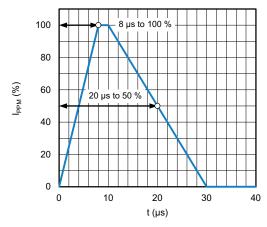


Fig. 2 - 8/20  $\mu s$  Peak Pulse Current Wave Form acc. IEC 61000-4-5

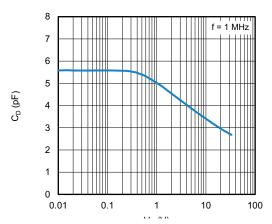


Fig. 3 - Typical Capacitance vs. Reverse Voltage

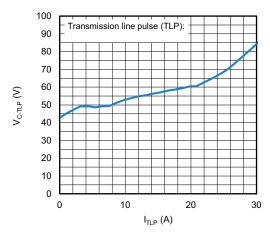


Fig. 4 - Typical Clamping Voltage vs. Peak Pulse Current

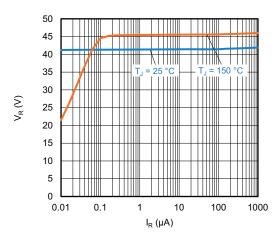


Fig. 5 - Typical Reverse Voltage vs. Reverse Current

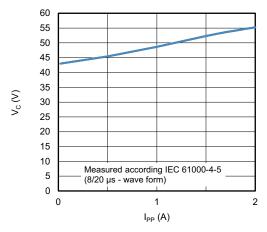
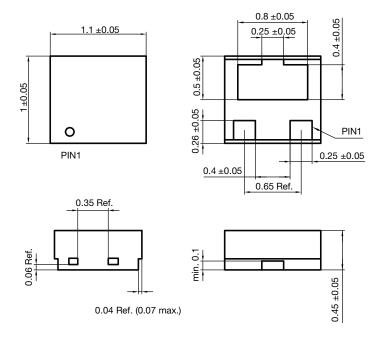
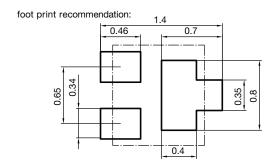


Fig. 6 - Typical Peak Clamping Voltage vs. Peak Pulse Current

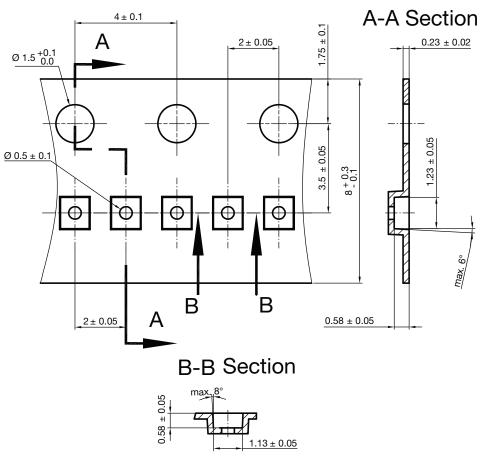
## **PACKAGE DIMENSIONS** in millimeters (inches)





Document no.: S8-V-3906.04-062 (4) Package name: DFN1110-3A Created - Date: 04-Apr-2019

## **CARRIER TAPE DFN1110-3A**

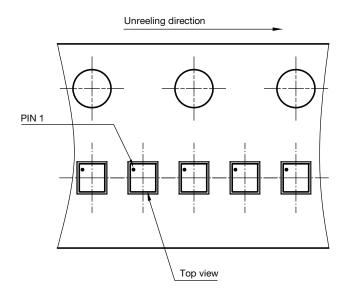


Document no: S8-V-3906.04-065 (4) Package name: DFN1110-3A Created date: 28.10.2019

surface resistance: 10<sup>5</sup> - 10<sup>11</sup> OHMS SQ

Cummulative tolerances of 10 sprocket holes is  $\pm$  0.2 mm

## **ORIENTATION IN CARRIER TAPE DFN1110-3A**



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