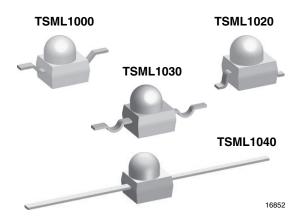
TSML1000, TSML1020, TSML1030, TSML1040

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High Power Infrared Emitting Diode, 940 nm, GaAlAs, MQW



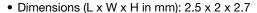
DESCRIPTION

TSML1000 is an infrared, 940 nm emitting diode in GaAlAs multi quantum well (MQW) technology with high radiant power and high speed molded in a clear, untinted plastic package (with lens) for surface mounting (SMD).

FEATURES

· Package type: surface-mount





Peak wavelength: λ_p = 940 nm

· High radiant power

High radiant intensity

• Angle of half intensity: $\varphi = \pm 12^{\circ}$

· Low forward voltage

· Suitable for high pulse current operation

· Good spectral matching with Si photodetectors

· Versatile terminal configurations

Package matches with detector TEMT1000

• Floor life: 168 h, MSL 3, according to J-STD-020

 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- · For remote control
- Encoder
- Photointerrupters

PRODUCT SUMMARY				
COMPONENT	I _e (mW/sr) at I _F = 20 mA	φ (°)	λ _P (nm)	t _r (ns)
TSML1000	11	± 12	940	15
TSML1020	11	± 12	940	15
TSML1030	11	± 12	940	15
TSML1040	11	± 12	940	15

Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMAT	ION		
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM
TSML1000	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Reverse gullwing
TSML1020	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Gullwing
TSML1030	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Yoke
TSML1040	Bulk	MOQ: 1000 pcs, 1000 pcs/bulk	Axial leads

Note

· MOQ: minimum order quantity









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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V_{R}	5	V	
Forward current		I _F	100	mA	
Peak forward current	$t_p/T = 0.5, t_p = 100 \mu s$	I _{FM}	200	mA	
Surge forward current	t _p = 100 μs	I _{FSM}	1.0	Α	
Power dissipation		P _V	160	mW	
Junction temperature		Tj	100	°C	
Operating temperature range		T _{amb}	-40 to +85	°C	
Storage temperature range		T _{stg}	-40 to +100	°C	
Soldering temperature	According to Fig. 10, J-STD-020	T _{sd}	260	°C	
Thermal resistance junction to ambient	EIA / JESD51	R _{thJA}	400	K/W	

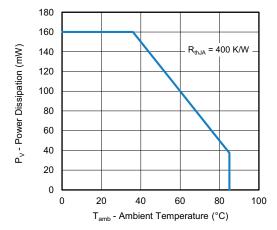


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

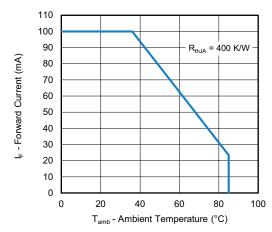


Fig. 2 - Forward Current vs. Ambient Temperature

PARAMETER	CS (T _{amb} = 25 °C, unless othe	SYMBOL	MIN.	TYP.	MAX.	UNIT
PARAMETER	TEST CONDITION	STWIDUL	WIIIN.	ITP.	WAX.	UNIT
Forward voltage	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	V_{F}	-	1.2	1.5	V
	$I_F = 1 A$, $t_p = 100 \mu s$	V _F	-	2.2	-	V
Temperature coefficient of V _F	I _F = 1 mA	TK _{VF}	-	-1.8	-	mV/K
Reverse current	V _R = 5 V	I _R	-	-	10	μΑ
Junction capacitance	V _R = 0 V, f = 1 MHz, E = 0	Cj	-	40	-	pF
Radiant intensity	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	l _e	3	11	15	mW/sr
Radiant power	$I_F = 100 \text{ mA}, t_p = 20 \text{ ms}$	фe	-	40	-	mW
Temperature coefficient of φ _e	I _F = 20 mA	TKφe	-	-0.6	-	%/K
Angle of half intensity		φ	-	± 12	-	0
Peak wavelength	I _F = 100 mA	λ_{p}	-	940	-	nm
Spectral bandwidth	I _F = 100 mA	Δλ	-	30	-	nm
Temperature coefficient of λ_p	I _F = 100 mA	TKλ _p	-	0.2	-	nm/K
Rise time	I _F = 100 mA	t _r	-	15	-	ns
Fall time	I _F = 100 mA	t _f	-	15	-	ns

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

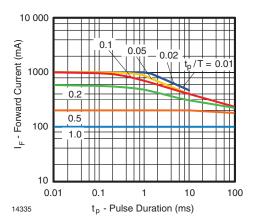


Fig. 3 - Pulse Forward Current vs. Pulse Duration

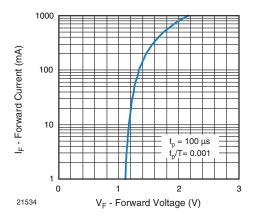


Fig. 4 - Forward Current vs. Forward Voltage

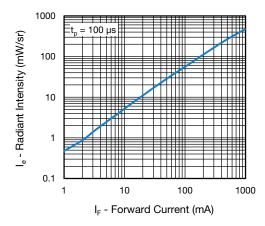


Fig. 5 - Radiant Intensity vs. Forward Current

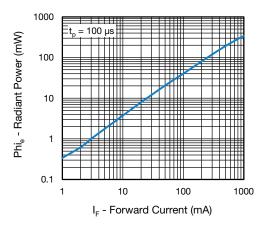


Fig. 6 - Radiant Power vs. Forward Current

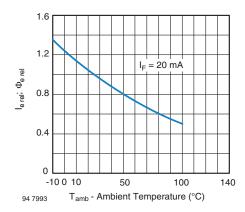


Fig. 7 - Relative Radiant Intensity/Power vs. Ambient Temperature

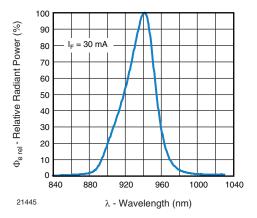


Fig. 8 - Relative Radiant Power vs. Wavelength

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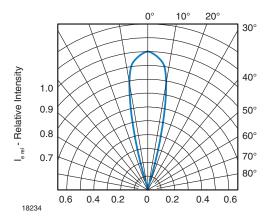


Fig. 9 - Relative Radiant Intensity vs. Angular Displacement

PRECAUTIONS FOR USE

1. Over-current-proof

Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (burn out will happen).

2. Storage

- Storage temperature and rel. humidity conditions are: 5 °C to 35 °C, R.H. 60 %.
- Floor life must not exceed 168 h, according to JEDEC® level 3, J-STD-020.
 - Once the package is opened, the products should be used within a week. Otherwise, they should be kept in a damp proof box with desiccant.
 - Considering tape life, we suggest to use products within one year from production date.
- If opened more than one week in an atmosphere 5 °C to 35 °C, R.H. 60 %, devices should be treated at 60 °C ± 5 °C for 15 h.
- If humidity indicator in the package shows pink color (normal blue), then devices should be treated with the same conditions as 2.3.

REFLOW SOLDER PROFILE

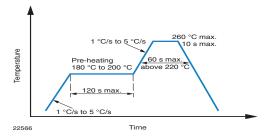


Fig. 10 - Lead (Pb)-Free Reflow Solder Profile According to J-STD-020

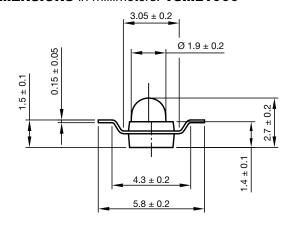
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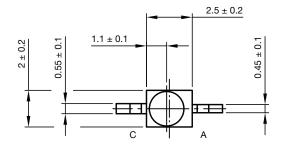


Vishay Semiconductors

PACKAGE DIMENSIONS in millimeters: TSML1000



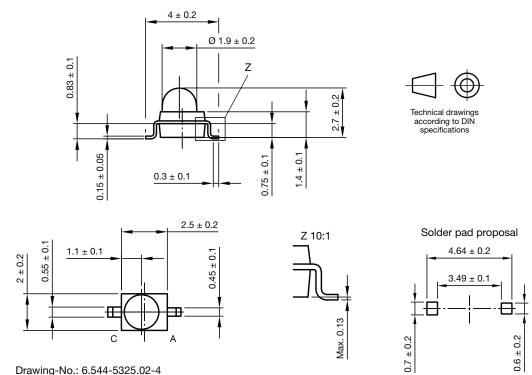




Drawing-No.: 6.544-5326.02-4 Issue: 5; 15.09.2021

PACKAGE DIMENSIONS in millimeters: **TSML1020**

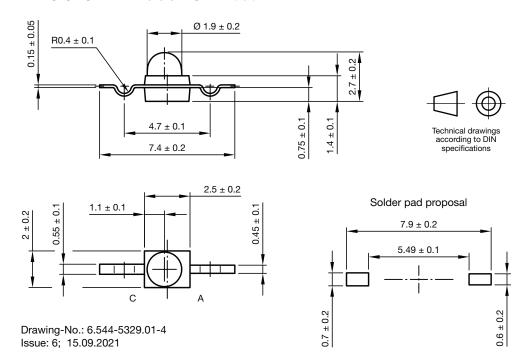
Issue: 5; 15.09.2021



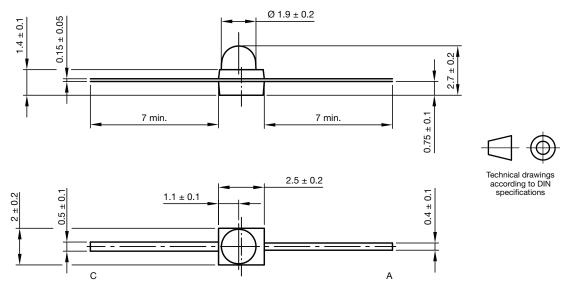


Vishay Semiconductors

PACKAGE DIMENSIONS in millimeters: TSML1030

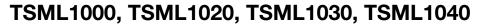


PACKAGE DIMENSIONS in millimeters: TSML1040



Drawing-No.: 6.544-5339.02-4

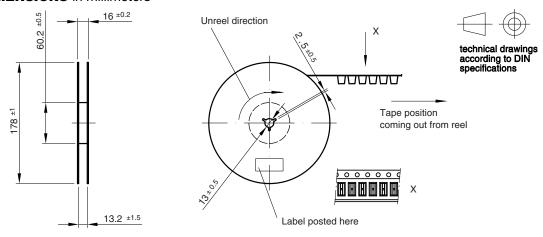
Issue: 4; 04.08.2021



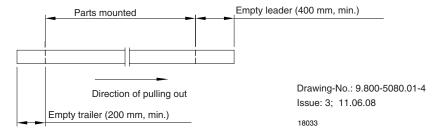


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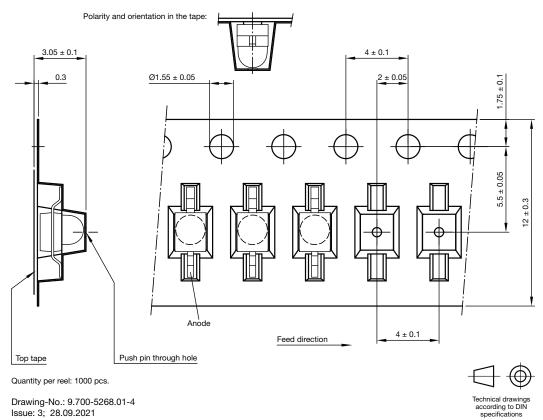
REEL DIMENSIONS in millimeters



Leader and trailer tape:



TAPING DIMENSIONS in millimeters: TSML1000

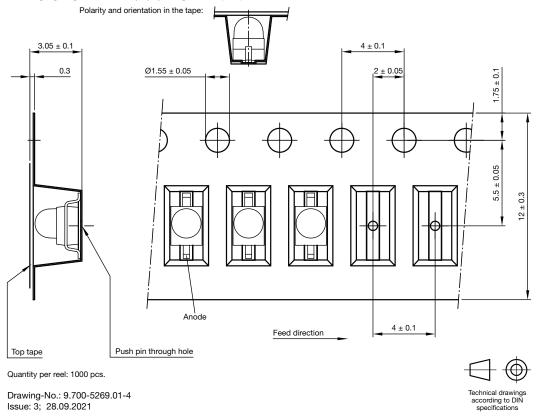




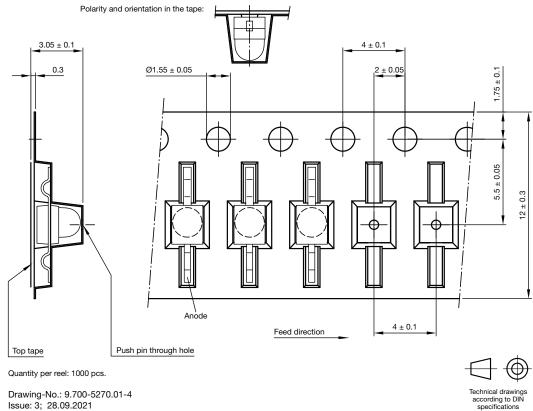


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TAPING DIMENSIONS in millimeters: TSML1020



TAPING DIMENSIONS in millimeters: TSML1030





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