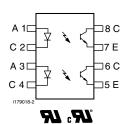
COMPLIANT



## Vishay Semiconductors

# Optocoupler, Phototransistor Output, Dual Channel, SOIC-8 Package





#### **LINKS TO ADDITIONAL RESOURCES**













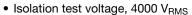
#### **DESCRIPTION**

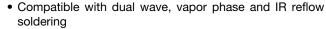
The ILD217T are optically coupled pairs with a GaAs infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The ILD217T come in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space. In addition to eliminating through-holes requirements, this package conforms to standards for surface mounted devices.

The high  $BV_{CEO}$  of 70 V gives a higher safety margin compared to the industry standard of 30 V.

### **FEATURES**

- Two channel coupler
- SOIC-8 surface mountable package
- Standard lead spacing of 0.05"
- Available only on tape and reel option (conforms to EIA standard 481-2)

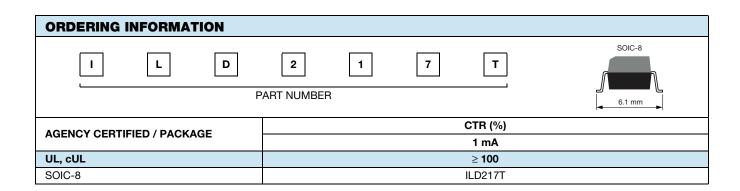






#### **AGENCY APPROVALS**

- UL
- cUL





## Vishay Semiconductors

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Peak reverse voltage		$V_{R}$	6	V			
Peak pulsed current	1 µs, 300 pps		1	А			
Continuous forward current per channel		l <sub>F</sub>	30	mA			
Power dissipation		P <sub>diss</sub>	50	mW			
OUTPUT							
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V			
Emitter collector breakdown voltage		$BV_{ECO}$	7	V			
Power dissipation per channel		P <sub>diss</sub>	125	mW			
COUPLER							
Isolation test voltage	t = 1 s	$V_{ISO}$	4000	$V_{RMS}$			
Total package dissipation ambient (2 LEDs and 2 detectors, 2 channels)		P <sub>tot</sub>	350	mW			
Storage temperature		T <sub>stg</sub>	-55 to +150	°C			
Operating temperature		T <sub>amb</sub>	-55 to +100	°C			
Soldering time from 260 °C <sup>(1)</sup>		T <sub>sld</sub>	10	S			

#### Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I <sub>F</sub> = 10 mA	$V_{F}$	-	1.2	1.55	V
Reverse current	V <sub>R</sub> = 6 V	I <sub>R</sub>	-	0.1	100	μΑ
Capacitance	$V_R = 0 V$	Co	-	25	-	pF
OUTPUT						
Collector emitter breakdown voltage	I <sub>C</sub> = 10 μA	BV <sub>CEO</sub>	70	-	-	V
Emitter collector breakdown voltage	I <sub>E</sub> = 10 μA	BV <sub>ECO</sub>	7	-	-	V
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$	I <sub>CEO</sub>	-	5	50	nA
Collector emitter capacitance	V <sub>CE</sub> = 0 V	C <sub>CE</sub>	-	10	-	pF
COUPLER						
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 2.5 \text{ mA}$	V <sub>CEsat</sub>	-	-	0.4	V
Capacitance (input to output)		C <sub>IO</sub>	-	0.5	-	pF
Resistance (input to output)		R <sub>IO</sub>	-	100	-	GΩ

#### Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	$V_{CE} = 5 \text{ V}, I_F = 1 \text{ mA}$	CTR	100	120	-	%



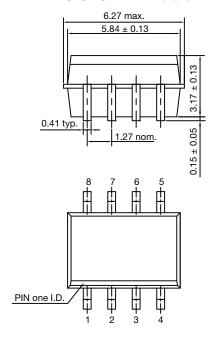
## Vishay Semiconductors

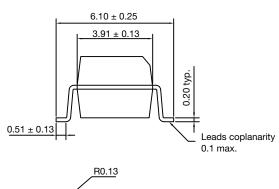
SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification	According to IEC 68 part 1		-	55 / 100 / 21	-		
Comparative tracking index		CTI	175	-	399		
V <sub>IOTM</sub>			6000	-	-	V	
V <sub>IORM</sub>			560	-	-	V	
P <sub>SO</sub>			-	-	350	mW	
I <sub>SI</sub>			-	-	150	mA	
T <sub>SI</sub>			-	-	165	°C	
Creepage distance			4	-	-	mm	
Clearance distance			4	-	-	mm	
Insulation thickness			0.2	-	-	mm	

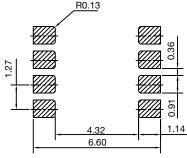
#### Note

As per IEC 60747-5-5, §7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

### **PACKAGE DIMENSIONS** in millimeters

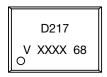








### **PACKAGE MARKING** (Example)



#### Notes

- XXXX = LMC (lot marking code)
- Tape and reel suffix (T) is not part of the package marking



## **Legal Disclaimer Notice**

Vishay

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