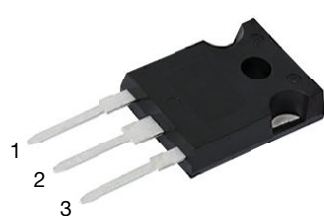
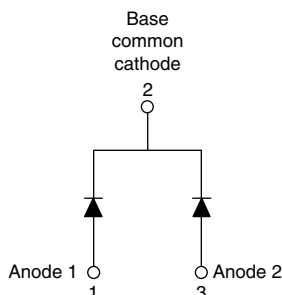


# Ultrafast Rectifier, 2 x 30 A FRED Pt®


**TO-247AC 3L**


## FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

## DESCRIPTIONS/APPLICATIONS

VS-CPU60... series are the state of the art ultrafast recovery rectifiers designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

## PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 30 A
$V_R$	600 V
$V_F$ at $I_F$	1.75 V
$t_{rr}$ typ.	26 ns
$T_J$ max.	175 °C
Package	TO-247AC 3L
Circuit configuration	Common cathode

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	$V_{RRM}$		600	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 131\text{ °C}$	60	A
Non-repetitive peak surge current per leg	$I_{FSM}$	$T_J = 25\text{ °C}, t_p = 10\text{ ms}$	250	
Operating junction and storage temperatures	$T_J, T_{Stg}$		-65 to +175	°C

## ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\text{ }\mu\text{A}$	600	-	-	V
Forward voltage	$V_F$	$I_F = 30\text{ A}$	-	1.4	1.75	
		$I_F = 30\text{ A}, T_J = 150\text{ °C}$	-	1.1	1.4	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	0.02	30	$\mu\text{A}$
		$T_J = 150\text{ °C}, V_R = V_R$ rated	-	30	200	
Junction capacitance	$C_T$	$V_R = 600\text{ V}$	-	20	-	pF

**DYNAMIC RECOVERY CHARACTERISTICS** ( $T_J = 25\text{ }^{\circ}\text{C}$  unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	$t_{rr}$	$I_F = 1.0\text{ A}$ , $dI_F/dt = 100\text{ A}/\mu\text{s}$ , $V_R = 30\text{ V}$	-	26	34	ns
		$T_J = 25\text{ }^{\circ}\text{C}$	-	42	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	100	-	
Peak recovery current	$I_{RRM}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	5	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	10	-	
Reverse recovery charge	$Q_{rr}$	$T_J = 25\text{ }^{\circ}\text{C}$	-	125	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	580	-	

**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	$T_J$ , $T_{Stg}$		-65	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to case per leg	$R_{thJC}$		-	0.7	1	$^{\circ}\text{C}/\text{W}$
Thermal resistance, junction to ambient per leg	$R_{thJA}$	Typical socket mount	-	-	70	
Thermal resistance, case to heatsink	$R_{thCS}$	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	6.0	-	g
			-	0.21	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC 3L	CPU6006			

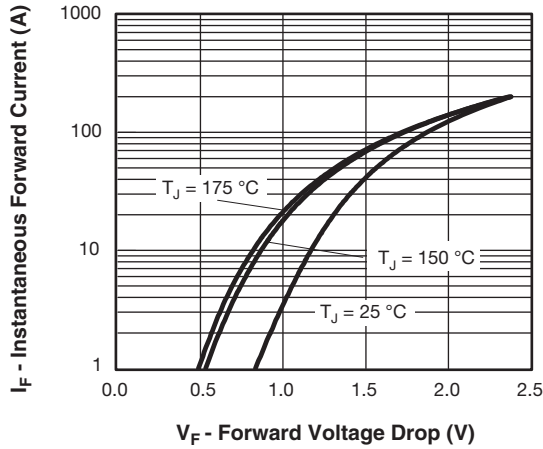


Fig. 1 - Typical Forward Voltage Drop Characteristics

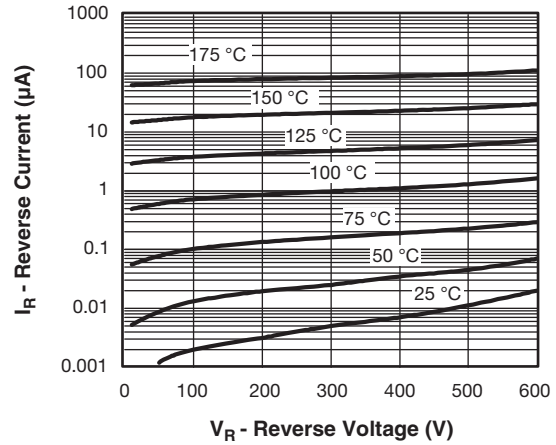


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

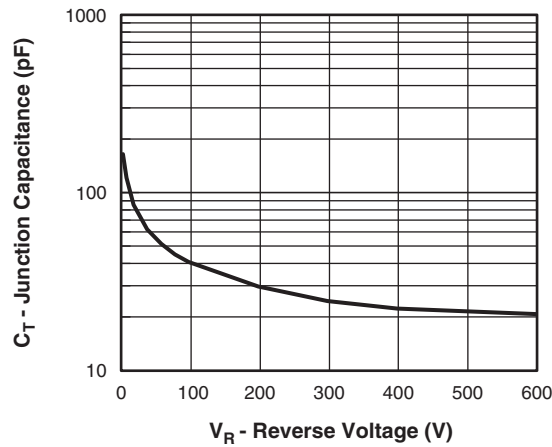
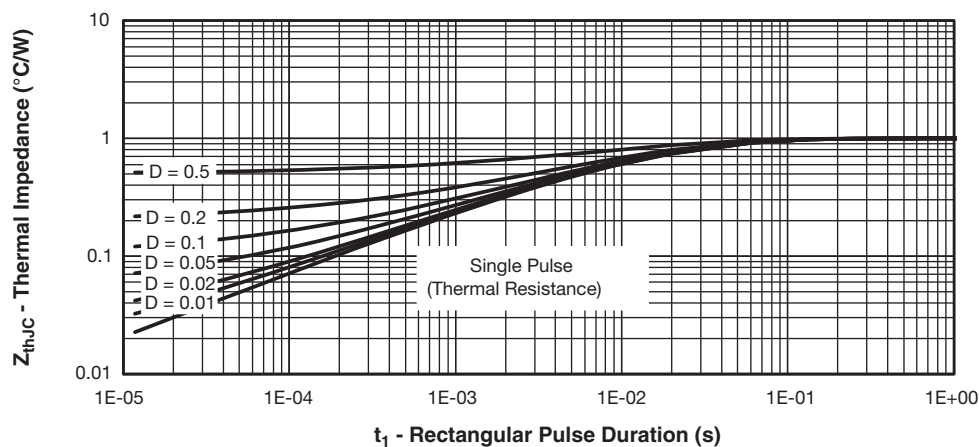


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

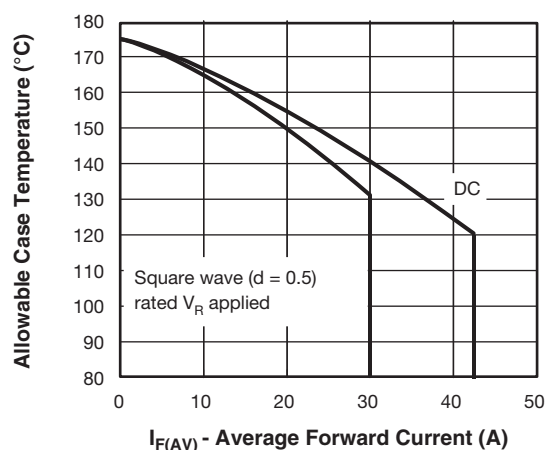


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

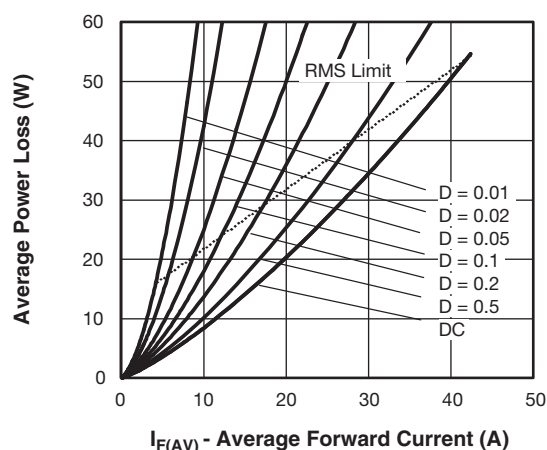


Fig. 7 - Forward Power Loss Characteristics

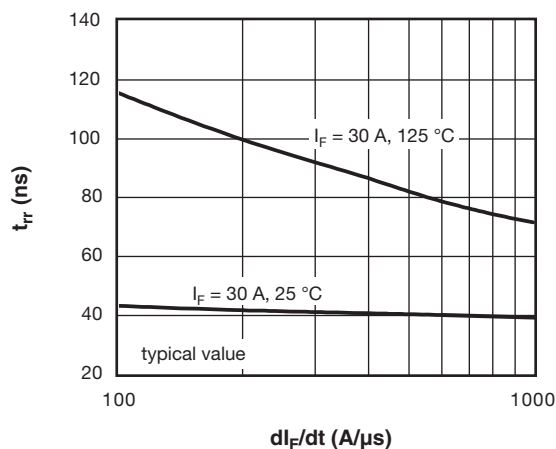


Fig. 6 - Typical Reverse Recovery Time vs.  $dI_F/dt$

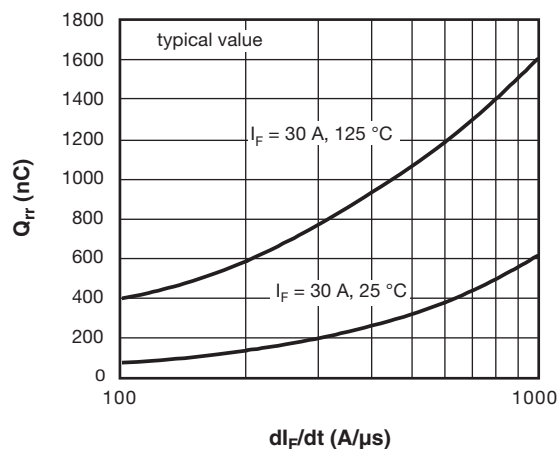


Fig. 8 - Typical Stored Charge vs.  $dI_F/dt$

#### Note

- (1) Formula used:  $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$ ;  
 $P_d$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  
 $P_{d_{REV}}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

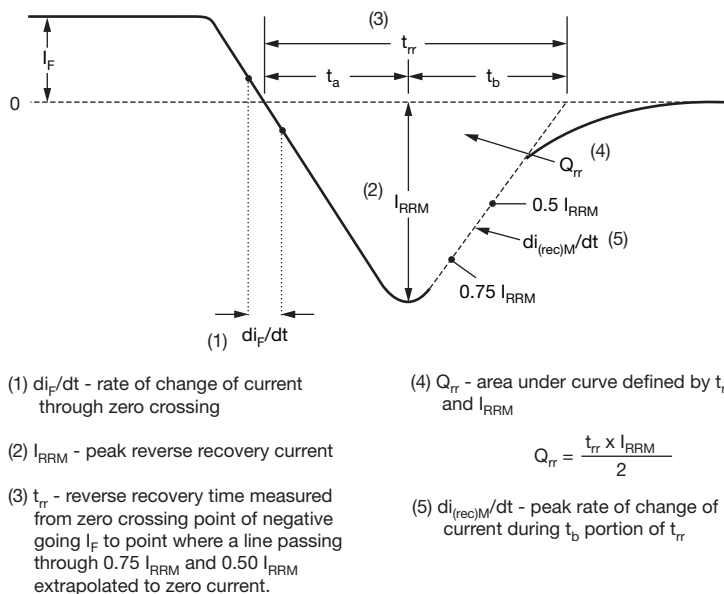


Fig. 9 - Reverse Recovery Waveform and Definitions

## ORDERING INFORMATION TABLE

Device code	VS-	C	P	U	60	06	-N3
	1	2	3	4	5	6	7

- 1** - Vishay Semiconductors product
- 2** - Circuit configuration:  
C = common cathode
- 3** - P = TO-247AC
- 4** - U = ultrafast recovery time
- 5** - Current code (60 = 2 x 30 A)
- 6** - Voltage code (06 = 600 V)
- 7** - Environmental digit:  
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

## ORDERING INFORMATION (Example)

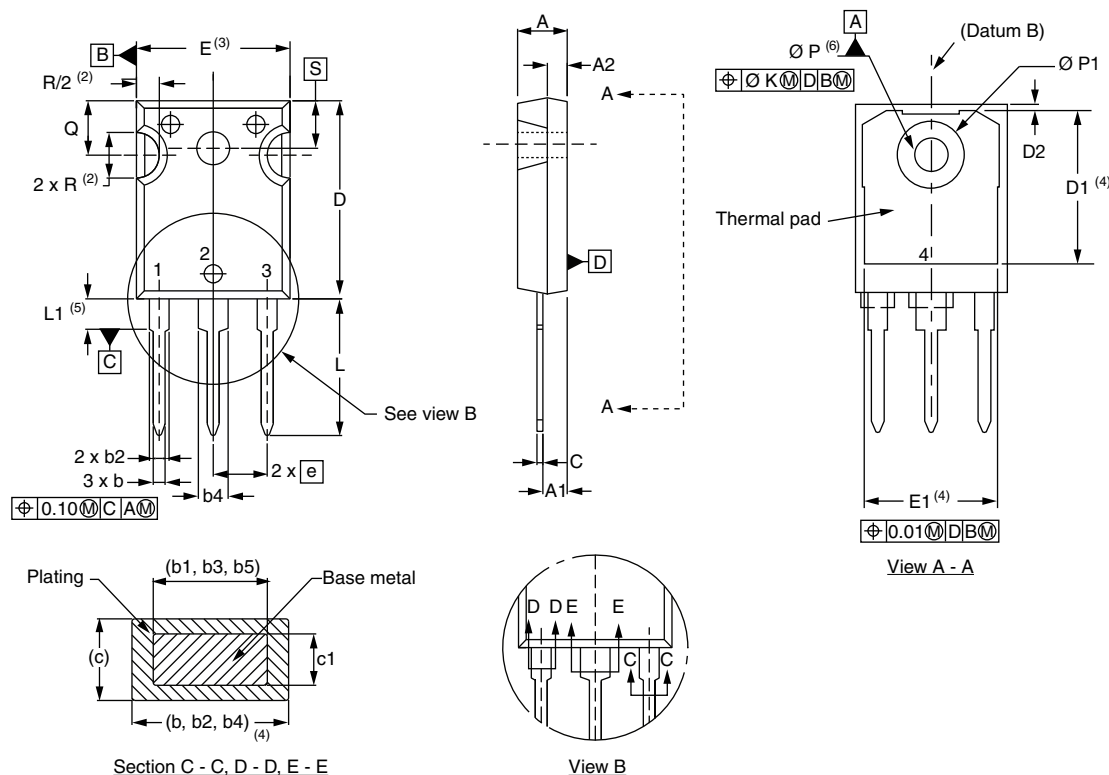
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-CPU6006-N3	25	500	Antistatic plastic tube

## LINKS TO RELATED DOCUMENTS

Dimensions	<a href="http://www.vishay.com/doc?96138">www.vishay.com/doc?96138</a>
Part marking information	<a href="http://www.vishay.com/doc?95007">www.vishay.com/doc?95007</a>

# TO-247AC 3L

**DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
c	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

## Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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