Vishay Siliconix

# Automotive Dual N-Channel 60 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V <sub>DS</sub> (V)	60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.064			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.082			
I <sub>D</sub> (A) per leg	6			
Configuration	Dual			

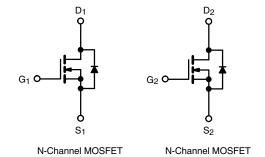
#### **FEATURES**

- TrenchFET® power MOSFET
- 100 % R<sub>g</sub> and UIS tested
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ9945BEY (for detailed order number please see <a href="https://www.vishay.com/doc?79771">www.vishay.com/doc?79771</a> )

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V <sub>DS</sub>	60	V	
Gate-Source Voltage		$V_{GS}$	± 20	V	
Continuous Drain Current	T <sub>C</sub> = 25 °C	1	5.4		
	T <sub>C</sub> = 125 °C	l <sub>D</sub>	3.1		
Continuous Source Current (Diode Conduction) a		I <sub>S</sub>	3.6	Α	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	21.5		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	8.5		
Single Pulse Avalanche Energy	L = U.T IIIII	E <sub>AS</sub>	3.6	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	В	4	W	
	T <sub>C</sub> = 125 °C	- P <sub>D</sub>	1.3	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount <sup>c</sup>	$R_{thJA}$	112	°C/W
Junction-to-Foot (Drain)		$R_{thJF}$	38	C/VV

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq 300 \,\mu\text{s}$ , duty cycle  $\leq 2 \,\%$ .
- c. When mounted on 1" square PCB (FR4 material).



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static					I.		
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		60	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_D = 250 \mu A$		2	2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	0 V, V <sub>GS</sub> = ± 20 V	-	-	± 100	nA
		$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V	-	-	1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	μΑ
-		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	150	1
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 \text{ V}$	20	-	-	Α
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 3.4 A	-	0.045	0.064	
Drain-Source On-State Resistance a	В	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 3.4 A, T <sub>J</sub> = 125 °C	-	-	0.110	Ω
Diani-Source On-State nesistance -	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 3.4 A, T <sub>J</sub> = 175 °C	-	-	0.137	
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 3.7 A	-	0.060	0.082	
Forward Transconductance f	9fs	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 3.7 A		-	12	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			-	375	470	
Output Capacitance	Coss	$V_{GS} = 0 V$	$V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	-	70	88	pF
Reverse Transfer Capacitance	C <sub>rss</sub>			-	30	36	
Total Gate Charge <sup>c</sup>	Qg			-	8	12	
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	V <sub>GS</sub> = 10 V	$V_{DS} = 30 \text{ V}, I_D = 4.3 \text{ A}$	1	1.2	1.5	nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			-	1.7	2.6	
Gate Resistance	$R_g$	f = 1 MHz		1.1	-	6.66	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	6	9	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 30 \text{ V, } R_L = 8.8 \Omega$ $I_D \cong 3.4 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 1 \Omega$		-	2.8	4.2	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	17	26	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	1.7	3	
Source-Drain Diode Ratings and Chara	cteristics <sup>b</sup>					_	
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	21.5	Α
Forward Voltage	$V_{SD}$	I <sub>F</sub> = 2 A, V <sub>GS</sub> = 0 V		-	0.75	1.1	V

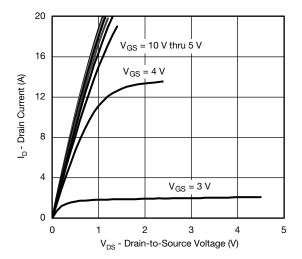
# Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

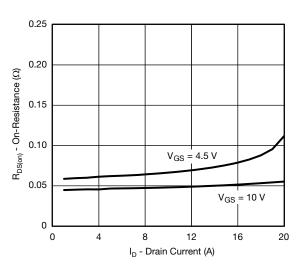
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



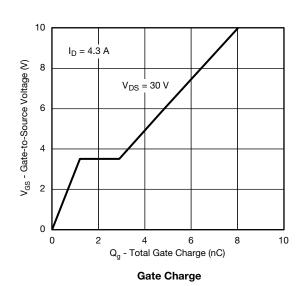
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)

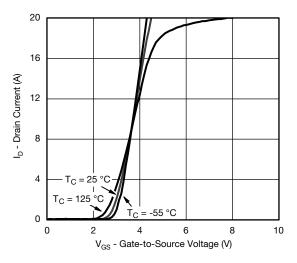


#### **Output Characteristics**

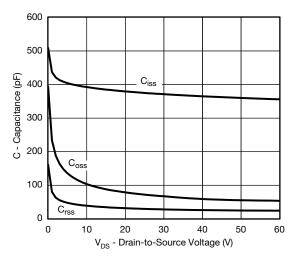


## On-Resistance vs. Drain Current

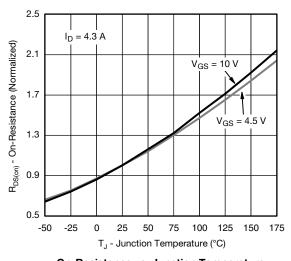




#### **Transfer Characteristics**



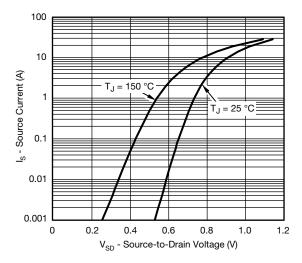
## Capacitance



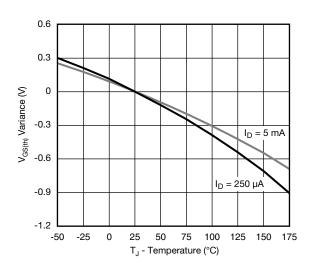
On-Resistance vs. Junction Temperature



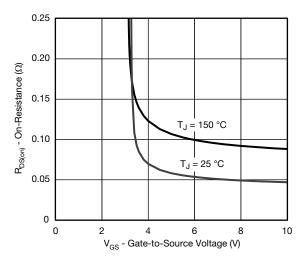
# **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



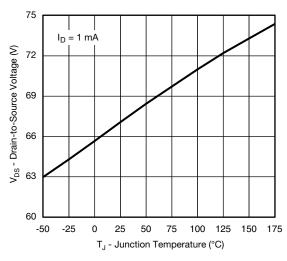
### **Source Drain Diode Forward Voltage**



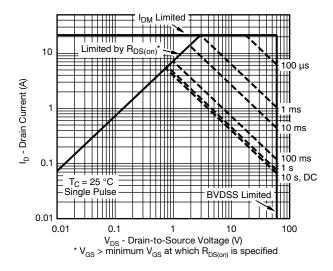
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature





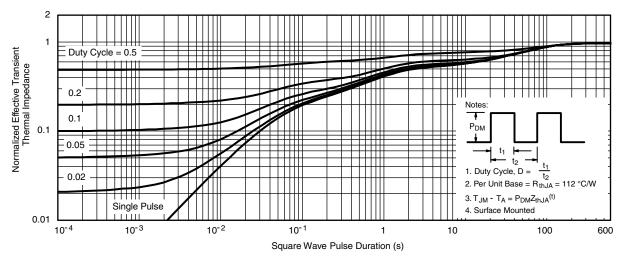


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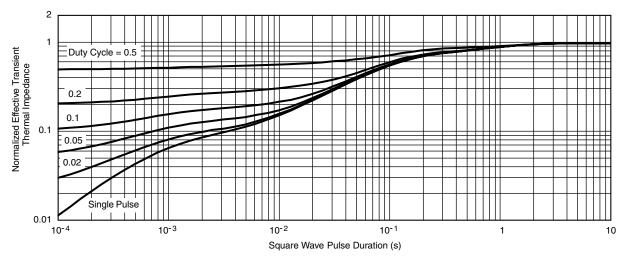
Safe Operating Area



# THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



### Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?71504">www.vishay.com/ppg?71504</a>.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIMETERS		INCHES		
DIM	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A <sub>1</sub>	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
Е	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I. 11-Sep-06					

DWG: 5498

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# LON NOTE



# **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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