COMPLIANT

HALOGEN

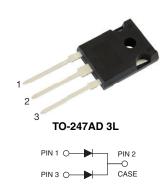
FREE



## Vishay General Semiconductor

# Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.41 \text{ V}$  at  $I_F = 10 \text{ A}$ 



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 40 A				
$V_{RRM}$	100 V				
I <sub>FSM</sub>	550 A				
$V_F$ at $I_F = 40 \text{ A } (T_J = 125 \text{ °C})$	0.65 V				
T <sub>J</sub> max.	150 °C				
Package	TO-247AD 3L				
Circuit configuration	Common cathode				

#### **FEATURES**

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- · High efficiency operation
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- AEC-Q101 qualified available:
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

#### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

#### **MECHANICAL DATA**

Case: TO-247AD 3L

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	VX80100PW	UNIT		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	100	V		
Maximum average forward rectified current	per device		80	^		
(fig. 1)	per diode	I <sub>F(AV)</sub>	40	A		
Peak forward surge current 8.3 ms single half superimposed on rated load	sine-wave	I <sub>FSM</sub>	550	А		
Operating junction temperature range		T <sub>J</sub> <sup>(1)</sup>	-40 to +150	°C		
Storage temperature range		T <sub>STG</sub>	-40 to +150			

#### Note

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>θ,JA</sub>



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
	I <sub>F</sub> = 10 A		- V <sub>F</sub> <sup>(1)</sup>	0.48	-	V
	I <sub>F</sub> = 20 A	$T_J = 25  ^{\circ}C$		0.57	-	
Instantaneous forward valtage per diede	I <sub>F</sub> = 40 A			0.73	0.80	
Instantaneous forward voltage per diode	I <sub>F</sub> = 10 A	T <sub>J</sub> = 125 °C		0.41	-	
	I <sub>F</sub> = 20 A			0.52	-	
	I <sub>F</sub> = 40 A			0.65	0.71	
	V <sub>B</sub> = 70 V	T <sub>J</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.016	-	mA
Reverse current at rated V <sub>R</sub> per diode	v <sub>R</sub> = 70 v	T <sub>J</sub> = 125 °C		15.5	-	
	V = 100 V	T <sub>J</sub> = 25 °C		-	0.8	
	V <sub>R</sub> = 100 V	T <sub>J</sub> = 125 °C		29	80	
Typical junction capacitance	4.0 V,	1 MHz	$C_{J}$	3250	-	pF

#### Notes

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width  $\leq 5 \text{ ms}$ 

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL VX80100PW UNIT					
Typical thermal resistance per device	R <sub>0</sub> JC (1)	0.6	°C/W			

#### Note

(1) Thermal resistance junction-to-case to follow JEDEC® 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
VX80100PW-M3/P	5.64	Р	25/tube	Tube		
VX80100PWHM3/P (1)	5.64	Р	25/tube	Tube		

#### Note

(1) AEC-Q101 qualified



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### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C, unless otherwise noted)

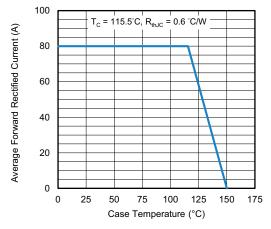


Fig. 1 - Maximum Forward Current Derating Curve

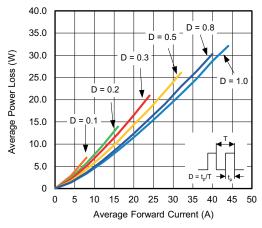


Fig. 2 - Average Power Loss Characteristics

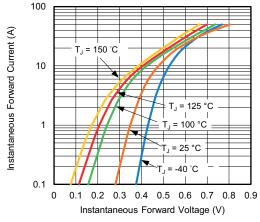


Fig. 3 - Typical Instantaneous Forward Characteristics

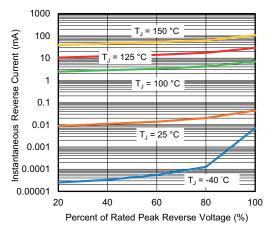


Fig. 4 - Typical Reverse Leakage Characteristics

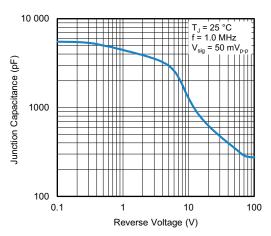


Fig. 5 - Typical Junction Capacitance

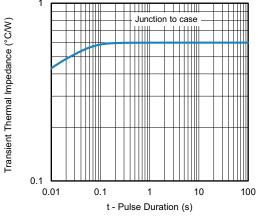
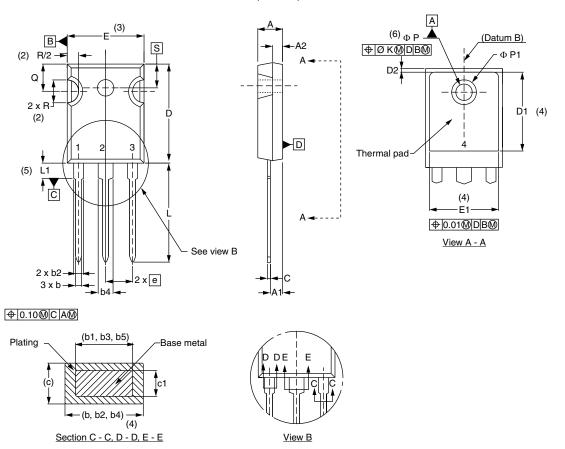


Fig. 6 - Typical Transient Thermal Impedance



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### PACKAGE OUTLINE DIMENSIONS in millimeters (inches) TO-247AD 3L



SYMBOL	MILLIMETERS		INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	NOTES	
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØК	0.2	0.254		)10	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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