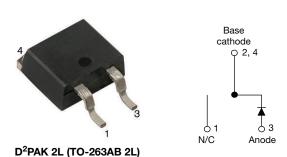


Vishay Semiconductors

Ultrafast Rectifier, 20 A FRED Pt®



PRIMARY CHARACTERISTICS					
I _{F(AV)}	20 A				
V _R	600 V				
V _F at I _F	1.2 V				
t _{rr} (typ.)	ns				
T _J max.	175 °C				
Package	D ² PAK 2L (TO-263AB 2L)				
Circuit configuration	Single				

FEATURES

- Low forward voltage drop
- · Ultrafast recovery time
- 175 °C operating junction temperature

Low leakage current



- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

State of the art, ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC)

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other switching applications

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adapters, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

MECHANICAL DATA

Case: D²PAK 2L (TO-263AB 2L)

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per

erminais. Matte till plated leads, solderable pe

J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Repetitive peak reverse voltage	V_{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 129 °C	20	^
Non-repetitive peak surge current	I _{FSM}	T _C = 25 °C	180	A
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	.,
Forward voltage V _F	I _F = 20 A	-	1.35	1.7	V	
	VF	I _F = 20 A, T _J = 150 °C	-	1.2	1.4	
Deverage leakage assurent		V _R = V _R rated	-	0.02	5	
Reverse leakage current I _R	T _J = 150 °C, V _R = V _R rated	-	20	200	μΑ	
Junction capacitance	C _T	V _R = 600 V	-	12	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nΗ



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		I _F = 1 A, dI _F /dt = 100 A/μs, V _R = 30 V		=	26	-	
Reverse recovery time	t _{rr}	T _J = 25 °C		=	42	-	ns
		T _J = 125 °C	I _F = 20 A dI _F /dt = 200 A/µs	=	89	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	4.9	-	Λ.
	T _J = 125 °C	$V_{R} = 390 \text{ V}$	=.	8.4	-	Α	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	110	-	С
		T _J = 125 °C		-	440	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	-	1.51	°C/W
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	70	
Thermal resistance, case to heat sink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D ² PAK 2L (TO-263AB 2L)		ETU2	006SH	

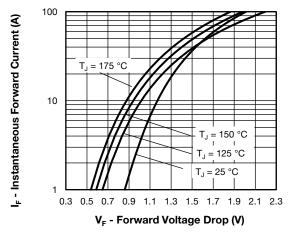


Fig. 1 - Typical Forward Voltage Drop Characteristics

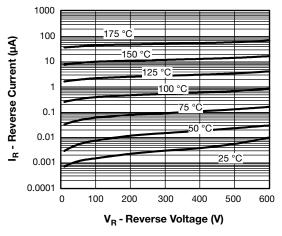


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



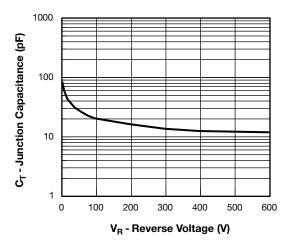


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

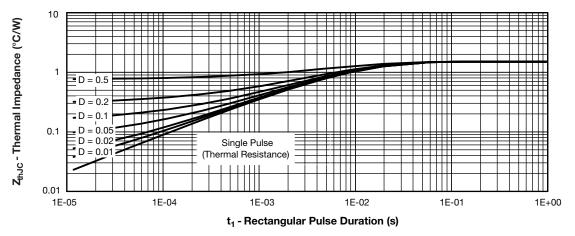


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics

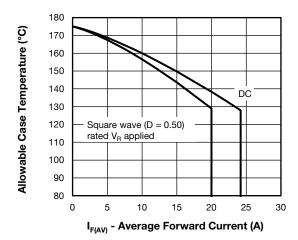


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

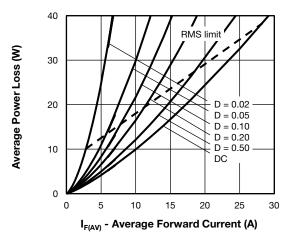


Fig. 6 - Forward Power Loss Characteristics

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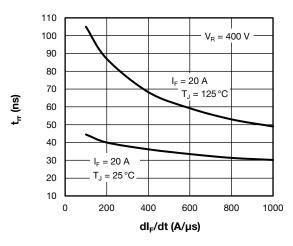


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

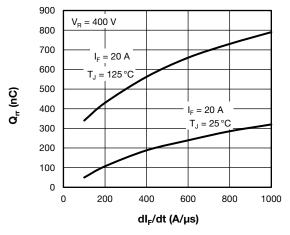
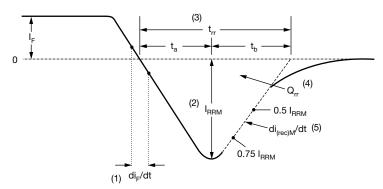


Fig. 8 - Typical Stored Charge vs. dl_F/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

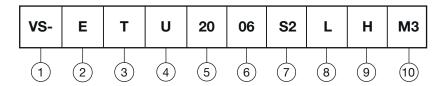
Fig. 9 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Circuit configuration
E = single diode

T = D²PAK (TO-263) package

- U = ultrafast recovery time

5 - Current code (20 = 20 A)

Voltage code (06 = 600 V)

7 - • S2 = true 2 pin D²PAK

8 - • None = tube (50 pieces)

- • L = tape and reel (left oriented, for D²PAK package)

If needed different orientation / packaging, please contact factory

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION		
VS-ETU2006S2LHM3	800	13" diameter reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96683</u>				
Part marking information	www.vishay.com/doc?96693			
Packaging information	www.vishay.com/doc?95032			



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