# High Performance Schottky Rectifier, 1.5 A

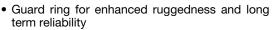


**SMA (DO-214AC)** 

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.5 A			
V <sub>R</sub>	40 V			
V <sub>F</sub> at I <sub>F</sub>	0.34 V			
I <sub>RM</sub>	20 mA at 125 °C			
E <sub>AS</sub>	6.0 mJ			
T <sub>J</sub> max.	150 °C			
Package	SMA (DO-214AC)			
Circuit configuration	Single			

#### **FEATURES**







• Surface mountable

Compact size

HALOGEN FREE

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **APPLICATIONS**

- Switching power supplies
- Meter protection
- Reverse protection for power input to PC board circuits
- · Battery isolation and charging
- Low threshold voltage diode
- · Freewheeling or by-pass diode
- · Low voltage clamp

#### **DESCRIPTION**

The VS-15MQ040-M3 Schottky rectifier is designed to be used for low power applications where a reverse voltage of 40 V is encountered and surface mountable is required.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	1.5	Α		
$V_{RRM}$		40	V		
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	330	Α		
V <sub>F</sub>	2 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.43	V		
TJ	Range	-40 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-15MQ040-M3	UNITS
Maximum DC reverse voltage	$V_R$	40	V
Maximum working peak reverse voltage	$V_{RWM}$	40	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at T <sub>L</sub> = 105 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		2.1	- A
See fig. 4	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>L</sub> = 113 °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1.5	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	330	_
non-repetitive surge current I <sub>FSM</sub> See fig. 6		10 ms sine or 6 ms rect. pulse	load condition and with rated V <sub>RRM</sub> applied	140	Α
Non-repetitive avalanche energy	E <sub>AS</sub>	$T_J = 25 ^{\circ}\text{C},  I_{AS} = 1  \text{A},  L = 12  \text{mH}$		6.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical  1.0		Α	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1		1.5 A	T <sub>.1</sub> = 25 °C	0.43	V
	V (1)	2 A	1J = 25 C	0.49	
	V <sub>FM</sub> <sup>(1)</sup>	1.5 A	T 105 %C	0.34	
		2 A	T <sub>J</sub> = 125 °C	0.43	
Maximum reverse leakage current See fig. 2		T <sub>J</sub> = 25 °C	V Dated V	0.5	- mA
	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	20	
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.26	V
Forward slope resistance	r <sub>t</sub>			64.6	mΩ
Typical junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 10 V <sub>DC</sub> , T <sub>J</sub> = 25 °C, test signal = 1 MHz		134	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $^{(1)}\,$  Pulse width = 300  $\mu s,$  duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-40 to +150	°C
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (DO-214AC)	X	F

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

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# Vishay Semiconductors

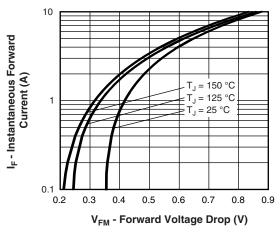


Fig. 1 - Maximum Forward Voltage Drop Characteristics

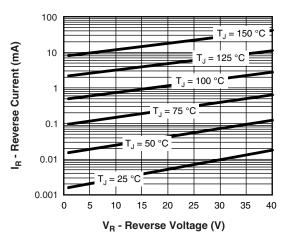


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

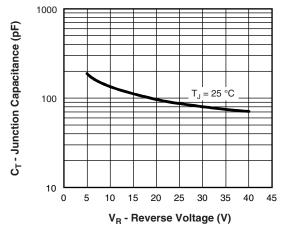
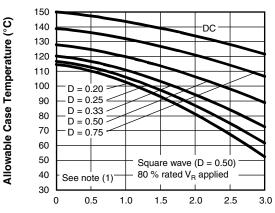


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

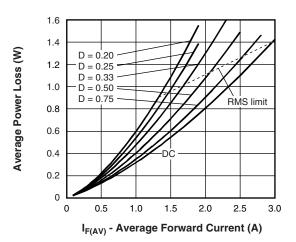


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

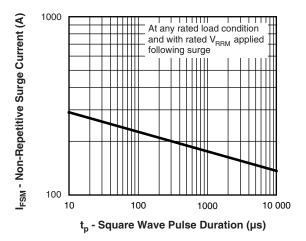


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

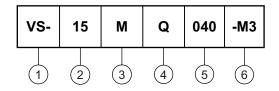
#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 



#### **ORDERING INFORMATION TABLE**

Device code



- 1 Vishay Semiconductors product
- 2 Current rating
- 3 M = SMA
- 4 Q = Schottky "Q" series
- 5 Voltage rating (040 = 40 V)
- 6 Environmental digit:

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-15MQ040-M3/5AT	5AT	7500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95400		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



### **SMA**

#### **DIMENSIONS** in inches (millimeters)

#### **DO-214AC (SMA)**



#### Mounting Pad Layout





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