

Automotive N-Channel 100 V (D-S) 175 °C MOSFET

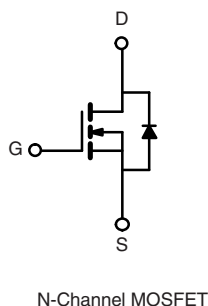
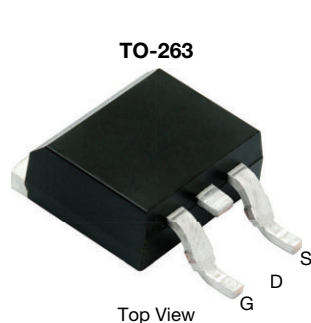
PRODUCT SUMMARY

V_{DS} (V)	100
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.030
$R_{DS(on)}$ (Ω) at $V_{GS} = 6$ V	0.034
I_D (A)	40
Configuration	Single
Package	TO-263

FEATURES

- TrenchFET® power MOSFET
- Package with low thermal resistance
- AEC-Q101 qualified ^d
- 100 % R_g and UIS tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912

AUTOMOTIVE
GRADE

RoHS
COMPLIANT
HALOGEN
FREE


ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	40	A
		22	
Continuous Source Current (Diode Conduction) ^a	I_S	60	
Pulsed Drain Current ^b	I_{DM}	155	
Single Pulse Avalanche Current	I_{AS}	40	
Single Pulse Avalanche Energy	E_{AS}	80	mJ
Maximum Power Dissipation ^b	P_D	107	W
		35	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	40	°C/W
Junction-to-Case (Drain)	R_{thJC}	1.4	

Notes

- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR4 material).
- Parametric verification ongoing.

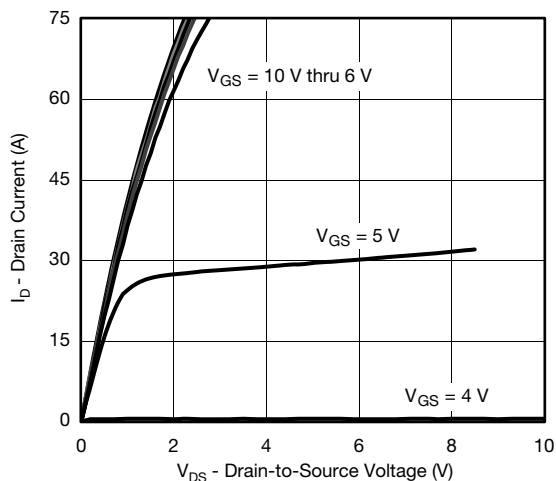
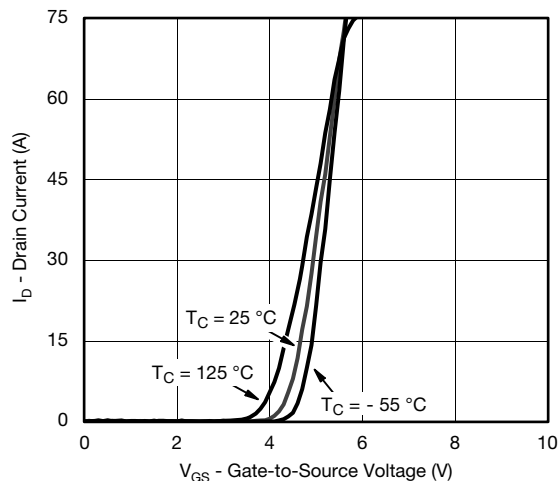
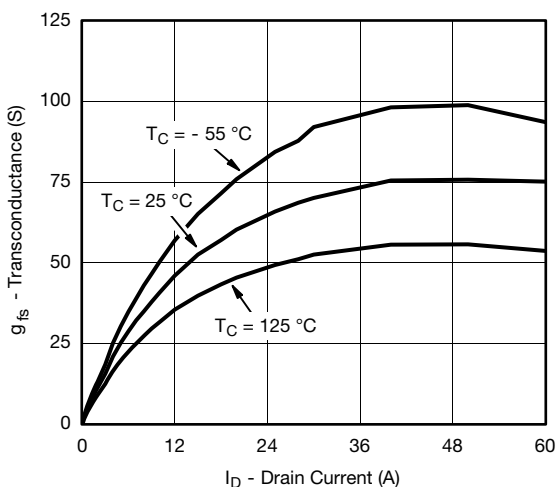
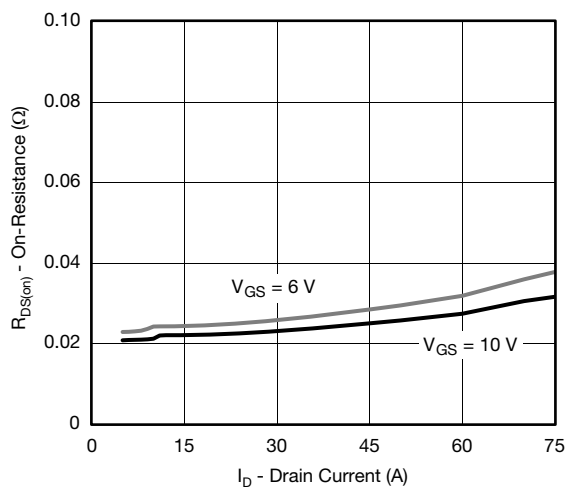
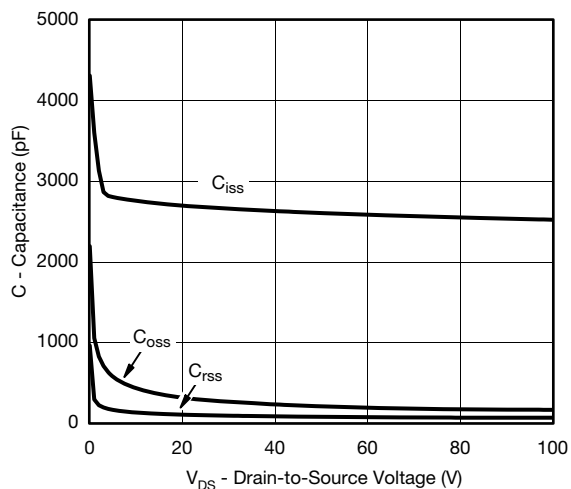
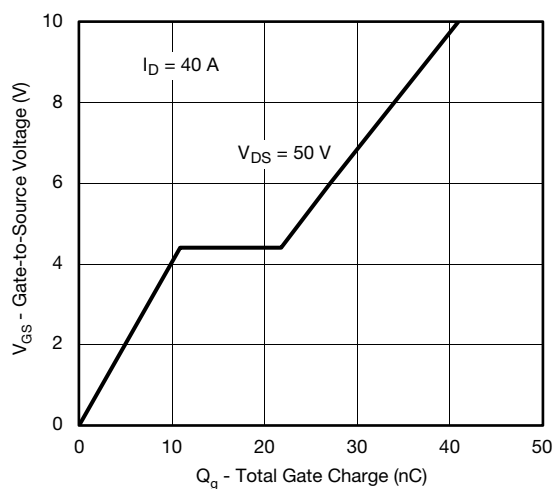


SPECIFICATIONS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA		100	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		2.5	3.0	3.5	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 100 V	-	-	1	μA
		V _{GS} = 0 V	V _{DS} = 100 V, T _J = 125 °C	-	-	50	
		V _{GS} = 0 V	V _{DS} = 100 V, T _J = 175 °C	-	-	250	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	50	-	-	A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 15 A	-	0.023	0.030	Ω
		V _{GS} = 10 V	I _D = 15 A, T _J = 125 °C	-	-	0.054	
		V _{GS} = 10 V	I _D = 15 A, T _J = 175 °C	-	-	0.067	
		V _{GS} = 6 V	I _D = 10 A	-	0.025	0.034	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 15 A		-	52	-	S
Dynamic ^b							
Input Capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 25 V, f = 1 MHz	-	2676	3345	pF
Output Capacitance	C _{oss}			-	285	355	
Reverse Transfer Capacitance	C _{rss}			-	95	120	
Total Gate Charge ^c	Q _g	V _{GS} = 10 V	V _{DS} = 50 V, I _D = 40 A	-	41	62	nC
Gate-Source Charge ^c	Q _{gs}			-	11	-	
Gate-Drain Charge ^c	Q _{gd}			-	11	-	
Gate Resistance	R _g	f = 1 MHz		0.7	1.3	2.6	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 50 V, R _L = 1.25 Ω I _D ≅ 40 A, V _{GEN} = 10 V, R _g = 1 Ω		-	12	18	ns
Rise Time ^c	t _r			-	5	8	
Turn-Off Delay Time ^c	t _{d(off)}			-	23	35	
Fall Time ^c	t _f			-	5	8	
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	155	A
Forward Voltage	V _{SD}	I _F = 30 A, V _{GS} = 0 V		-	0.85	1.5	V

Notes

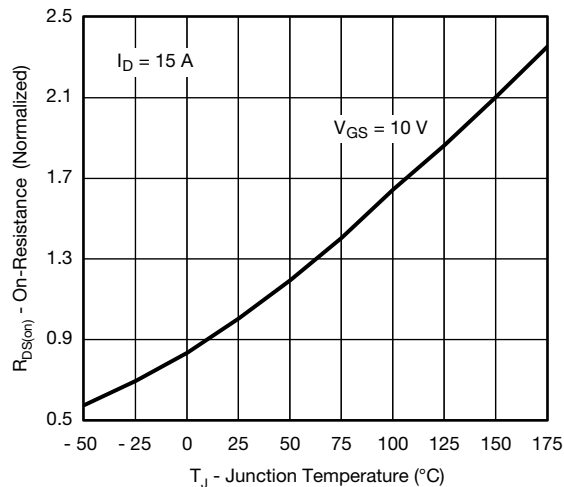
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

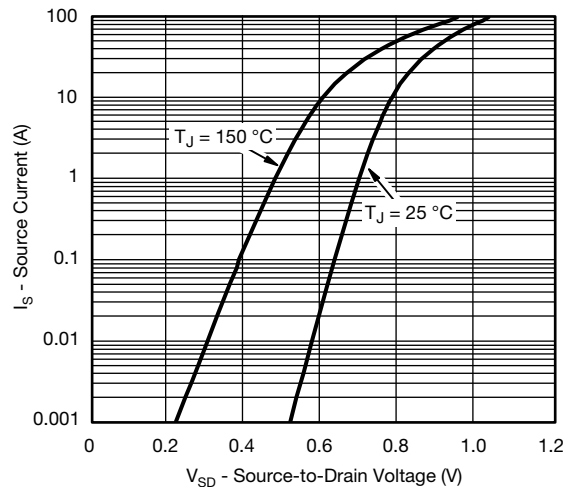
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Output Characteristics

Transfer Characteristics

Transconductance

On-Resistance vs. Drain Current

Capacitance

Gate Charge



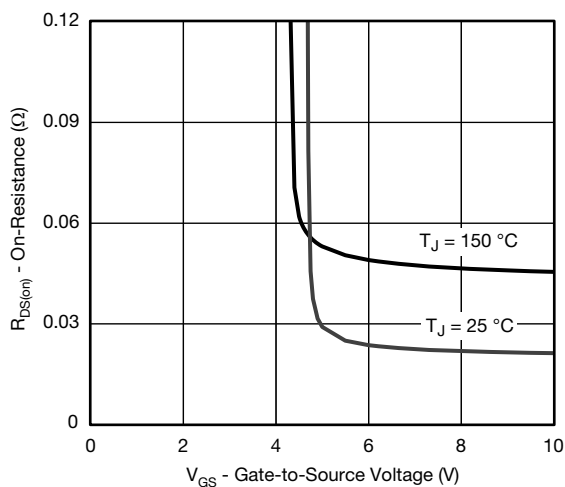
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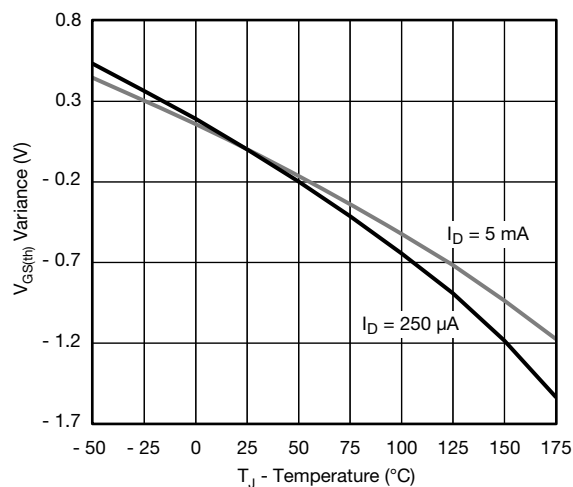
On-Resistance vs. Junction Temperature



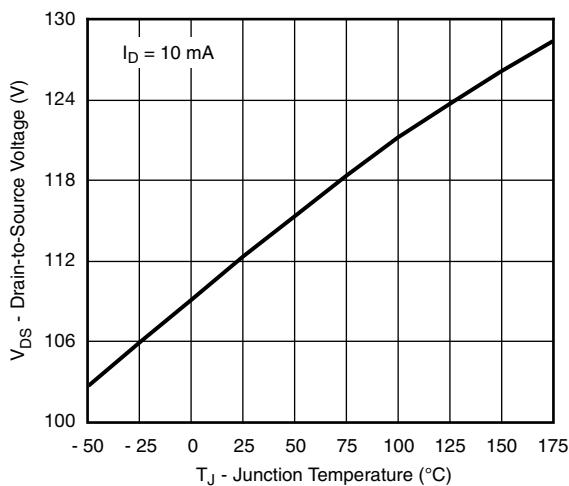
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



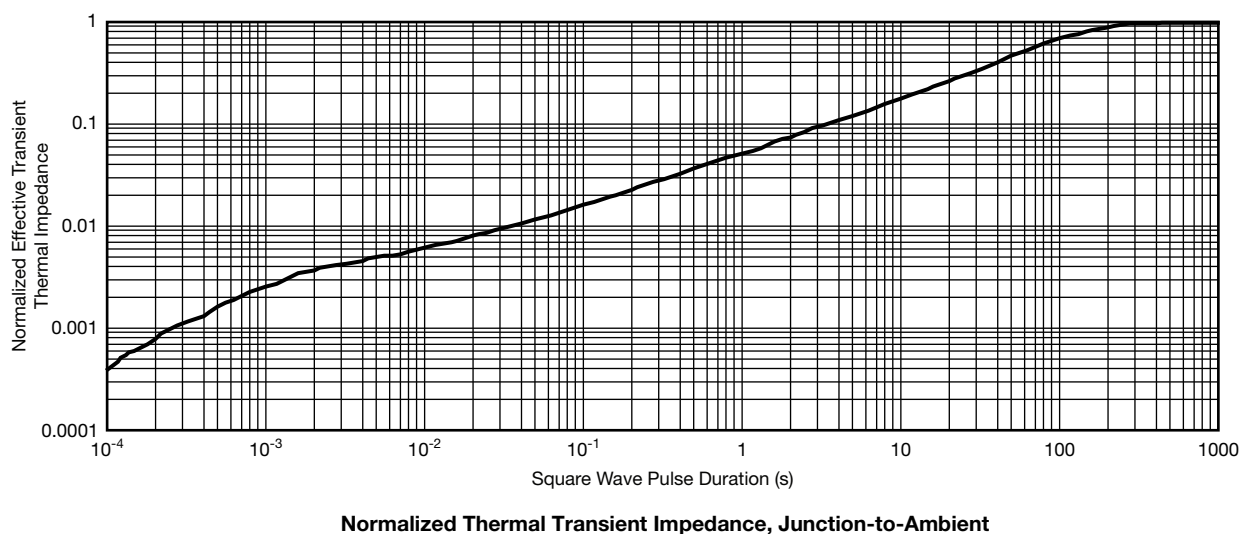
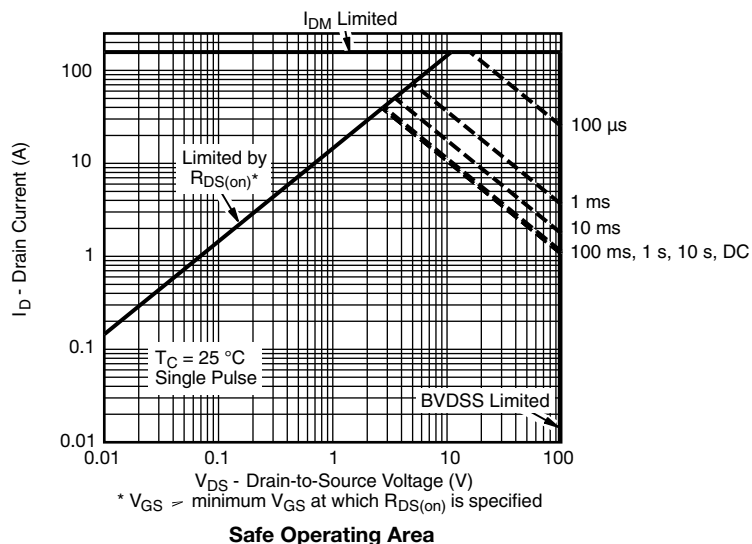
Threshold Voltage



Drain Source Breakdown vs. Junction Temperature

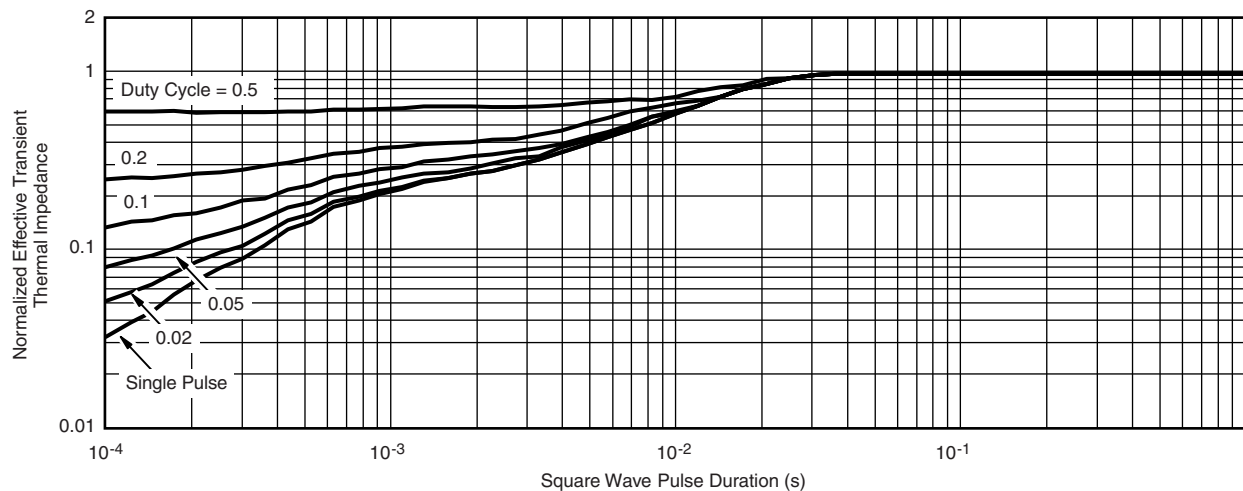


THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)





THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction to Case ($25\text{ }^{\circ}\text{C}$)are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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REVISION HISTORY ^a		
REVISION	DATE	DESCRIPTION OF CHANGE
D	04-Aug-15	• Revised R _g minimum limit

Note

a. As of April 2014

RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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