

Vishay General Semiconductor

Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.41 \text{ V}$ at $I_F = 5 \text{ A}$



LINKS TO ADDITIONAL RESOURCES

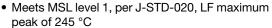


PRIMARY CHARACTERISTICS				
I _{F(AV)}	10 A			
V _{RRM}	45 V			
I _{FSM}	100 A			
V _F at I _F = 10 A	0.52 V			
T _{OP} max. (AC mode)	150 °C			
T _J max. (DC forward current)	200 °C			
Package	D ² PAK (TO-263AB)			
Circuit configuration	Single			

FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses





COMPLIANT

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VBT1045BP	UNIT	
Maximum repetitive peak reverse voltage	V_{RRM}	45	V	
Maximum DC forward bypassing current (fig. 1)	I _{F(DC)} (1)	10	Α	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	100	Α	
Operating junction temperature range (AC mode)	T _{OP}	-40 to +150	°C	
Junction temperature in DC forward current without reverse bias, t ≤ 1 h	T _J ⁽²⁾	≤ 200	°C	

Notes

(1) With heatsink

(2) Meets the requirements of IEC 61215 ed.2 bypass diode thermal test

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.50	-	- V
	I _F = 10 A			0.57	0.68	
	I _F = 5 A	T _A = 125 °C		0.41	-	
	I _F = 10 A			0.52	0.64	
Reverse current	V _R = 45 V	T _A = 25 °C	I _R ⁽²⁾	ı	500	μA
	v _R = 45 v	T _A = 125 °C		5	15	mA

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL VBT1045BP			
Typical thermal resistance	$R_{ heta JC}$	3.0	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
D ² PAK (TO-263AB)	VBT1045BP-E3/4W	1.37	4W	50/tube	Tube	
D ² PAK (TO-263AB)	VBT1045BP-E3/8W	1.37	8W	800/reel	Tape and reel	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

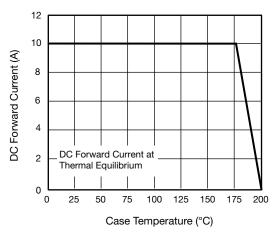
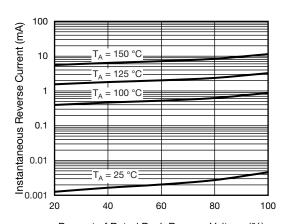


Fig. 1 - Maximum Forward Current Derating Curve



Percent of Rated Peak Reverse Voltage (%)

Fig. 3 - Typical Reverse Characteristics

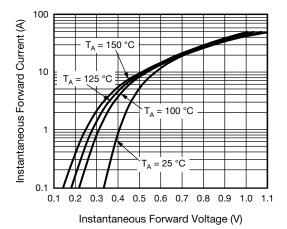


Fig. 2 - Typical Instantaneous Forward Characteristics

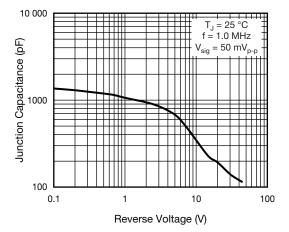


Fig. 4 - Typical Junction Capacitance



0.037 (0.940)

0.027 (0.686)

0.105 (2.67)

0.095 (2.41)

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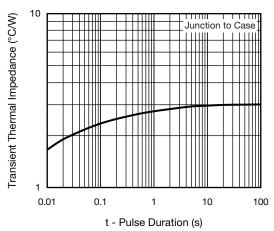


Fig. 5 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

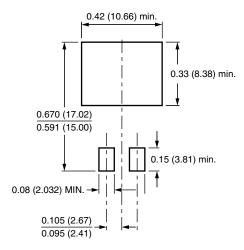
D²PAK (TO-263AB)

0.205 (5.20)

0.195 (4.95)

0.411 (10.45) 0.190 (4.83) 0.380 (9.65) 0.160 (4.06) 0.055 (1.40) 0.245 (6.22) 0.045 (1.14) MIN. 0.055 (1.40) 0.360 (9.14) 0.047 (1.19) 0.320 (8.13) 0.624 (15.85) Κ 2 0.591 (15.00) -0 to 0.01 (0 to 0.254) 0.110 (2.79) 0.090 (2.29)

Mounting Pad Layout



0.021 (0.53)

0.014 (0.36)

0.140 (3.56)

0.110 (2.79)



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