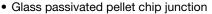


Fast Soft Recovery Rectifier Diode, 10 A



PRIMARY CHARACTERISTICS					
I _{F(AV)}	10 A				
V _R	600 V				
V _F at I _F	1.2 V				
I _{FSM}	130 A				
t _{rr}	50 ns				
T _J max.	150 °C				
Snap factor	0.6				
Package	2L TO-220AC				
Circuit configuration	Single				

FEATURES







- Flexible solution for reliable AC
- power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- AEC-Q101 qualified
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

APPLICATIONS

- On-board and off-board EV/HEV battery chargers
- Input rectification

DESCRIPTION

The VS-10ETF06THM3 fast soft recovery rectifier series has been optimized for combined short reverse recovery time and low forward voltage drop.

The glass passivation ensures stable reliable operation in the most severe temperature and power cycling conditions.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES UNITS								
V _{RRM}		600	V					
I _{F(AV)}	Sinusoidal waveform	10	^					
I _{FSM}		130	— A					
t _{rr}	1 A, 100 A/μs	50	ns					
V _F	10 A, T _J = 25 °C	1.2	V					
T _J		-40 to +150	°C					

VOLTAGE RATINGS			
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA
VS-10ETF06THM3	600	700	3

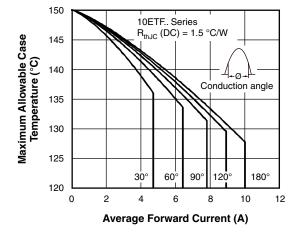
ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum average forward current	I _{F(AV)}	T _C = 128 °C, 180° conduction half sine wave	10				
Maximum peak one cycle	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	110	А			
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	130				
Maximum I ² t for fusing	I ² t	10 ms sine pulse, rated V _{RRM} applied	60	A ² s			
Maximum 1-t for fusing	1-1	10 ms sine pulse, no voltage reapplied	85	A-S			
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied	850	A²√s			

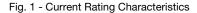


ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST COI	NDITIONS	VALUES	UNITS		
Maximum forward voltage drop	V_{FM}	10 A, T _J = 25 °C		1.2	V		
Forward slope resistance	r _t	T _{.1} = 150 °C		23.5	mΩ		
Threshold voltage	V _{F(TO)}	1) = 130 0		0.85	V		
Maximum reverse leakage current		T _J = 25 °C	V - Poted V	0.1	mA		
waxiiiluiii ieveise leakaye cuifelii	IRM	$V_R = Rated V_{RRM}$		3.0	IIIA		

RECOVERY CHARACTERISTICS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	· •		
Reverse recovery time	t _{rr}	In at 10 Anu	200	ns	I _{FM} t		
Reverse recovery current	I _{rr}	I _F at 10 A _{pk} 25 A/μs	2.75	Α	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
Reverse recovery charge	Q _{rr}	25 °C	0.32	μC	dir/ Q _{rr}		
Snap factor	S		0.6		I I _{RM(REC)}		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and sto temperature range	orage	T _J , T _{Stg}		-40 to +150	°C	
Maximum thermal resistance junction to case		R _{thJC}	DC operation	1.5		
Maximum thermal resistance junction to ambient		R _{thJA}		62	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5		
Anarovimoto weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf \cdot in)	
Marking device			Case style 2L TO-220AC	10ETF	06TH	





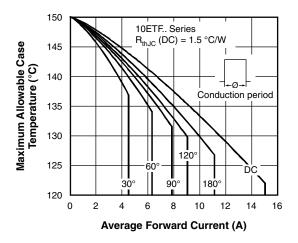


Fig. 2 - Current Rating Characteristics



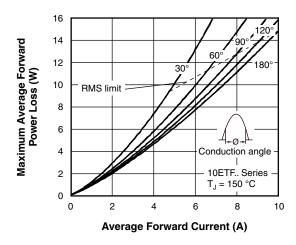


Fig. 3 - Forward Power Loss Characteristics

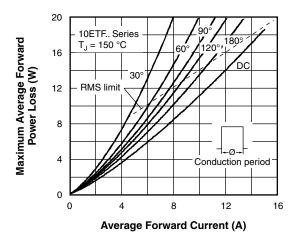


Fig. 4 - Forward Power Loss Characteristics

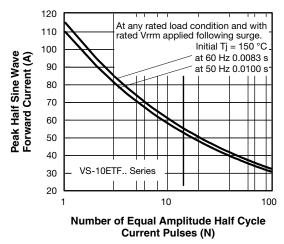


Fig. 5 - Maximum Non-Repetitive Surge Current

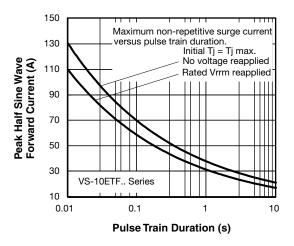


Fig. 6 - Maximum Non-Repetitive Surge Current

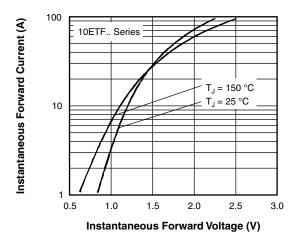


Fig. 7 - Forward Voltage Drop Characteristics

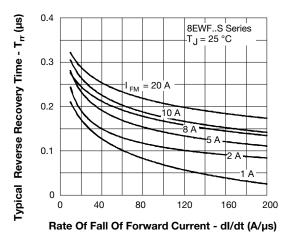


Fig. 8 - Recovery Time Characteristics, $T_J = 25 \, ^{\circ}\text{C}$

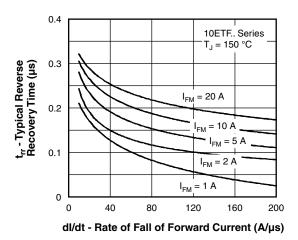


Fig. 9 - Recovery Time Characteristics, T_J = 150 °C

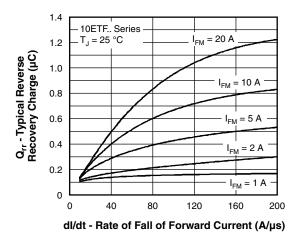
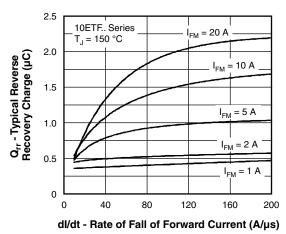
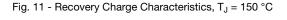


Fig. 10 - Recovery Charge Characteristics, T_J = 25 °C





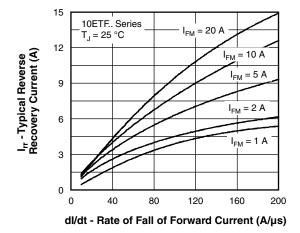


Fig. 12 - Recovery Current Characteristics, T_J = 25 °C

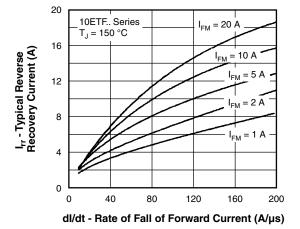


Fig. 13 - Recovery Current Characteristics, T_J = 150 °C

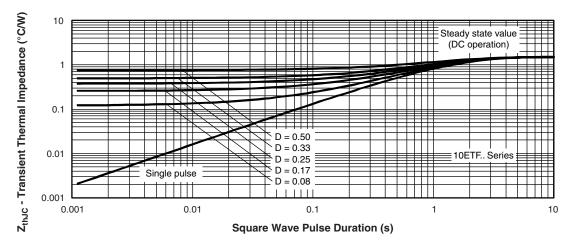


Fig. 14 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code VS-F T Н 10 Ε T 06 **M3** 2 (3) [5] 6 (7)(8) (4) Vishay Semiconductors product Current rating (10 = 10 A) Circuit configuration: E = 2L TO-220AC Package: T = TO-2205 Type of silicon: F = fast soft recovery rectifier 06 = 600 VVoltage code x $100 = V_{RRM}$ None = TO-220AB • T = true pin TO-220 H = AEC-Q101 qualified Environmental digit:

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-10ETF06THM3	50	1000	Antistatic plastic tube				

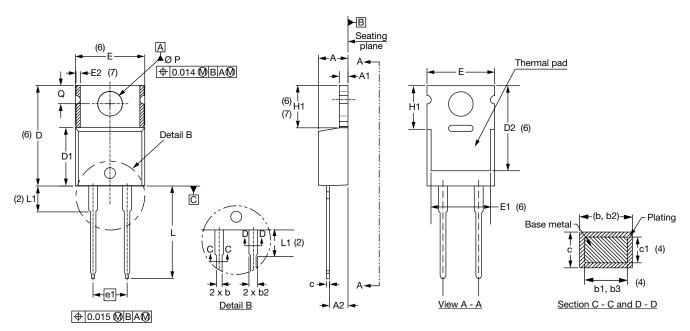
M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

LINKS TO RE	LATED DOCUMENTS
Dimensions	www.vishay.com/doc?96069
Part marking information	www.vishay.com/doc?95391



TO-220AC 2L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOIES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
E	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	ı	0.76	-	0.030	7
e1	4.88	5.28	0.192	0.208	
H1	5.84	6.86	0.230	0.270	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Ø	2.60	3.00	0.102	0.118	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- $^{(7)}$ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC® TO-220, except D2, where JEDEC® minimum is 0.480"



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Vishay

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