AUTOMOTIVE

RoHS

COMPLIANT HALOGEN

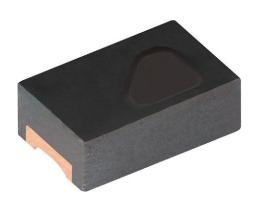
FREE

**GREEN** (5-2008)



# Vishay Semiconductors

## Silicon PIN Photodiode



#### LINKS TO ADDITIONAL RESOURCES





#### **DESCRIPTION**

The VEMD4080X02 is a high speed and high sensitive PIN photodiode with enhanced sensitivity for visible light. It is a low profile surface-mount device (SMD) with a 0.375 mm<sup>2</sup> sensitive area detecting visible and near infrared radiation.

#### **FEATURES**

Package type: surface-mount

• Package form: 0805



• Radiant sensitive area (in mm2): 0.375

• Ambient temperature range: T<sub>OP</sub> = -40 °C to +125 °C

• Angle of half sensitivity:  $\varphi = \pm 57^{\circ}$ 

• Floor life: 4 weeks, MSL2a, according to J-STD-020

· Lead (Pb)-free reflow soldering

AEC-Q102 qualified

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

#### **APPLICATIONS**

- · High speed photo detector
- Photo interrupters
- Automotive sensors

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μA)	φ (°)	λ <sub>0.5</sub> (nm)	
VEMD4080X02	2.3	± 57	480 to 1030	

#### Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMD4080X02	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	0805	

#### Note

· MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	20	V
Ambient temperature range		T <sub>amb</sub>	-40 to +125	°C
Storage temperature range		T <sub>stg</sub>	-40 to +125	°C
Soldering temperature	According to reflow solder profile Fig. 8	T <sub>sd</sub>	260	°C



PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	$V_{F}$	-	1.1	1.3	V
Breakdown voltage	$I_R = 100 \mu\text{A},  E = 0  \text{mW/cm}^2$	V <sub>(BR)</sub>	20	-	-	V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>	-	-	3	nA
Diode capacitance	$V_R = 0 \text{ V, f} = 1 \text{ MHz, E} = 0 \text{ mW/cm}^2$	$C_D$	-	6.5	-	pF
Short circuit current	$E_e = 1 \text{ mW/cm}^2, \lambda = 940 \text{ nm}$	I <sub>k</sub>	-	2.4	-	μΑ
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 940 \text{ nm}$	Vo	-	312	-	mV
Temperature coefficient of Ik	$E_e = 1 \text{ mW/cm}^2, \lambda = 940 \text{ nm}$	TK <sub>lk</sub>	-	0.12	-	%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 460 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	0.7	1.0	1.4	μΑ
	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 530 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	0.9	1.4	2.0	μΑ
	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 850 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	1.7	2.3	3.1	μΑ
	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 940 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	1.7	2.3	3.0	μΑ
Angle of half sensitivity		φ	-	± 57	-	0
Wavelength of peak sensitivity		$\lambda_{p}$	-	870	-	nm
Range of spectral bandwidth	S <sub>rel</sub> > 0.5	λ <sub>0.5</sub>	-	480 to 1030	-	nm
Rise time	$V_R = 10 \text{ V}, R_L = 50 \Omega, \lambda = 890 \text{ nm}$	t <sub>r</sub>	-	340	-	ns
Fall time	$V_R = 10 \text{ V}, R_L = 50 \Omega, \lambda = 890 \text{ nm}$	t <sub>f</sub>	-	260	-	ns

## **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

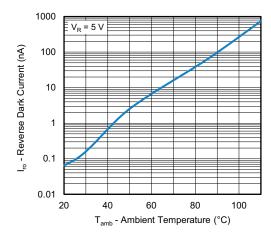


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

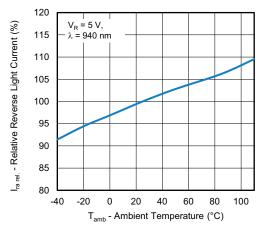


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature



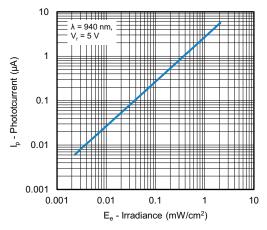


Fig. 3 - Reverse Light Current vs. Irradiance

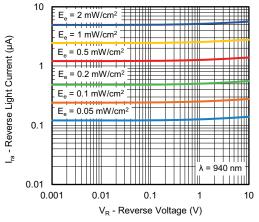


Fig. 4 - Reverse Light Current vs. Reverse Voltage

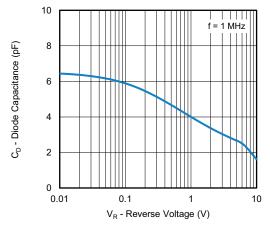


Fig. 5 - Diode Capacitance vs. Reverse Voltage

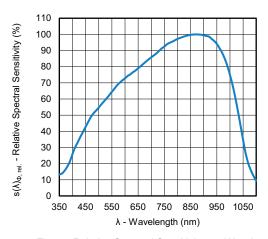


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

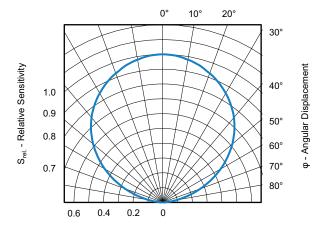


Fig. 7 - Relative Sensitivity vs. Angular Displacement



#### **REFLOW SOLDER PROFILE**

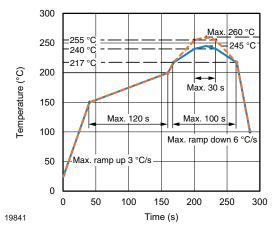


Fig. 8 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

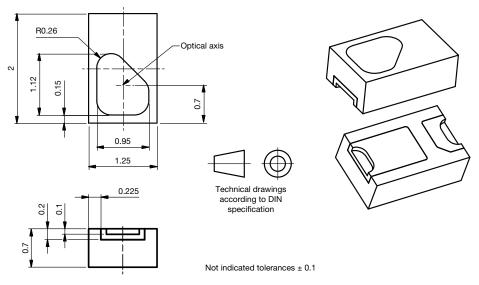
Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

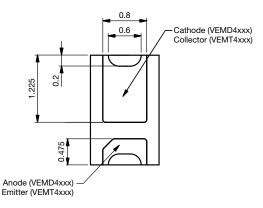
Moisture sensitivity level 2a, according to J-STD-020.

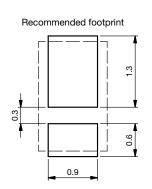
#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-033D or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5 %.

### **PACKAGE DIMENSIONS** in millimeters

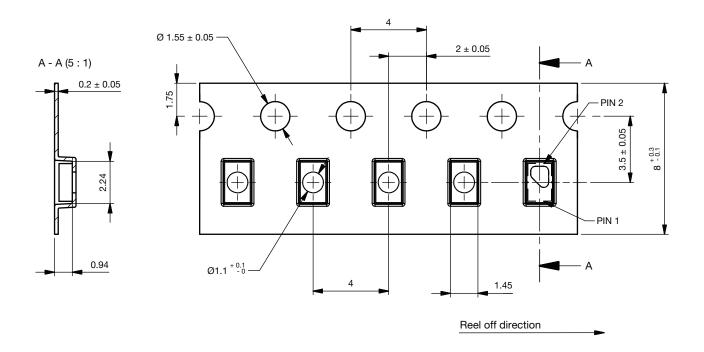






Drawing-No.: 6.550-5363.01-4 Issue: 2; 01.07.2020

### **BLISTER TAPE DIMENSIONS** in millimeters



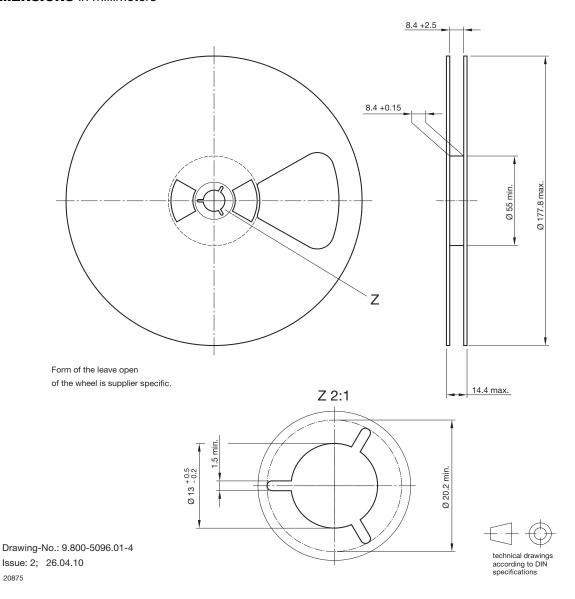
TYPE	PIN 1	PIN 2
VEMD4xxx	Anode	Cathode

Drawing-No.: 9.700-5411.0-4 Issue: 1\_A; 11.10.2022

20875

# Vishay Semiconductors

### **REEL DIMENSIONS** in millimeters





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