AUTOMOTIVE

ROHS

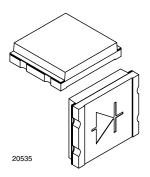
GREEN

(5-2008)\*\*



# Vishay Semiconductors

# Silicon PIN Photodiode



# DESCRIPTION

TEMD5010X01 is a high speed and high sensitive PIN photodiode. It is a miniature surface mount device (SMD) including the chip with a 7.5 mm<sup>2</sup> sensitive area detecting visible and near infrared radiation.

#### **FEATURES**

- Package type: surface mount
- · Package form: top view
- Dimensions (L x W x H in mm): 5 x 4.24 x 1.12
- Radiant sensitive area (in mm<sup>2</sup>): 7.5
- AEC-Q101 qualified
- · High photo sensitivity
- · High radiant sensitivity
- Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 65^{\circ}$
- Floor life: 72 h, MSL 4, acc. J-STD-020
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC



\*\* Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

## **APPLICATIONS**

· High speed photo detector

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μΑ)	φ (deg)	λ0.1 (nm)	
TEMD5010X01	55	± 65	430 to 1100	

## Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
TEMD5010X01	Tape and reel	MOQ: 1500 pcs, 1500 pcs/reel	Top view		

#### Note

· MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		$V_R$	60	V
Power dissipation	T <sub>amb</sub> ≤ 25 °C	P <sub>V</sub>	215	mW
Junction temperature		T <sub>j</sub>	100	°C
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C
Storage temperature range		T <sub>stg</sub>	- 40 to + 110	°C
Soldering temperature	Acc. reflow solder profile fig. 8	T <sub>sd</sub>	260	°C
Thermal resistance junction/ambient		R <sub>thJA</sub>	350	K/W



PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1	1.3	V
Breakdown voltage	I <sub>R</sub> = 100 μA, E = 0	V <sub>(BR)</sub>	60			V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>		2	30	nA
Diada assaultana	$V_R = 0 \text{ V, } f = 1 \text{ MHz, } E = 0$	C <sub>D</sub>		70		pF
Diode capacitance	V <sub>R</sub> = 3 V, f = 1 MHz, E = 0	C <sub>D</sub>		25	40	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	TK <sub>Vo</sub>		- 2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	I <sub>k</sub>		50		μΑ
Temperature coefficient of I <sub>k</sub>	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	TK <sub>lk</sub>		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2,  \lambda = 950 \text{ nm}, \\ V_R = 5 \text{ V}$	I <sub>ra</sub>	45	55		μΑ
Angle of half sensitivity		φ		± 65		deg
Wavelength of peak sensitivity		$\lambda_{p}$		940		nm
Range of spectral bandwidth		λ <sub>0.1</sub>		430 to 1100		nm
Noise equivalent power	$V_R = 10 \text{ V}, \ \lambda = 950 \text{ nm}$	NEP		4 x 10 <sup>-14</sup>		W/√Hz
Rise time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega,$ $\lambda = 820 \text{ nm}$	t <sub>r</sub>		100		ns
Fall time	$V_R$ = 10 V, $R_L$ = 1 kΩ, $\lambda$ = 820 nm	t <sub>f</sub>		100		ns

# **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

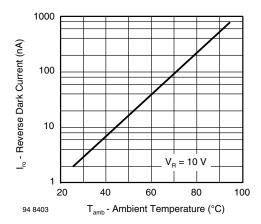


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

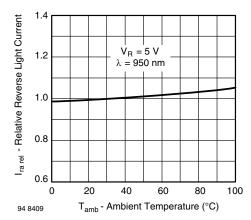


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature



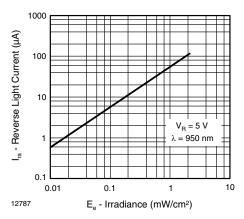


Fig. 3 - Reverse Light Current vs. Irradiance

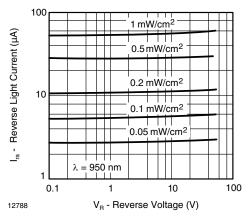


Fig. 4 - Reverse Light Current vs. Reverse Voltage

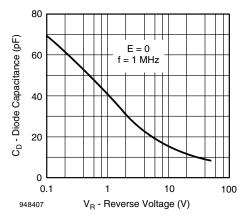


Fig. 5 - Diode Capacitance vs. Reverse Voltage

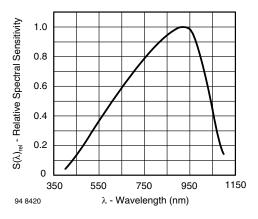


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

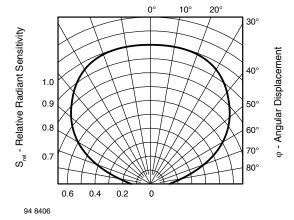
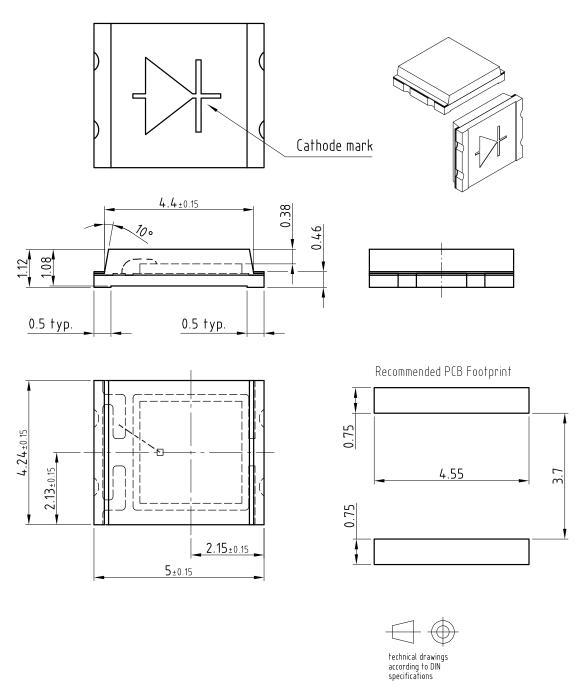


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement

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## **PACKAGE DIMENSIONS** in millimeters



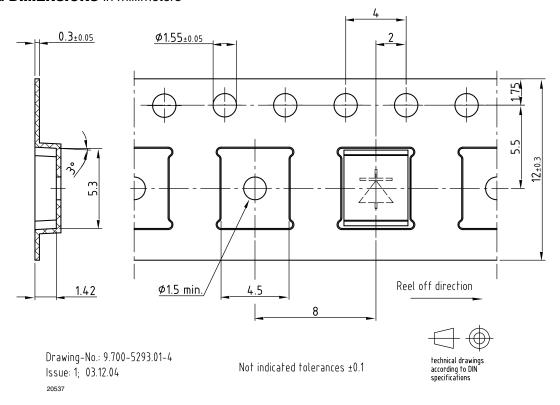
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Issue: 3; 05.02.08

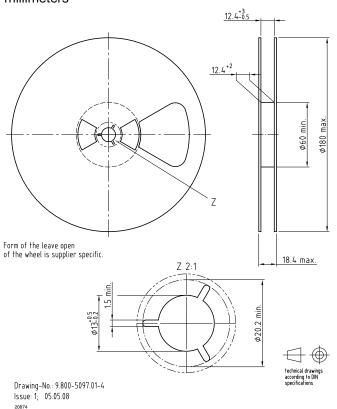
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Not indicated tolerances ± 0.1

## **TAPING DIMENSIONS** in millimeters



## **REEL DIMENSIONS** in millimeters





## **SOLDER PROFILE**

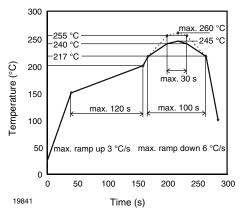


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020D

### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 4

Floor life: 72 h

Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

## **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions:

192 h at 40 °C (+ 5 °C), RH < 5 %

or

96 h at 60 °C (+ 5 °C), RH < 5 %.



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Vishay

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