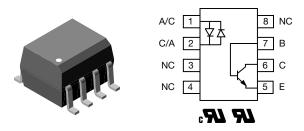


# Vishay Semiconductors

# Optocoupler, Phototransistor Output, Dual Channel, AC Input



### **LINKS TO ADDITIONAL RESOURCES**









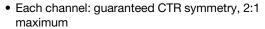
### **DESCRIPTION**

The ILD256T is a dual channel optocoupler. Each channel consists of two infrared emitters coupled to a silicon NPN phototransistor detector.

These circuit elements are constructed with a standard SOIC-8A footprint.

The product is well suited for telecom applications such as ring detection or off / on hook status, given its bidirectional LED input and guaranteed current transfer ratio (CTR) of 20 % at  $I_{\rm F}$  = 10 mA.

### **FEATURES**





- · Bidirectional AC input
- SOIC-8 surface mountable package
- Isolation test voltage, 4000 V<sub>RMS</sub>
- RoHS COMPLIANT

- · Standard lead spacing, 0.05
- Available only on tape and reel option (conform to EIA standard 481-2)
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **APPLICATIONS**

Telecom applications ring detection off / on hook status

### **AGENCY APPROVALS**

- <u>UL</u>
- cUL

ORDERING INFORMATION					
I L D	2 5 6 T	SOIC-8			
PART	NUMBER	6.1 mm			
AGENCY CERTIFIED / PACKAGE	CTR (%)				
AGENCT CENTIFIED / FACRAGE	10 mA				
UL, cUL	≥ 20				
SOIC-8	ILD256T				

## Note

· Additional options may be possible, please contact sales office



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ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Forward continuous current		I <sub>F</sub>	30	mA		
Power dissipation		P <sub>diss</sub>	50	mW		
Derate linearly from 25 °C			0.66	mW/°C		
OUTPUT						
Collector-emitter breakdown voltage		BV <sub>CEO</sub>	70	V		
Emitter-collector breakdown voltage		BV <sub>ECO</sub>	7.0	V		
Power dissipation		P <sub>diss</sub>	125	mW		
Derate linearly from 25 °C			1.67	mW/°C		
COUPLER						
Isolation voltage, input to output		V <sub>ISO</sub>	4000	$V_{RMS}$		
Total package dissipation (LED and detector)		P <sub>tot</sub>	300	mW		
Derate linearly from 25 °C			4.0	mW/°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		
Operating temperature		T <sub>amb</sub>	-55 to +100	°C		
Soldering time at 260 °C			10	s		

#### **Notes**

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	-	1.2	1.55	V
Reverse voltage	V <sub>R</sub> = 6 V	I <sub>R</sub>	-	0.1	100	mA
OUTPUT						
Collector emitter breakdown voltage	I <sub>C</sub> = 10 μA	BV <sub>CEO</sub>	70	-	-	V
Emitter collector breakdown voltage	I <sub>E</sub> = 10 μA	BV <sub>ECO</sub>	7	-	-	V
Collector emitter leakage current	V <sub>CE</sub> = 10 V	I <sub>CEO</sub>	-	5	50	nA
COUPLER						
Symmetry (CTR at +10 mA)/(CTR at -10 mA)			0.5	1	2	
Collector emitter saturation voltage	$I_F = \pm 16 \text{ mA}, I_C = 2 \text{ mA}$	V <sub>CEsat</sub>	-	-	0.4	V

## Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
I <sub>C</sub> /I <sub>F</sub>	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR	20	ı	ı	%

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

# Vishay Semiconductors

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification	According to IEC 68 part 1		-	55 / 100 / 21	-			
Comparative tracking index		CTI	175	-	399			
$V_{IOTM}$			6000	-	-	V		
V <sub>IORM</sub>			560	-	-	V		
P <sub>SO</sub>			-	-	350	mW		
I <sub>SI</sub>			-	-	150	mA		
T <sub>SI</sub>			-	-	165	°C		
Creepage distance			4	-	-	mm		
Clearance distance			4	-	-	mm		
Insulation thickness			0.2	-	-	mm		

#### Note

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

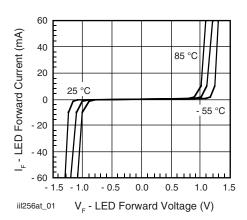


Fig. 1 - LED Forward Current vs. Forward Voltage

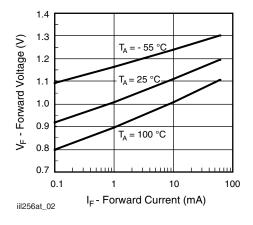


Fig. 2 - Forward Voltage vs. Forward Current

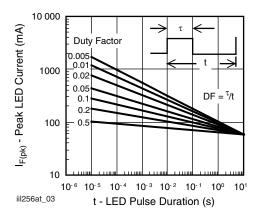


Fig. 3 - Peak LED Current vs. Duty Factor, t

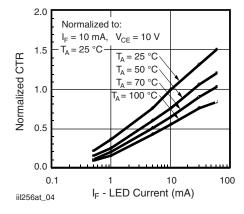


Fig. 4 - Normalized CTR vs. I<sub>F</sub> and T<sub>amb</sub>

As per IEC 60747-5-5, §7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.



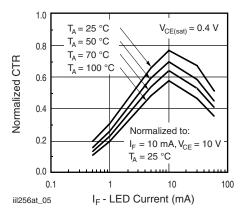


Fig. 5 - Normalized Saturated CTR

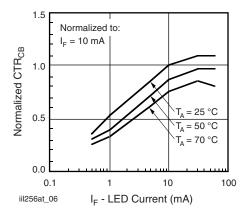


Fig. 6 - Normalized CTR<sub>CB</sub>

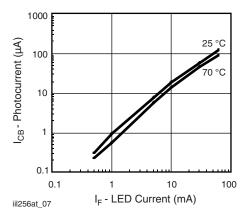


Fig. 7 - Photocurrent vs. LED Current

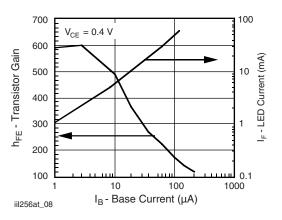


Fig. 8 - Base Current vs. I<sub>F</sub> and h<sub>FE</sub>

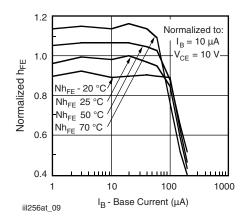


Fig. 9 - Normalized hFE vs. Base Current and Temperature

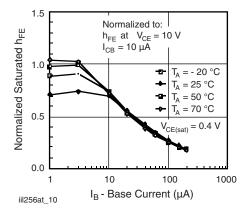


Fig. 10 - Normalized Saturated h<sub>FE</sub> vs. Base Current



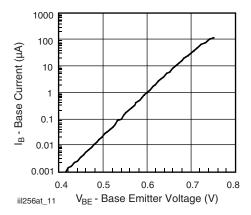


Fig. 11 - Base Emitter Voltage vs. Base Current

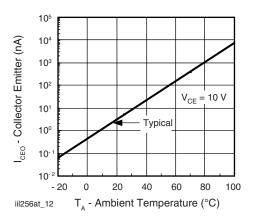
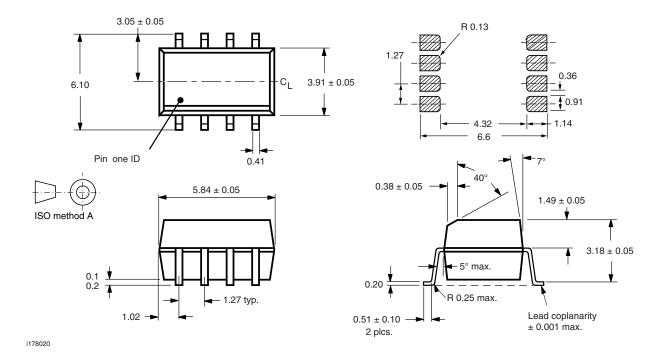


Fig. 12 - Collector Emitter Leakage Current vs. Temperature

## **PACKAGE DIMENSIONS** in millimeters



## **PACKAGE MARKING** (example)



#### **Notes**

- XXXX = LMC (lot marking code)
- Tape and reel suffix (T) is not part of the package marking



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Vishay

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