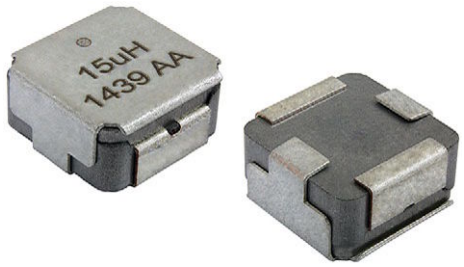
# IHLE® High Current Inductors With E-Field Shield




## LINKS TO ADDITIONAL RESOURCES

Design Tools

## FEATURES

- High temperature, up to 155 °C
- Integrated E-Shield for maximum EMI reduction (1)
- Excellent DC/DC energy storage up to 1 MHz to 2 MHz. Filter inductor applications up the SRF (see standard electrical specifications table).
- Integrated E-Field shield eliminates need for separate shielding
- 20 dB E-Field reduction at 1 cm
  - Measured vertically from top center of device
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without saturation
- Coplanarity of the 4 terminals ≤ 100 µm
- IHLE design; PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free
RoHS COMPLIANT
HALOGEN FREE GREEN (5-2008)

**Note**

(1) Maximum E-Field reduction is realized when the IHLE shield is connected to ground

## STANDARD ELECTRICAL SPECIFICATIONS

| $L_0$ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (µH) | DCR TYP. 25 °C (mΩ) | DCR MAX. 25 °C (mΩ) | HEAT RATING CURRENT DC TYP. (A) (1) | SATURATION CURRENT DC TYP. (A) (2) | SRF TYP. (MHz) |
|---|---|---|---|---|---|
| 0.22 | 1.68 | 1.86 | 36.0 | 32.0 | 117 |
| 0.47 | 2.38 | 2.55 | 27.0 | 19.0 | 77 |
| 0.68 | 3.30 | 3.53 | 21.5 | 16.2 | 51 |
| 1.0 | 4.58 | 4.90 | 19.0 | 16.2 | 45 |
| 2.2 | 11.70 | 12.50 | 11.5 | 14.0 | 32 |
| 3.3 | 15.40 | 16.48 | 10.6 | 11.8 | 23 |
| 4.7 | 29.60 | 28.46 | 7.2 | 9.1 | 18 |
| 5.6 | 29.60 | 31.67 | 6.9 | 9.0 | 18 |
| 10 | 50.00 | 53.50 | 5.1 | 5.2 | 13 |
| 15 | 62.00 | 66.34 | 4.8 | 3.6 | 10 |
| 22 | 103.00 | 110.21 | 3.7 | 3.8 | 9 |
| 33 | 149.00 | 159.43 | 3.1 | 3.2 | 6.1 |

**Notes**

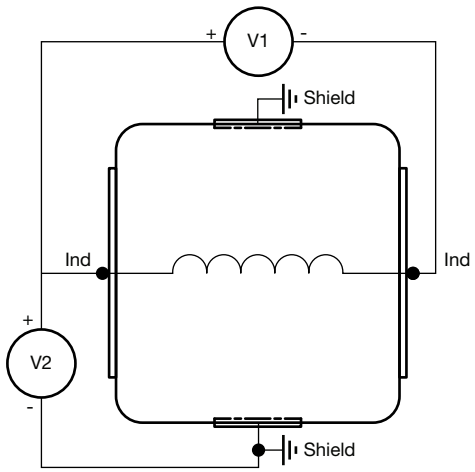
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage, across inductor (V1) = 50 V
- Rated isolation voltage, inductor lead to shield (V2) = 50 V

(1) DC current (A) that will cause an approximate ΔT of 40 °C

(2) DC current (A) that will cause $L_0$ to drop approximately 20 %

## APPLICATIONS

- PDA / notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)
- Telecom infrastructure




**PATENT(S): www.vishay.com/patents**
**This Vishay product is protected by one or more United States and international patents.**

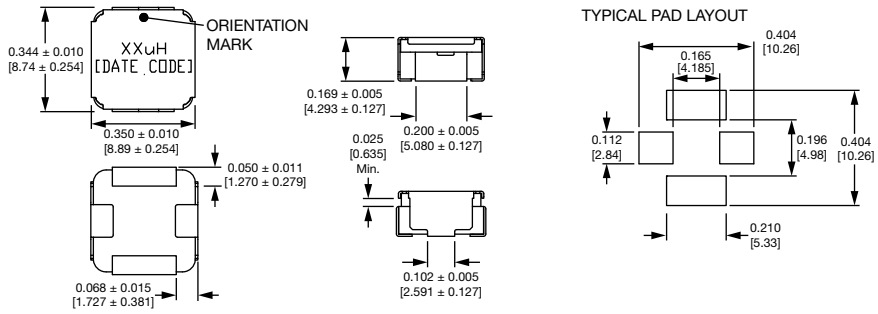
## DESCRIPTION

| IHLE-3232DD-51 | 15 µH | ± 20 % | ER | e3 |
|---|---|---|---|---|
| MODEL | INDUCTANCE VALUE | INDUCTANCE TOLERANCE | PACKAGE CODE | JEDEC® LEAD (Pb)-FREE STANDARD |

## GLOBAL PART NUMBER

| I | H | L | E | 3 | 2 | 3 | 2 | D | D | E | R | 1 | 5 | 0 | M | 5 | 1 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| PRODUCT FAMILY | | | | SIZE | | | | | | PACKAGE CODE | | INDUCTANCE VALUE | | | TOL. | SERIES | |

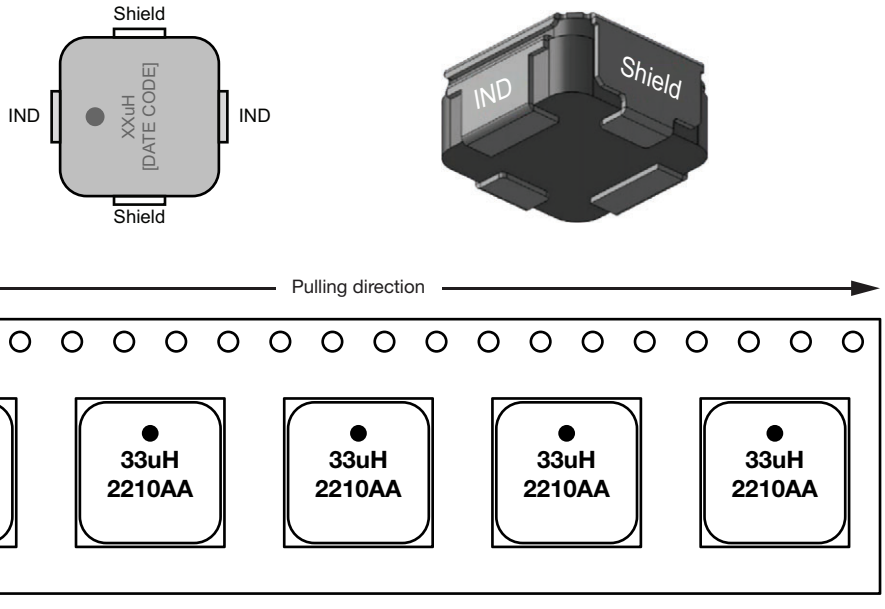
## DIMENSIONS in inches [millimeters]



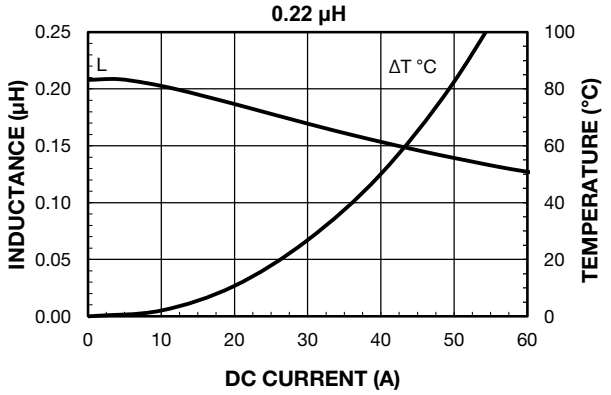

**Notes**

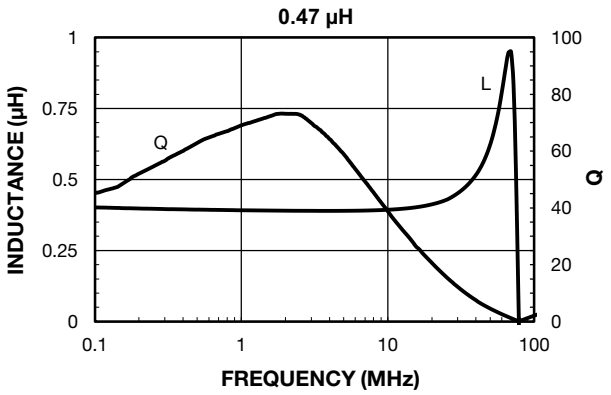
- Dot indicate the coil termination
- Coplanarity of 4 terminals: 0.004” [0.10]

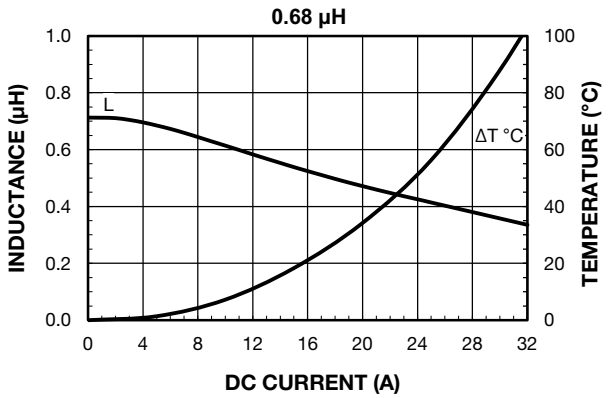
## PART MARKING / POCKET TAPE ORIENTATION

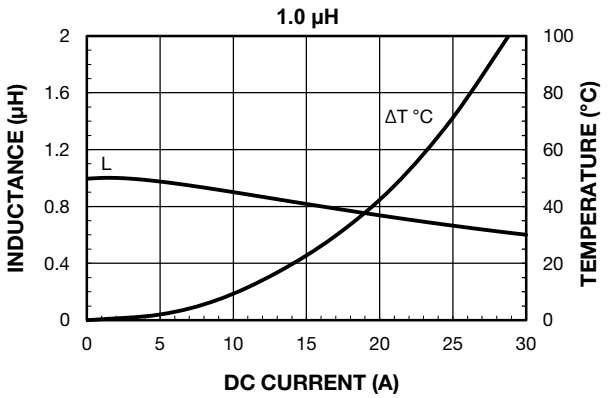


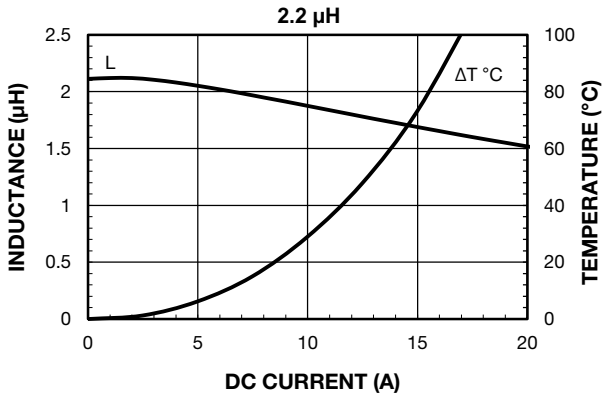
## PERFORMANCE GRAPHS

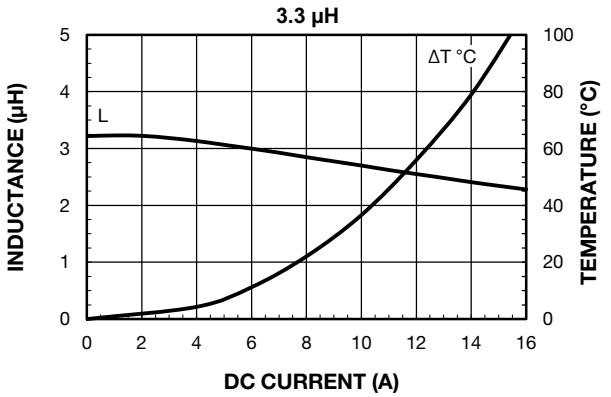


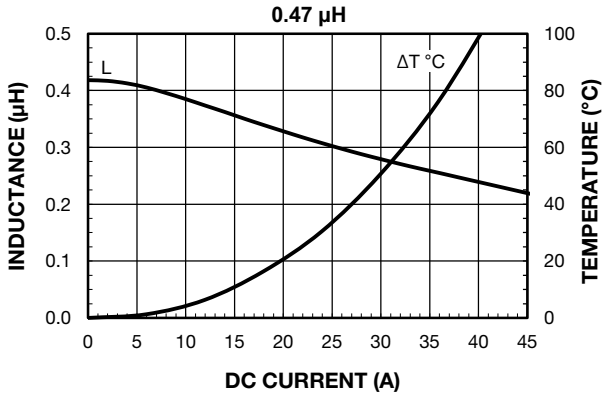



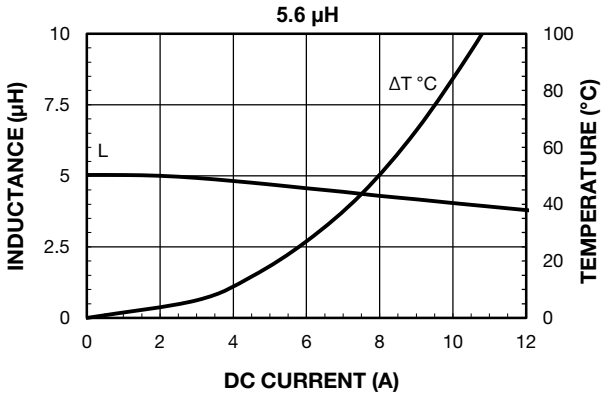



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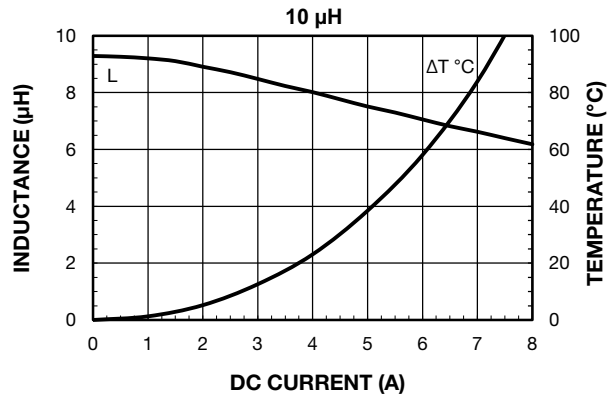
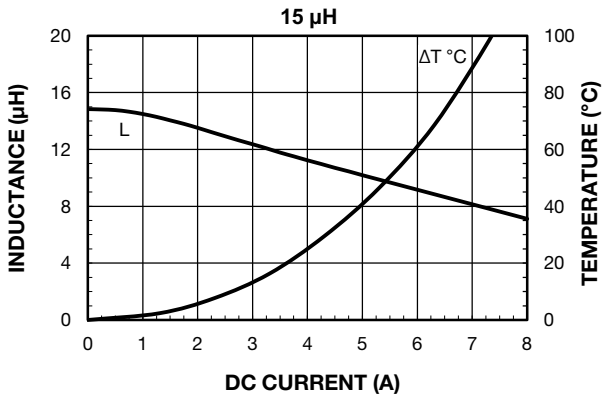
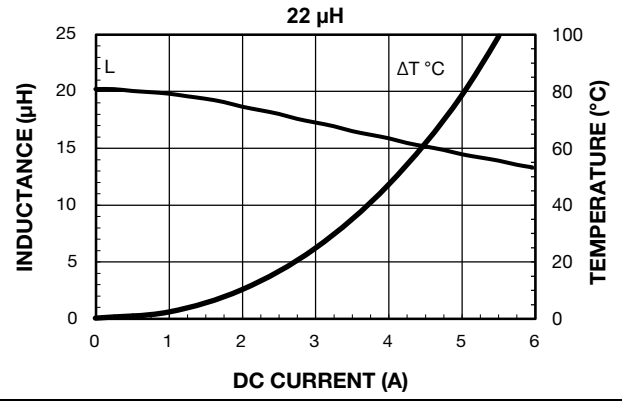
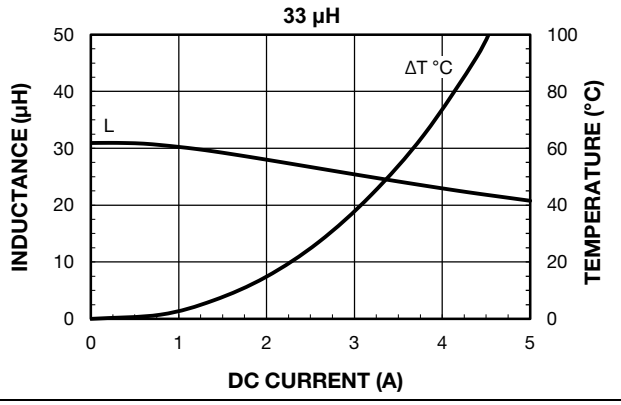
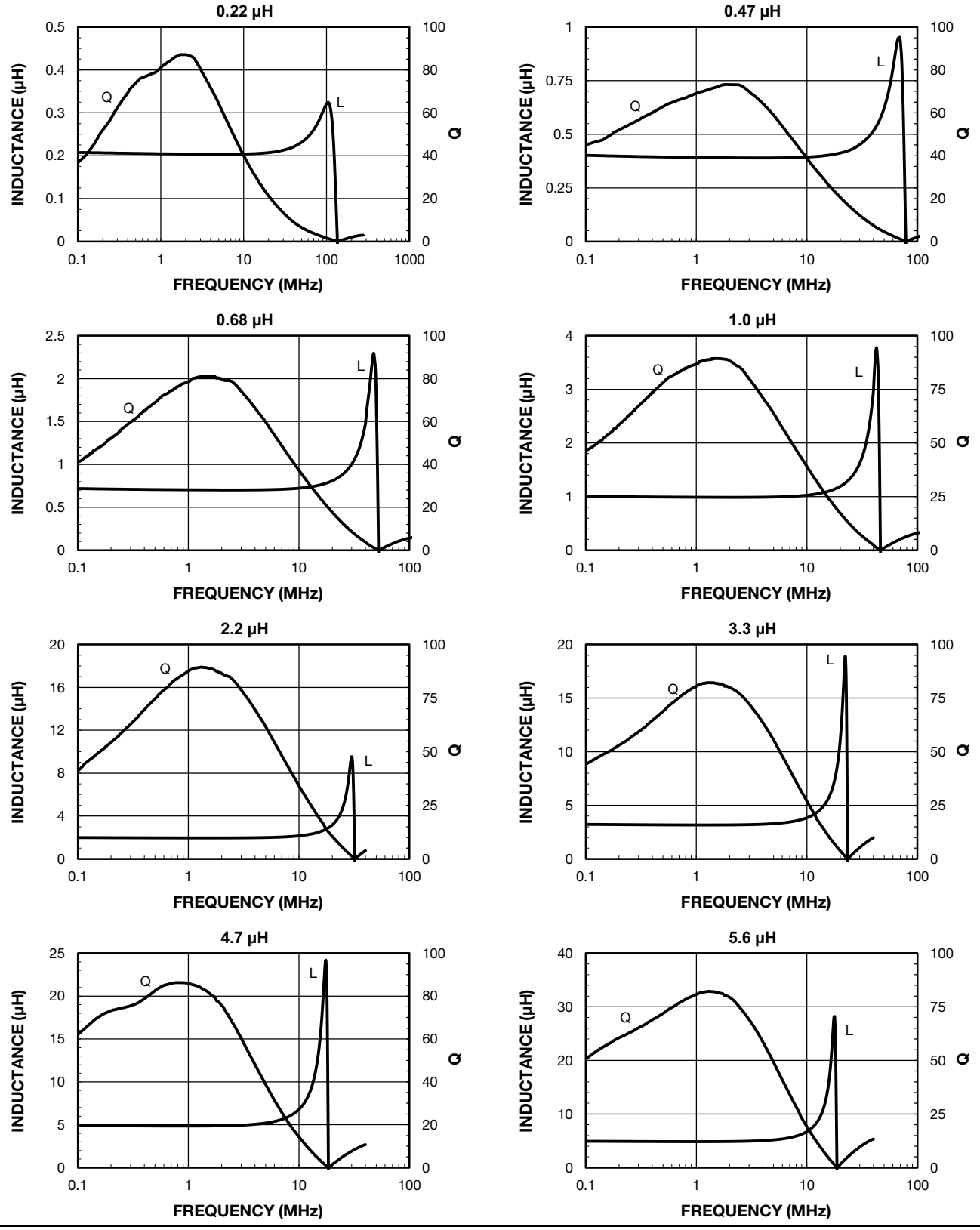


## PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY



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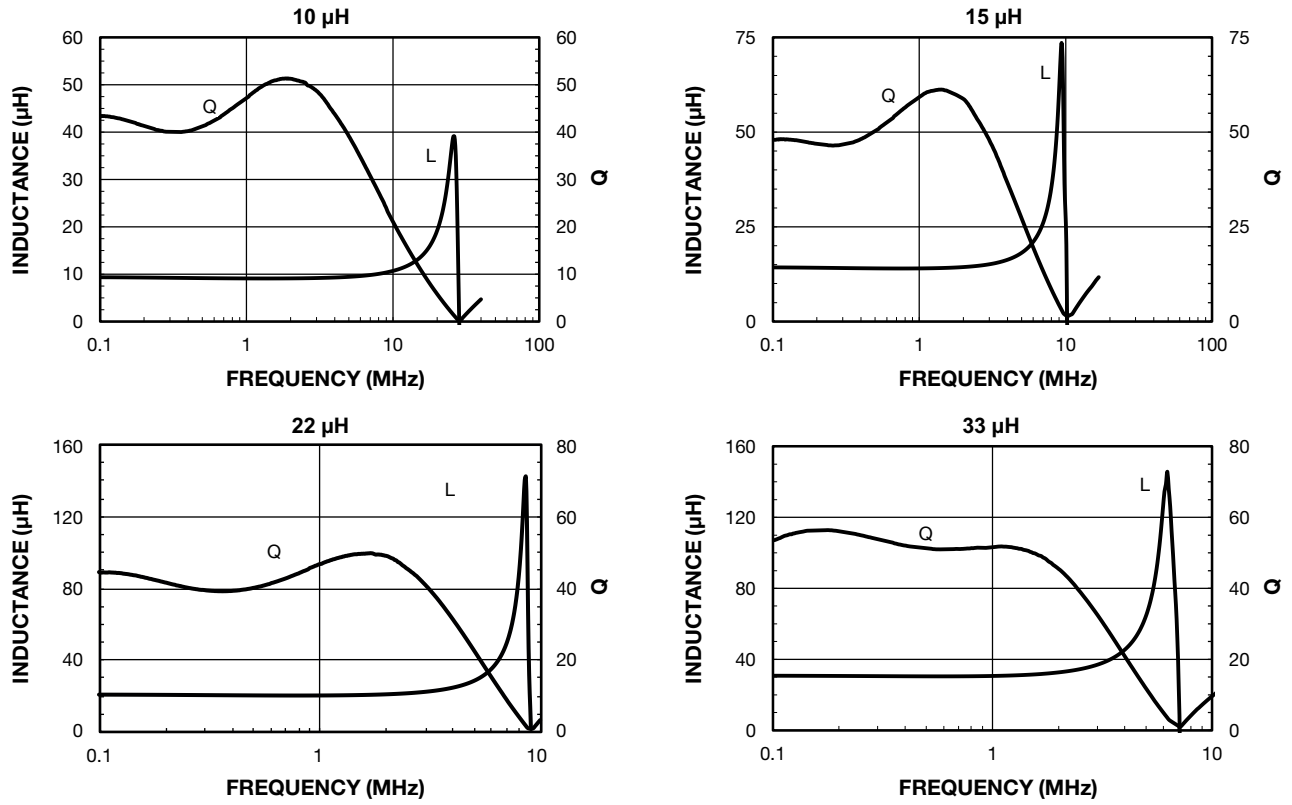

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