



P-Channel 30 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$ Max.	I _D (A) ^a	Q _g (Typ.)		
	0.042 at V _{GS} = - 10 V	- 5			
- 30	0.054 at V _{GS} = - 6 V	- 4.4	6.9 nC		
	0.068 at V _{GS} = - 4.5 V	- 3.9			

FEATURES

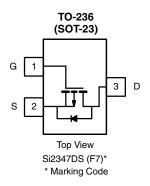
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912



HALOGEN FREE

APPLICATIONS

- Load Switch
- Notebook Adaptor Switch
- DC/DC Converter
- **Power Management**



Ordering Information: Si2347DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	- 30	V	
Gate-Source Voltage	V_{GS}	± 20	¬	
	T _C = 25 °C		- 5	
Continuous Drain Current (T ₁ = 150 °C)	T _C = 70 °C		- 4	
Continuous Brain Current (1) = 100 °C)	T _A = 25 °C	I _D	- 3.8 ^{b,c}	
	T _A = 70 °C		- 3 ^{b,c}	A
Pulsed Drain Current (t = 300 μs)	I _{DM}	- 20		
Continuous Source-Drain Diode Current	T _C = 25 °C	l _o	- 1.4	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 0.63 ^{b,c}	
	T _C = 25 °C		1.7	
Maximum Power Dissipation	T _C = 70 °C	P_{D}	1.1	w
Maximum Fower Dissipation	T _A = 25 °C	1 ' 0	1.20 ^{b, c}	
	T _A = 70 °C		0.6 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	100	130	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	60	75	O/ VV	

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 175 °C/W.

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MOSFET SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•	l	•	
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			- 25		m\//°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		3.9		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 1		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zaura Casta Valta va Dunius Communi	_	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α	
		$V_{GS} = -10 \text{ V}, I_D = -3.8 \text{ A}$		0.033	0.042		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 6 V, I _D = - 3.3 A		0.041	0.054	Ω	
	,	V _{GS} = - 4.5 V, I _D = - 3 A		0.050	0.068	1	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 3.8 A		10		S	
Dynamic ^b							
Input Capacitance	C _{iss}			705		1	
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		93		pF	
Reverse Transfer Capacitance	C _{rss}	20 4 40		73			
·	Qg	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 5 A		14.5	22		
Total Gate Charge		25 7 GS 7 B		6.9	10.4	·	
Gate-Source Charge	Q _{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5 \text{ A}$		2.3		nC	
Gate-Drain Charge	Q _{gd}	30 30		2.1		1	
Gate Resistance	R _q	f = 1 MHz	1.7	8.3	17	Ω	
Turn-On Delay Time	t _{d(on)}			6	12		
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_1 = 5 \Omega$		6	12	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D = -3 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 1 \Omega$		19	29		
Fall Time	t _f			9	18	1	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{I} = 5 \Omega$		9	18	- ns	
Turn-Off Delay Time	t _{d(off)}	$I_D = -3 \text{ A}, V_{GEN} = -6 \text{ V}, R_G = 1 \Omega$		18	27		
Fall Time	t _f			7	14		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 1.4	l .	
Pulse Diode Forward Current ^a	I _{SM}				- 20	A	
Body Diode Voltage	V _{SD}	I _S = - 3 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}	-		13	20	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	 		5	10	nC	
Reverse Recovery Fall Time	t _a	$I_F = -3 \text{ A}$, $dI/dt = 100 \text{ A/}\mu\text{s}$, $T_J = 25 ^{\circ}\text{C}$		7			
Reverse Recovery Rise Time t _b			6			ns	

Notes:

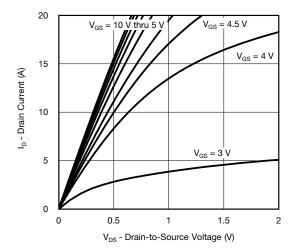
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

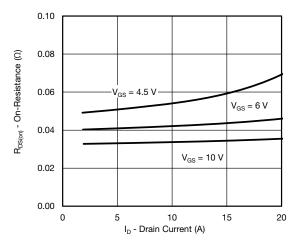
b. Guaranteed by design, not subject to production testing.



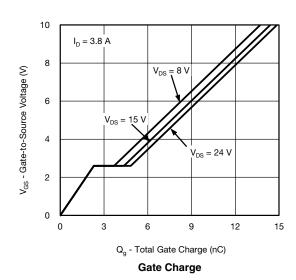
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

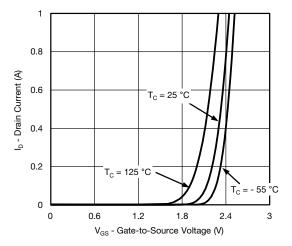


Output Characteristics

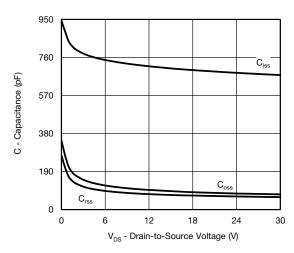


On-Resistance vs. Drain Current and Gate Voltage

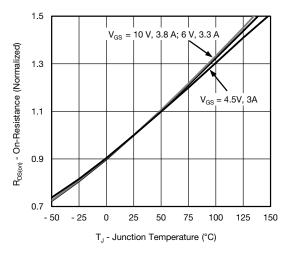




Transfer Characteristics



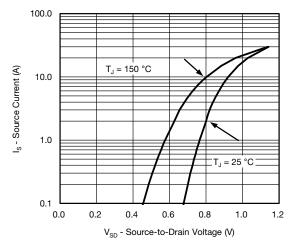
Capacitance



On-Resistance vs. Junction Temperature

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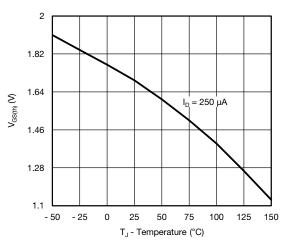
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

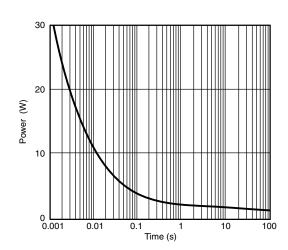


0.125 $I_D = 3.8 A$ 0.1 R_{DS(on)} - On-Resistance (Ω) 0.075 $T_J = 125 \, ^{\circ}C$ 0.05 $T_J = 25 \, ^{\circ}C$ 0.025 0 V_{GS} - Gate-to-Source Voltage (V)

Source-Drain Diode Forward Voltage

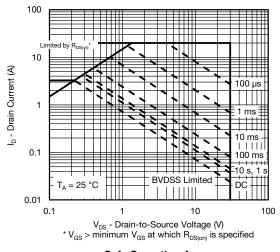
On-Resistance vs. Gate-to-Source Voltage





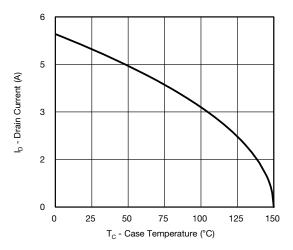
Threshold Voltage

Single Pulse Power

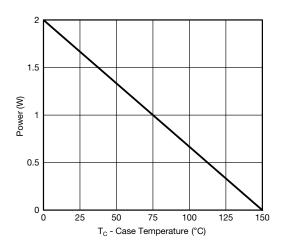


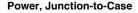


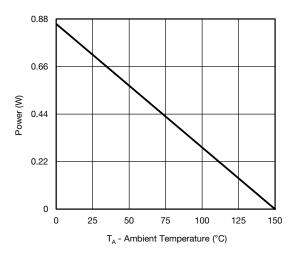
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*







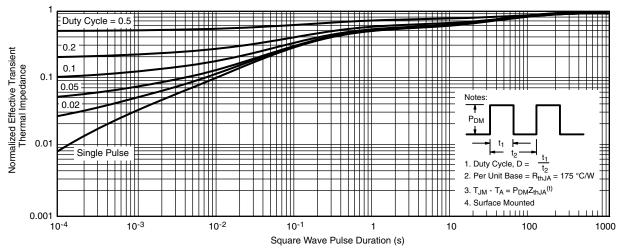
Power, Junction-to-Ambient

^{*} The power dissipation PD is based on TJ(max.) = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heats inking is used. It is used to determine the current rating, when this rating falls below the package limit.

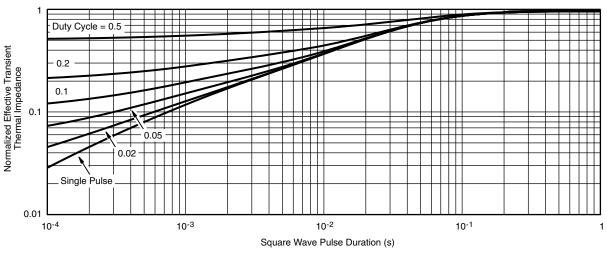
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95	95 BSC 0.0374		Ref	
e ₁	1.90	BSC	0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K. 09-	Jul-01				

DWG: 5479

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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