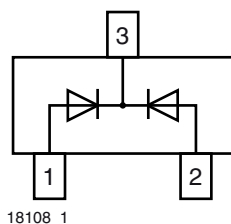
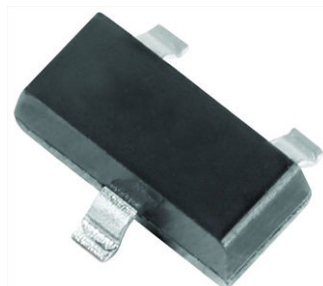


Dual Common Cathode Small Signal High Voltage Switching Diode



FEATURES

- Silicon epitaxial planar diode
- Fast switching dual common cathode diode, especially suited for applications requiring high voltage capability
- AEC-Q101 qualified available (part number on request)
- Molding compound meets UL 94 V-0 flammability rating
- Moisture sensitivity level (MSL) 1
- Base P/N-G3 - green, commercial grade
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



LINKS TO ADDITIONAL RESOURCES



MECHANICAL DATA

Case: SOT-23

Weight: approx. 9.2 mg

Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box

08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE

PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY
GSD2004C-G	GSD2004C-G3-08	no	DBK	Common anode	3 000 (8 mm tape on 7" reel)	15 000
	GSD2004C-G3-18	no			10 000 (8 mm tape on 13" reel)	10 000

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Continuous reverse voltage		V _R	240	V
Peak repetitive reverse voltage		V _{RRM}	300	V
Forward current (continuous) ⁽¹⁾		I _F	400	mA
Peak repetitive forward current ⁽¹⁾		I _{FRM}	625	mA
Non-repetitive peak forward current ⁽¹⁾	t _p = 1 μs	I _{FSM}	4	A
	t _p = 1 s		1	A
Power dissipation	on FR-4 board with recommended soldering footprint	P _{tot}	300	mW
	Infinite heatsink		500	mW

Note

⁽¹⁾ Infinite heatsink

THERMAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Typical thermal resistance junction to ambient air	according to JEDEC® 51-3 on FR-4 board with recommended soldering footprint	R _{thJA}	420	K/W
Thermal resistance junction to lead	Infinite heatsink	R _{thJL}	250	K/W
Junction temperature		T _j	150	°C
Storage temperature range		T _{stg}	-65 to +150	°C
Operating temperature range		T _{op}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	$I_R = 100\text{ }\mu\text{A}$	V_{BR}	300			V
Leakage current	$V_R = 240\text{ V}$	I_R			100	nA
	$V_R = 240\text{ V}, T_J = 150\text{ }^{\circ}\text{C}$	I_R			100	μA
Forward voltage	$I_F = 20\text{ mA}$	V_F		0.83	0.87	V
	$I_F = 100\text{ mA}$	V_F			1	V
Diode capacitance	$V_F = V_R = 0, f = 1\text{ MHz}$	C_D			5	pF
Reverse recovery time	$I_F = I_R = 30\text{ mA}, I_R = 3\text{ mA}, R_L = 100\text{ }\Omega$	t_{rr}			50	ns

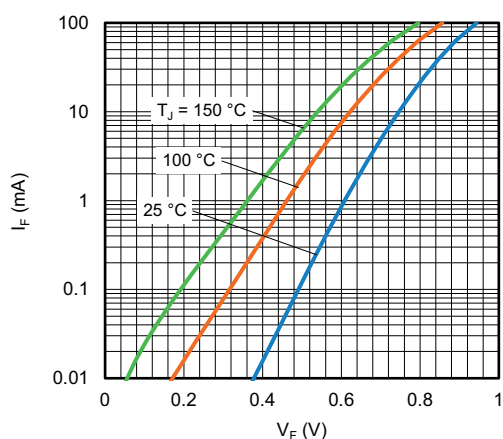
TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)


Fig. 1 - Forward Current vs. Forward Voltage

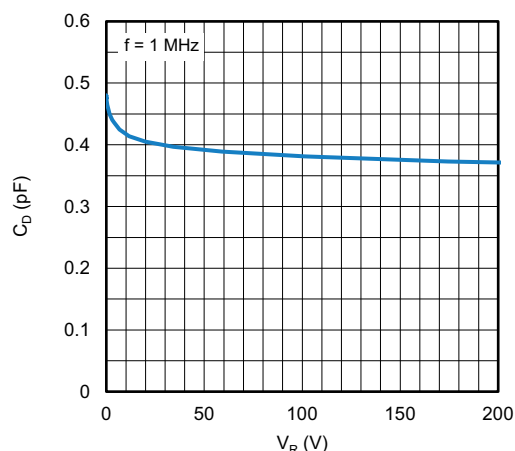


Fig. 3 - Typical Capacitance vs. Reverse Voltage

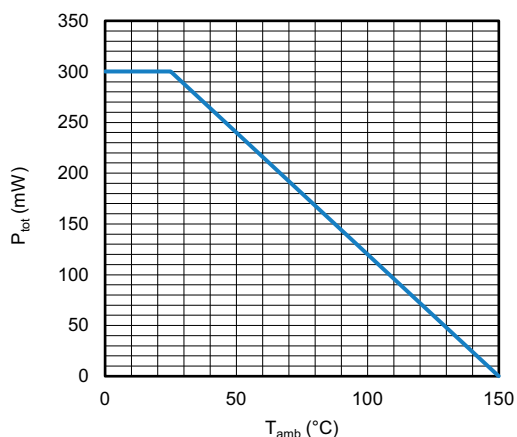


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

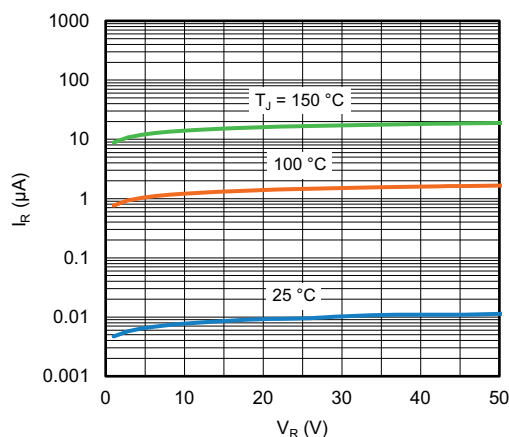
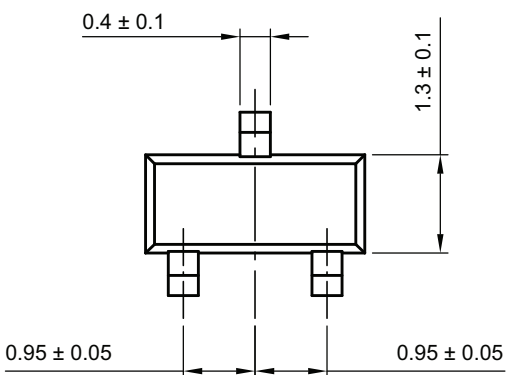
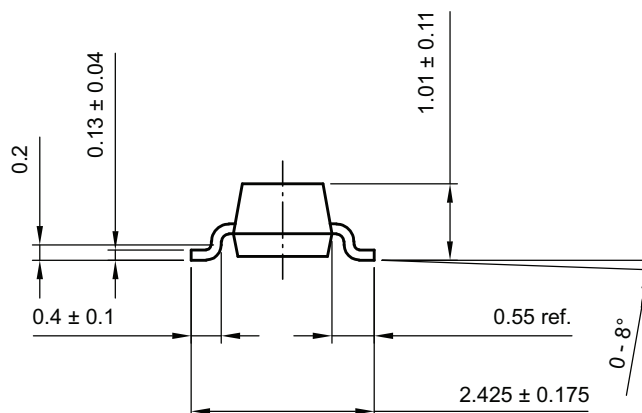


Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage



PACKAGE DIMENSIONS in millimeters: **SOT-23**



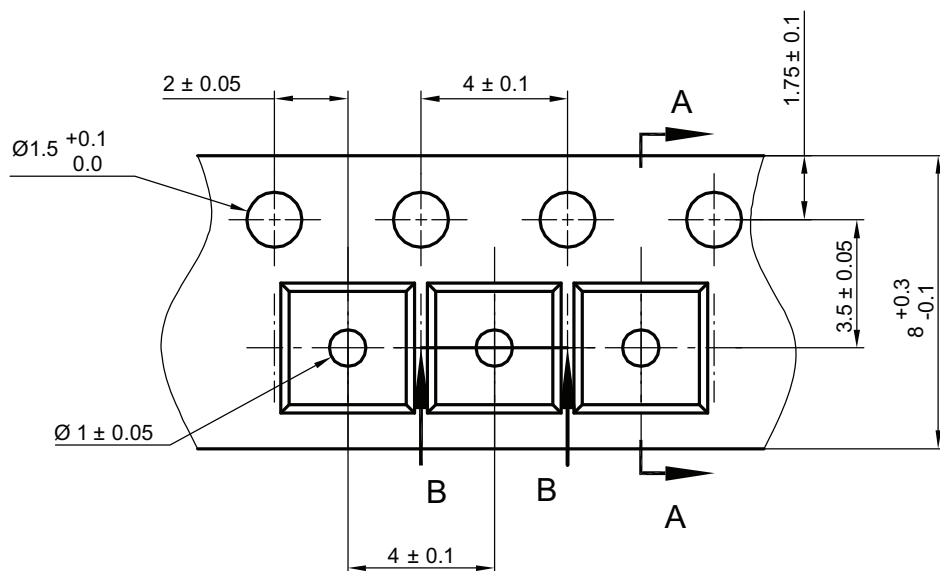
footprint recommendation:



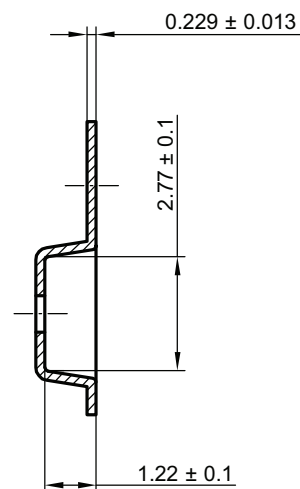
Created - Date: 18-Oct-2021
Rev. 01 - Date: 18-Jan-2022
S8-V-3929.01-009 (4)



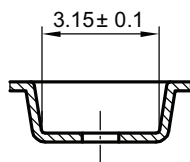
CARRIER TAPE SOT-23



A-A Section



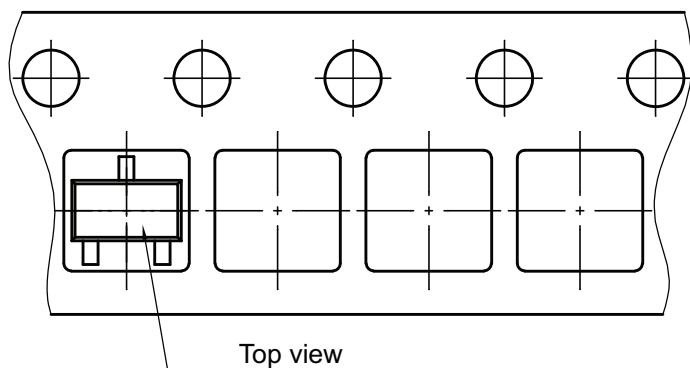
B-B Section



Created Date: 04-Feb-2010
Rev. Date: 07-Feb-2022
S8-V-3929.01-005 (4)

ORIENTATION IN CARRIER TAPE SOT-23

Unreeling direction →



SOT-23

Created Date: 04-Feb-2010
Rev. Date: 07-Nov-2022
S8-V-3929.01-005 (4)



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