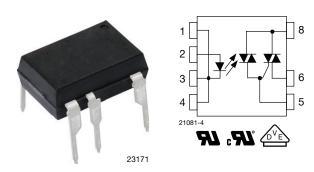


### www.vishay.com

# Vishay Semiconductors

# **Optocoupler, Power Phototriac**



PIN	FUNCTION	
1	LED cathode	
2	LED anode	
3	LED cathode	
4	LED cathode	
5	Triac gate	
6	Triac T1	
8	Triac T2	

## **LINKS TO ADDITIONAL RESOURCES**





## **DESCRIPTION**

The VO2223A is an optically couple phototriac driving a power triac in a DIP-8 package. It provides a 5300 V of input to output isolation.

#### **FEATURES**

- Maximum trigger current (I<sub>FT</sub>): 10 mA
- Isolation test voltage 5300 V<sub>RMS</sub>
- Peak off-state voltage 600 V
- Load current 1 A<sub>RMS</sub>
- dV/dt of 210 V/µs
- DIP-8 package
- · Pure tin leads
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

#### **APPLICATIONS**

- · Home appliances (air conditioners, microwave ovens, machines, personal hygiene systems, refrigerators, fan heaters, inductive heating cooker, water heaters, etc.)
- Industrial equipments

## **AGENCY APPROVALS**

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1

ORDERING INFORMATION				
V O 2 2 2	3 A - X 0 0 #			
PART NUMBER	PACKAGE OPTION 7.62 mm			
AGENCY CERTIFIED / PACKAGE	TRIGGER, CURRENT I <sub>FT</sub> (mA)			
UL, cUL	10			
DIP-8	VO2223A			
DIP-8, option 7	VO2223A-X007T			
UL, cUL, VDE (option 1)	10			
DIP-8	VO2223A-X001			



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL VALUE		UNIT		
INPUT						
LED continuous forward current		I <sub>F</sub>	50	mA		
LED reverse voltage		$V_R$	5	V		
OUTPUT						
Repetitive peak off-state voltage	Sine wave, 50 Hz to 60 Hz, gate open	$V_{DRM}$	600	V		
On-state RMS current		I <sub>T(RMS)</sub>	1	Α		
Peak non-repetitive surge current (50 Hz, peak)		I <sub>TSM</sub>	10	Α		
COUPLER						
Total power dissipation (2)		P <sub>diss</sub>	1.2	W		
Ambient temperature range		T <sub>amb</sub>	-40 to +85	°C		
Storage temperature range		T <sub>stg</sub>	-40 to +125	°C		
Soldering temperature (1)	t ≤ 10 s max.	T <sub>sld</sub>	260	°C		
Isolation test voltage	For 1 s	V <sub>ISO</sub>	5300	$V_{RMS}$		

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to wave profile for soldering conditions for through hole devices
- (2) Total power dissipation value is based on 2S2P PCB

### **ABSOLUTE MAXIMUM RATING CURVES**

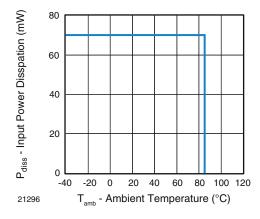


Fig. 1 - Power Dissipation vs. Temperature

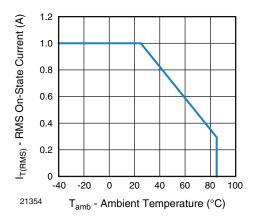


Fig. 2 - Allowable Load Current vs. Ambient Temperature

#### Note

• The allowable load current was calculated out under a given operating conditions and only for reference: LED power: Q<sub>E</sub> = 0.015 W,  $\theta_{BA}$  (4-layer) = 30 °C/W

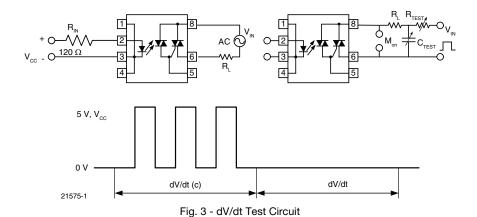
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## Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED trigger current	V <sub>T</sub> = 6 V	I <sub>FT</sub>	2.5	-	10	mA
Input reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>	ı	-	10	μA
LED forward voltage	I <sub>F</sub> = 10 mA	$V_{F}$	0.9	-	1.4	V
OUTPUT						
Peak on-state voltage	I <sub>TM</sub> = 1 A	$V_{TM}$	ı	-	1.7	٧
Peak off-state current	V <sub>DRM</sub> = 600 V, T <sub>A</sub> = 110 °C	I <sub>DRM</sub>	ı	-	100	μΑ
Holding current	$R_L = 100 \Omega$	I <sub>H</sub>	-	-	25	mA
Critical rate of rise of off-state voltage	V <sub>IN</sub> = 400 V <sub>RMS</sub> (Fig. 3)	dV/dt <sub>cr</sub>	ı	210	-	V/µs
Critical rate of rise of commutating voltage	$V_{IN} = 240 V_{RMS}, I_T = 1 A_{RMS}$ (Fig. 3)	dV/dt <sub>crq</sub>	-	0.7	-	V/µs

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.



SAFETY AND INSULATION RATINGS **TEST CONDITION** SYMBOL MIN. TYP. UNIT **PARAMETER** MAX. IEC 68 part 1 40/85/21 Climatic classification Pollution degree **DIN VDE 0109** 2 \_ Insulation group IIIa CTI 175 Tracking resistance (comparative tracking index) Transient overvoltage Highest allowable overvoltage 8000  $V_{IOTM}$  $V_{peak}$ 890 Maximum working insulation voltage Recurring peak voltage  $V_{IORM}$  $V_{peak}$ Insulation resistance at 25 °C  $V_{10} = 500 \text{ V}$  $\geq 10^{12}$  $R_{IS}$ Ω V<sub>IO</sub> = 500 V ≥ 10<sup>9</sup> Insulation resistance at T<sub>S</sub>  $R_{\text{IS}}$ Ω \_ Insulation resistance at 100 °C  $V_{IO} = 500 \text{ V}$  $\mathsf{R}_{\mathsf{IS}}$  $\geq 10^{11}$ Ω Method b, Partial discharge test voltage  $V_{pd}$ 1424  $V_{peak}$  $V_{pd} = V_{IORM} \times 1.6$ °C Case temperature  $T_{SI}$ 165 Safety limiting values maximum values allowed 150 Input current  $I_{SI}$ mA in the event of a failure Output power  $P_{SO}$ 2000 mW Measured from input terminals Minimum external air gap (clearance distance) to output terminals, shortest ≥ 7 mm distance through air Measured from input terminals Minimum external tracking (creepage distance) to output terminals, shortest ≥ 7 mm distance path along body

#### Note

This phototriac coupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with safety ratings shall be
ensured by means of protective circuits.

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

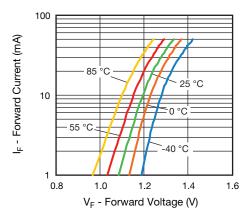


Fig. 4 - Forward Current vs. Forward Voltage

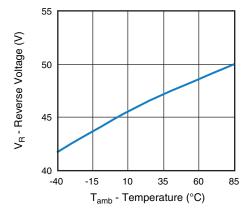


Fig. 5 - Reverse Voltage vs. Temperature

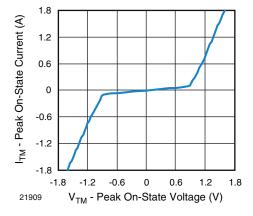


Fig. 6 - On-State Current vs. On-State Voltage

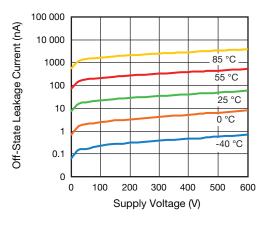


Fig. 7 - Off-State Leakage Current vs. Voltage

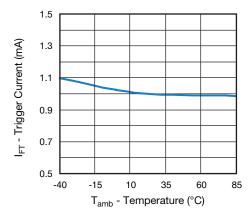


Fig. 8 - Normalized Trigger Input Current vs. Temperature

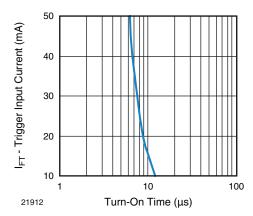


Fig. 9 - Trigger Input Current vs. Turn-on Time



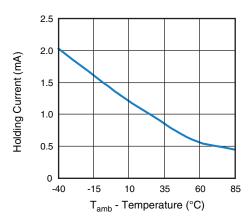


Fig. 10 - Normalized Holding Current vs. Temperature

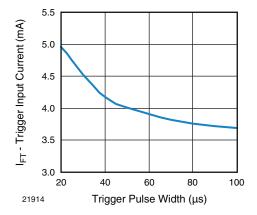


Fig. 11 - Trigger Current vs. Trigger Pulse Width

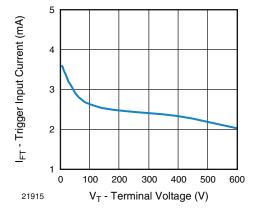
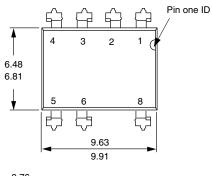


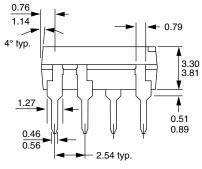
Fig. 12 - Trigger Current vs.  $V_{\text{LOAD}}$ 

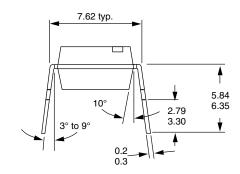


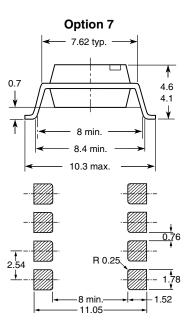
## **PACKAGE DIMENSIONS** in millimeters



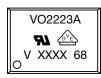








## PACKAGE MARKING (Example of VO2223A-X001)



## Note

• XXXX = LMC (lot marking code)

## **PACKING INFORMATION**

DEVICE PER TUBE				
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX	
DIP-8	50	40	2000	

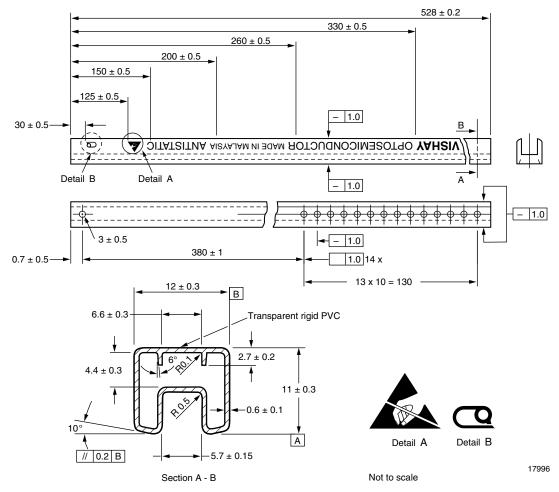


Fig. 13 - Shipping Tube Specifications for DIP Packages

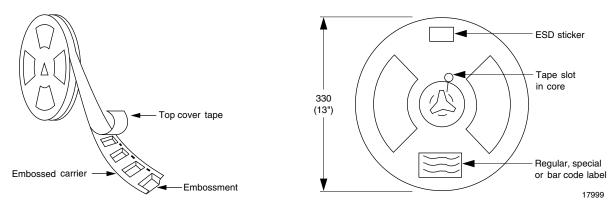


Fig. 14 - Tape and Reel Shipping Medium

17998

Fig. 15 - Tape and Reel Shipping Medium



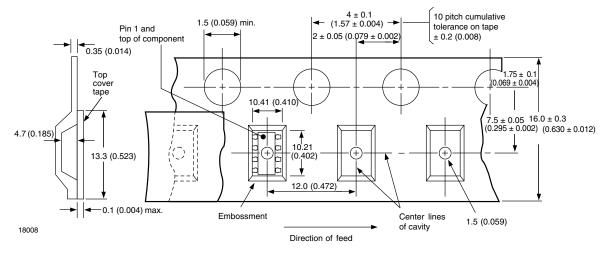


Fig. 16 - Tape and Packing (1000 pieces on reel)

#### **SOLDER PROFILES**

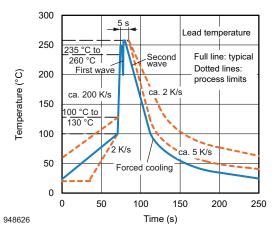


Fig. 17 - Recommended Wave Soldering Double Wave Profile for DIP Devices

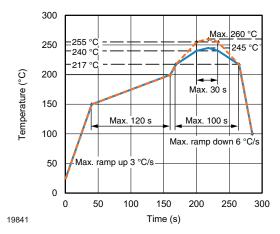


Fig. 18 - Recommended Lead (Pb)-free Reflow Solder Profile for SMD Devices

#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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