

# Automotive P-Channel 100 V (D-S) 175 °C MOSFET

## PRODUCT SUMMARY

$V_{DS}$ (V)	- 100
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = -10$ V	0.040
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = -4.5$ V	0.048
$I_D$ (A)	- 40
Configuration	Single

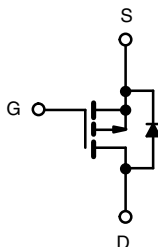
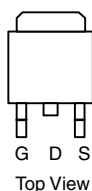
## FEATURES

- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- 100 %  $R_g$  and UIS Tested
- AEC-Q101 Qualified<sup>d</sup>
- Material categorization:  
For definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

AUTOMOTIVE  
GRADE

**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

TO-263



P-Channel MOSFET

## ORDERING INFORMATION

Package	TO-263
Lead (Pb)-free and Halogen-free	SQM40P10-40L-GE3

## ABSOLUTE MAXIMUM RATINGS ( $T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$V_{DS}$	- 100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	- 40	A
		- 23	
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	- 60	
Pulsed Drain Current <sup>b</sup>	$I_{DM}$	- 160	
Single Pulse Avalanche Current	$I_{AS}$	- 45	
Single Pulse Avalanche Energy	$E_{AS}$	100	mJ
Maximum Power Dissipation <sup>b</sup>	$P_D$	150	W
		50	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 175	°C

## THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	$R_{thJA}$	40	°C/W
Junction-to-Case (Drain)	$R_{thJC}$	1	

### Notes

- Package limited.
- Pulse test; pulse width  $\leq 300$   $\mu$ s, duty cycle  $\leq 2$  %.
- When mounted on 1" square PCB (FR-4 material).
- Parametric verification ongoing.



SPECIFICATIONS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA		- 100	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA		- 1.5	- 2.0	- 2.5	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V		-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 100 V	-	-	- 1	μA
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 100 V, T <sub>J</sub> = 125 °C	-	-	- 50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 100 V, T <sub>J</sub> = 175 °C	-	-	- 250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = - 10 V	V <sub>DS</sub> ≤ - 5 V	- 30	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 17 A	-	0.033	0.040	Ω
		V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 17 A, T <sub>J</sub> = 125 °C	-	-	0.060	
		V <sub>GS</sub> = - 10 V	I <sub>D</sub> = - 17 A, T <sub>J</sub> = 175 °C	-	-	0.099	
		V <sub>GS</sub> = - 4.5 V	I <sub>D</sub> = - 14 A	-	0.0367	0.0480	
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 17 A		-	47	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = - 25 V, f = 1 MHz	-	4236	5295	pF
Output Capacitance	C <sub>oss</sub>			-	314	395	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	216	270	
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>GS</sub> = - 10 V	V <sub>DS</sub> = - 50 V, I <sub>D</sub> = - 40 A	-	89	134	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			-	11.6	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	19.6	-	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.4	2.89	4.5	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = - 50 V, R <sub>L</sub> = 1.25 Ω I <sub>D</sub> ≅ - 40 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω		-	10	15	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			-	10	15	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	63	95	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	20	30	
Source-Drain Diode Ratings and Characteristics <sup>b</sup>							
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	- 160	A
Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = - 30 A, V <sub>GS</sub> = 0 V		-	- 0.9	- 1.5	V

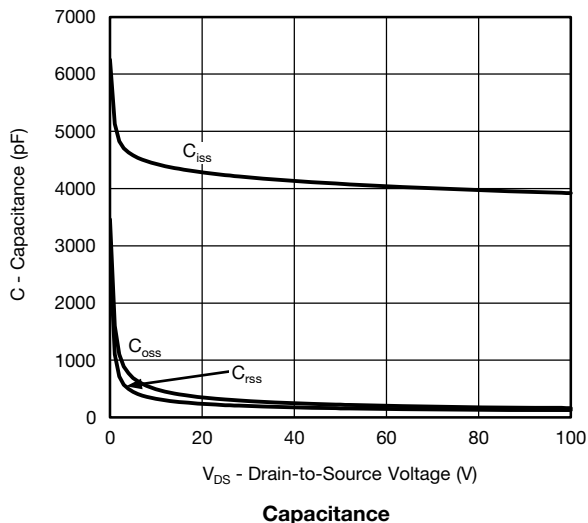
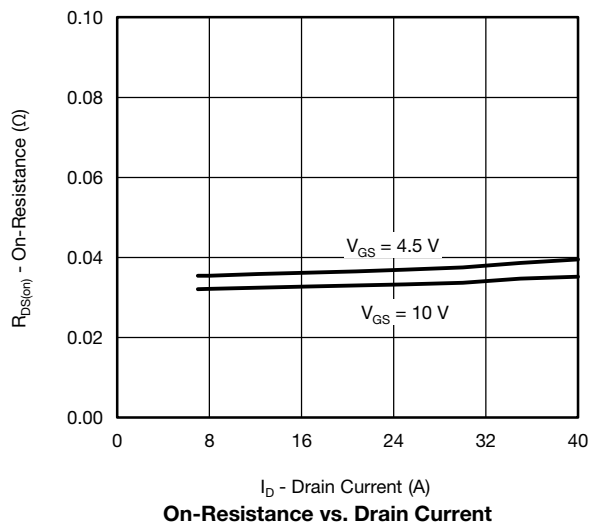
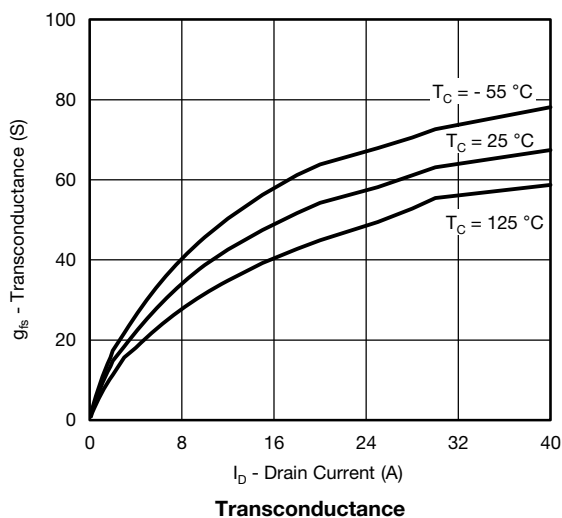
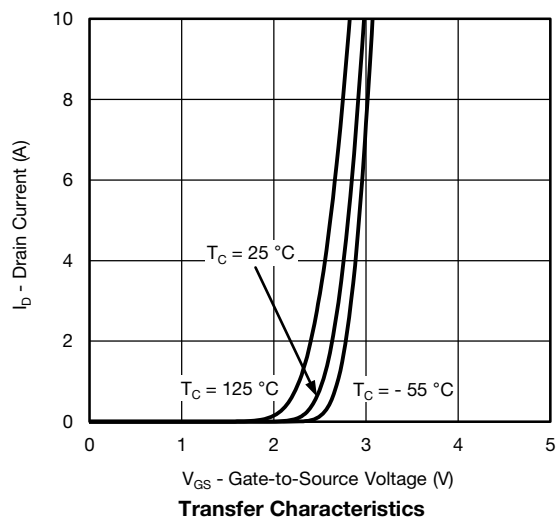
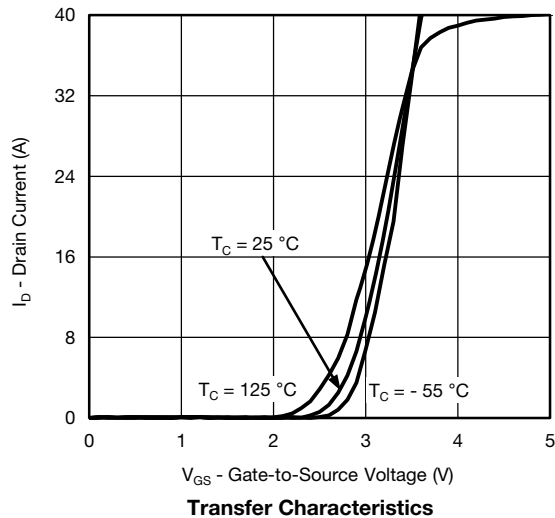
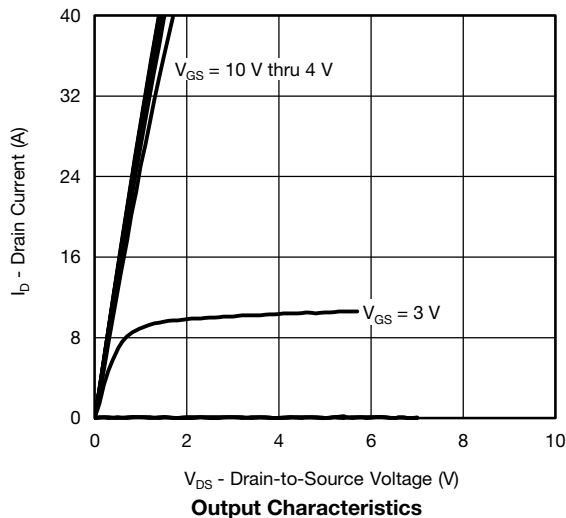
**Notes**

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.  
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

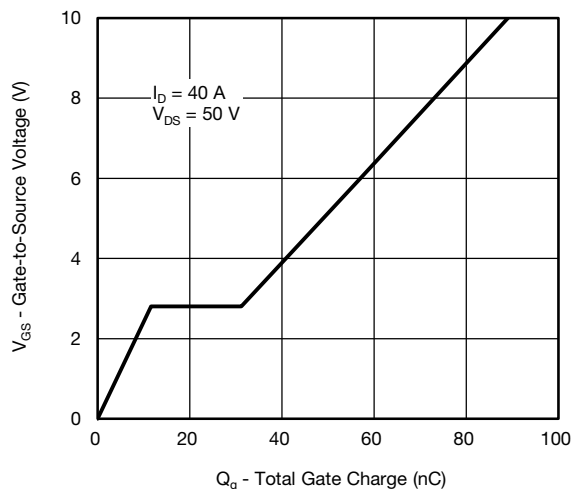


**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)

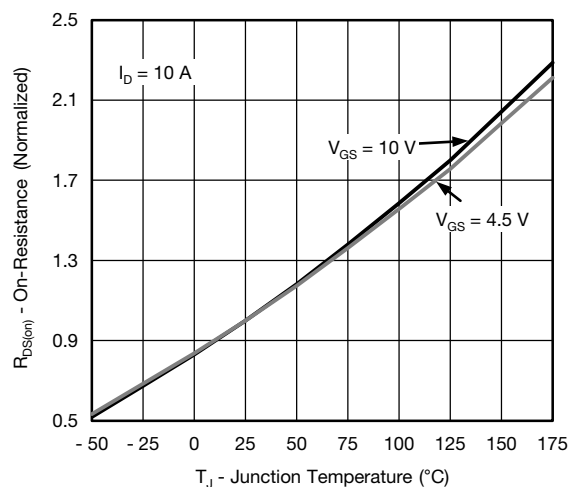




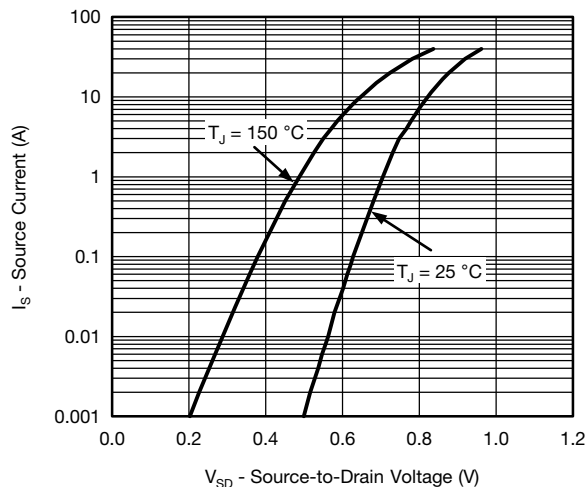
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



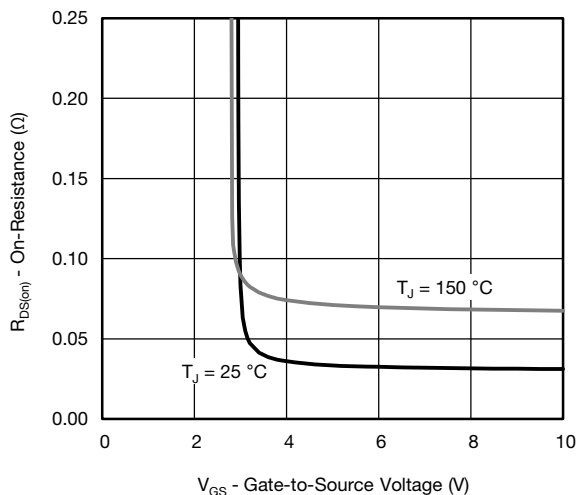
**Gate Charge**



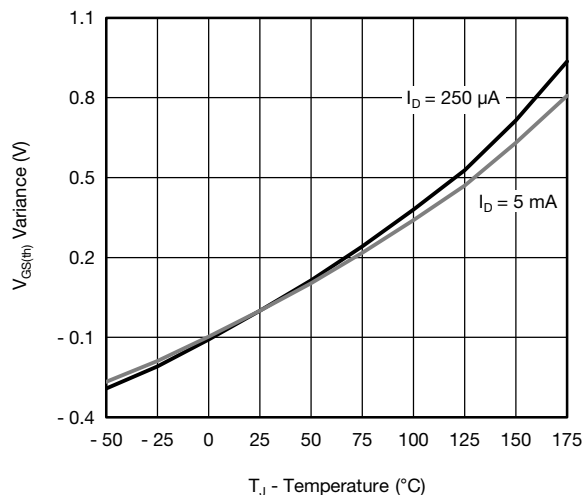
**On-Resistance vs. Junction Temperature**



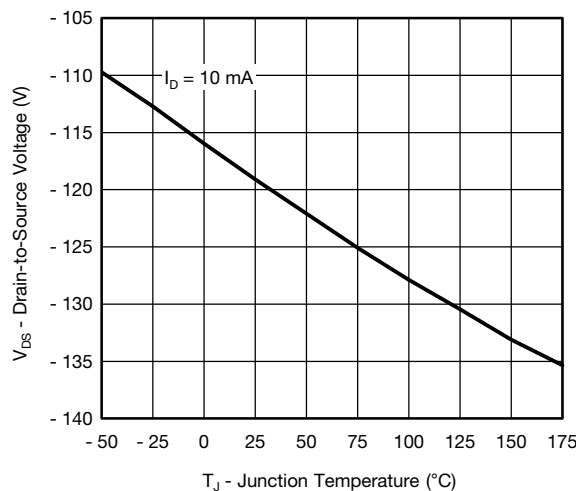
**Source Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**



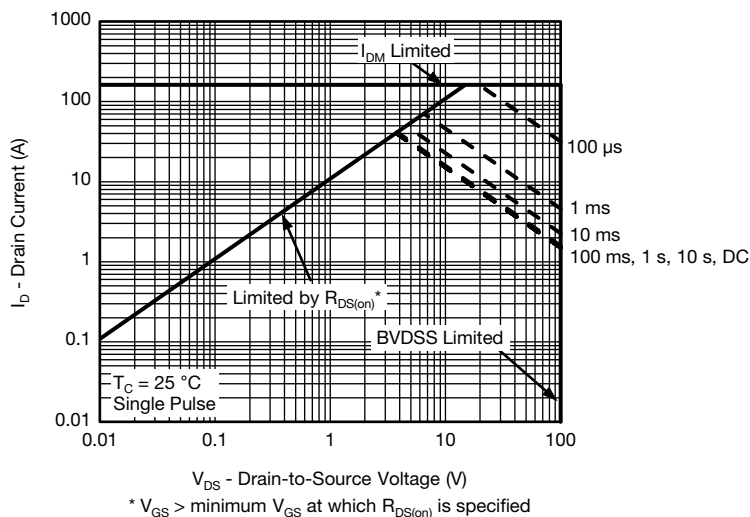
**Threshold Voltage**



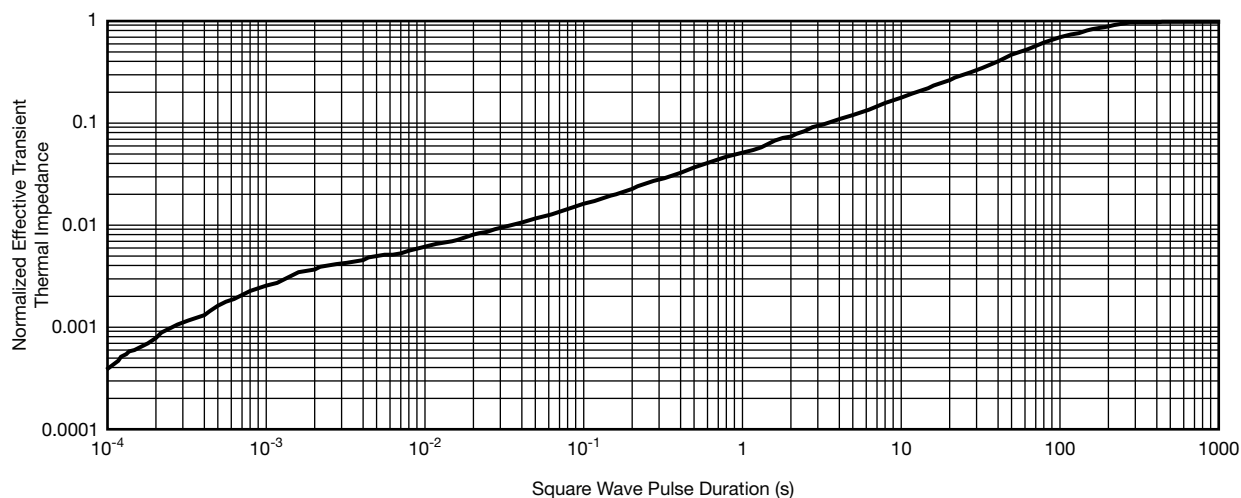
**Drain Source Breakdown vs. Junction Temperature**



**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



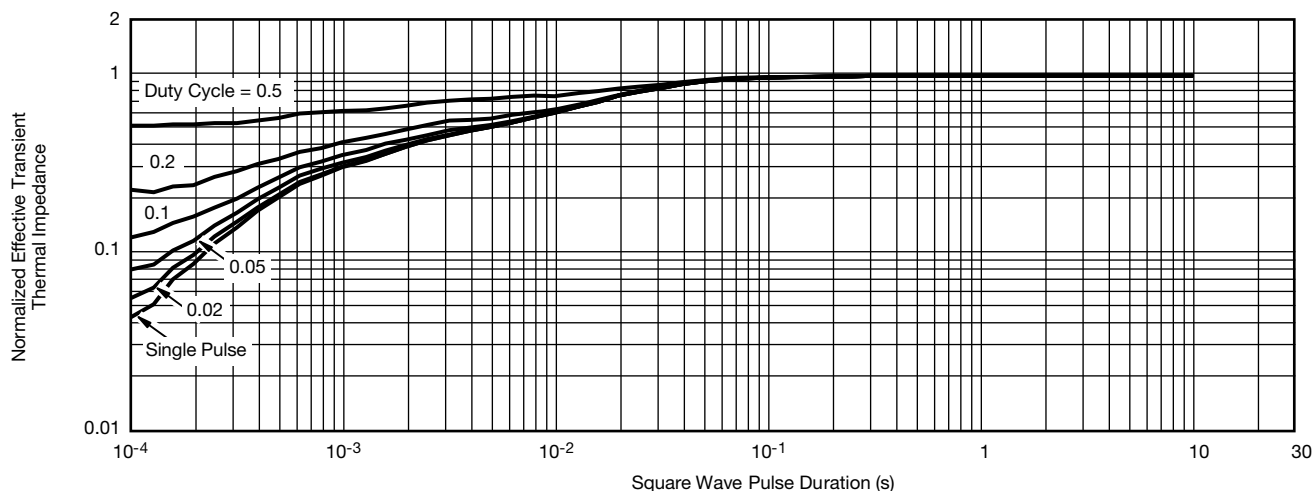
**Safe Operating Area**



**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**THERMAL RATINGS** ( $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Case**

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient ( $25\text{ }^{\circ}\text{C}$ )
  - Normalized Transient Thermal Impedance Junction-to-Case ( $25\text{ }^{\circ}\text{C}$ )are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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**RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead**



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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