AUTOMOTIVE

COMPLIANT

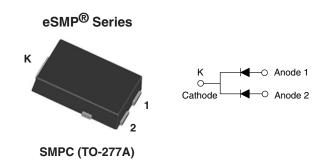
HALOGEN

FREE



Vishay Semiconductors

Hyperfast Rectifier, 2 x 3 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 3 A			
V _R	100 V			
V _F at I _F	0.75 V			
t _{rr (typ.)}	27 ns			
T _J max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Common cathode			

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		V_{RRM}		100	V
Average restified forward current	per device	1	T _{Sp} = 165 °C	6	۸
Average rectified forward current	per diode	I _{F(AV)}		3	
Non repetitive peak aurae aurrent	per device		T _J = 25 °C, 6 ms square pulse	150	А
Non-repetitive peak surge current	per diode	I _{FSM}		80	
Operating junction and storage terr	peratures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	100	-	-	
Converd voltage per diada		I _F = 3 A	-	0.87	0.94	V
Forward voltage, per diode	V _F	I _F = 3 A, T _J = 125 °C	-	0.75	0.79	
Poverse leakage current, per diede	I _R	V _R = V _R rated	-	-	2	
Reverse leakage current, per diode		$T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	0.7	10	μΑ
Junction capacitance	C _T	V _R = 100 V	-	13	-	pF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A, dI_F/dt =$	$= 50 \text{ A/}\mu\text{s}, V_{\text{R}} = 30 \text{ V}$	-	27	ı	
Boyorga raggyary time		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	ı	25	
Reverse recovery time		T _J = 25 °C		-	20	-	ns
		T _J = 125 °C		-	26	-	
Dook roopyony gurrent	1	T _J = 25 °C	I _F = 3 A dI _F /dt = 200 A/μs V _R = 160 V	-	2.4	-	Α
Peak recovery current I _{RRM}	IRRM	T _J = 125 °C		-	3.8	-	
Deverage receivery charge	0	T _J = 25 °C		-	23	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	50	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to mount, per diode	R _{thJM}		-	2.8	4	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style SMPC (TO-277A)		NC	H1	

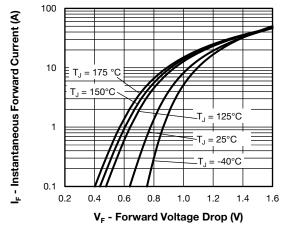


Fig. 1 - Typical Forward Voltage Drop Characteristics

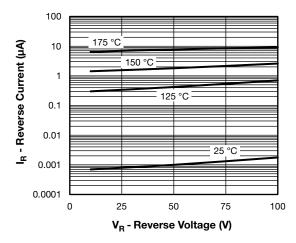


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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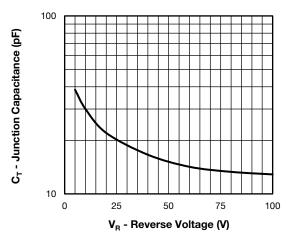


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

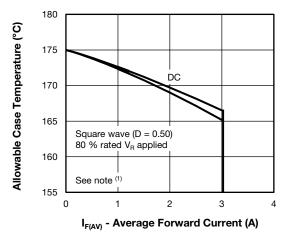


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

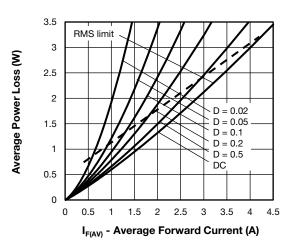


Fig. 5 - Forward Power Loss Characteristics

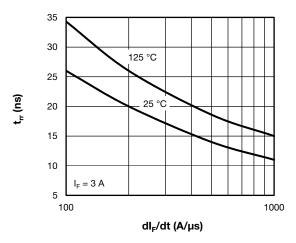


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

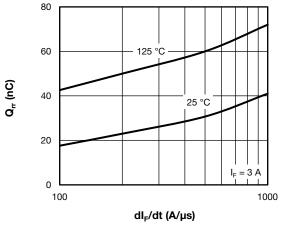


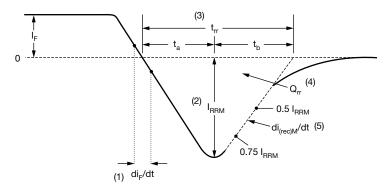
Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$



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- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_{r}$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

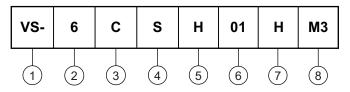
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- Vishay Semiconductors product
- Current rating (6 = 6 A)
- 3 Circuit configuration:

C = common cathode

4 - S = SMPC package

5 - Process type,

H = hyper fast recovery

6 - Voltage code (01 = 100 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-6CSH01HM3/86A	1500	1500	7" diameter plastic tape and reel			
VS-6CSH01HM3/87A	6500	6500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95570			
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96378			



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SMPC (TO-277A)

DIMENSIONS in inches (millimeters)





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