HALOGEN

FREE



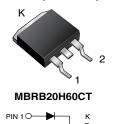
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## Vishay General Semiconductor

# **Dual Common Cathode Schottky Rectifier**

High Barrier Technology for Improved High Temperature Performance

#### D<sup>2</sup>PAK (TO-263AB)



HEATSINK

## **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 10 A			
$V_{RRM}$	60 V			
I <sub>FSM</sub>	150 A			
V <sub>F</sub>	0.61 V			
I <sub>R</sub>	100 μΑ			
T <sub>J</sub> max.	175 °C			
Package	D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	Common cathode			

#### **FEATURES**

- Power pack
- · Guardring for overvoltage protection
- Low power loss, high efficiency
- Low forward voltage drop
- · Low leakage current
- High forward surge capability
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **TYPICAL APPLICATIONS**

For use in low voltage, high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters, and polarity protection application.

## **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Base P/NHM3 - RoHS-compliant, halogen-free, AEC-Q101

qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

HM3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER			MBRB20H60CT	UNIT		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	60			
Working peak reverse voltage		V <sub>RWM</sub>	60	V		
Maximum DC blocking voltage	ıximum DC blocking voltage		60			
Maximum average forward rectified current (fig. 1)	total device		20	_		
	per diode	I <sub>F(AV)</sub>	10	_ A		
Non-repetitive avalanche energy per diode at 25 °C, I <sub>AS</sub> = 4 A, L = 10 mH			80	mJ		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	150	А		
Peak repetitive reverse surge current per diode at $t_p = 2.0$	I <sub>RRM</sub>	0.5				
Peak non-repetitive reverse energy (8/20 µs waveform)			10	mJ		
Electrostatic discharge capacitor voltage Human body model: C = 100 pF, R = 1.5 k $\Omega$		V <sub>C</sub>	25	kV		
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-65 to +175	°C		



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS		MBRB20H60CT		UNIT
PANAIVIETEN	STMBOL			TYP.	MAX.	ONII
Maximum instantaneous forward voltage per diode	V <sub>F</sub> (1)	I <sub>F</sub> = 10 A	T <sub>C</sub> = 25 °C	-	0.71	V
		I <sub>F</sub> = 10 A	T <sub>C</sub> = 125 °C	0.57	0.61	
		I <sub>F</sub> = 20 A	T <sub>C</sub> = 25 °C	=	0.85	
		I <sub>F</sub> = 20 A	T <sub>C</sub> = 125 °C	0.68	0.71	
Maximum reverse current per diode	I <sub>R</sub> <sup>(2)</sup>	1 (2) Data d 1 (	T <sub>J</sub> = 25 °C	=	100	μA
		IR (=)	Rated V <sub>R</sub>	T <sub>J</sub> = 125 °C	2.0	12

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL MBRB20H60CT		UNIT		
Typical resistance, junction to case per diode	$R_{ heta JC}$	2.0	°C/W		

ORDERING INFORMATION							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
D <sup>2</sup> PAK (TO-263AB)	MBRB20H60CTHM3/I	1.35	I	800/reel	Tape and reel		

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## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>C</sub> = 25 °C unless otherwise noted)

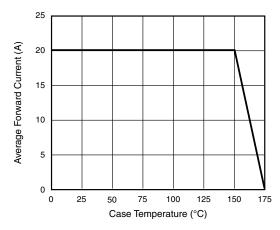


Fig. 1 - Forward Current Derating Curve (Total)

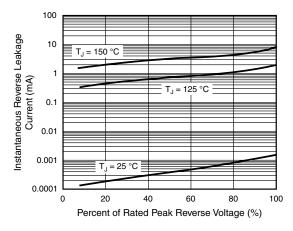


Fig. 4 - Typical Reverse Characteristics Per Diode

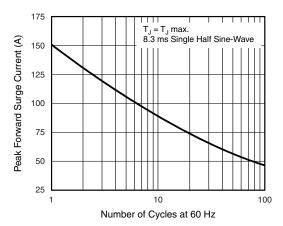


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

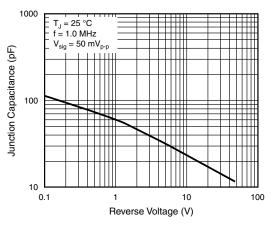


Fig. 5 - Typical Junction Capacitance Per Diode

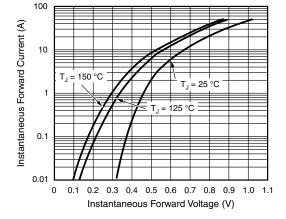


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

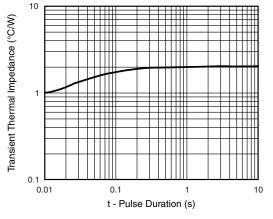


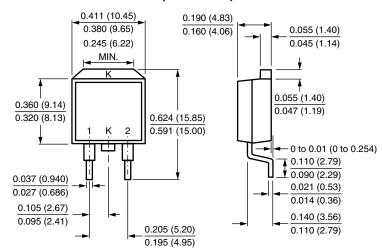
Fig. 6 - Typical Transient Thermal Impedance Per Diode



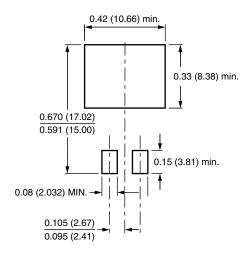
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## D<sup>2</sup>PAK (TO-263AB)



#### **Mounting Pad Layout**





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