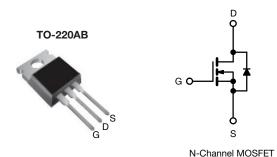
Vishay Siliconix

COMPLIANT

HALOGEN

FREE

E Series Power MOSFET



PRODUCT SUMMARY					
V_{DS} (V) at T_J max.	70	00			
R _{DS(on)} typ. (Ω) at 25 °C	V _{GS} = 10 V	0.070			
Q _g max. (nC)	8	0			
Q _{gs} (nC)	2	10			
Q _{gd} (nC)	1	19			
Configuration	Sin	Single			

FEATURES

- 4th generation E series technology
- Low figure-of-merit (FOM) Ron x Qg
- Low effective capacitance (Co(er))
- · Reduced switching and conduction losses
- Avalanche energy rated (UIS)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
 - High-intensity discharge (HID)
 - Fluorescent ballast lighting
- Industrial
 - Welding
 - Induction heating
 - Motor drives
 - Battery chargers
 - Solar (PV inverters)

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free and halogen-free	SiHP074N65E-GE3

PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage			V_{DS}	650	V	
Gate-source voltage			V_{GS}	± 30	\ \ \ \	
Continuous drain current (T _J = 150 °C)	V _{GS} at 10 V	$T_{\rm C} = 25 ^{\circ}{\rm C}$ $T_{\rm C} = 100 ^{\circ}{\rm C}$	- I _D	35		
	V _{GS} at 10 V	T _C = 100 °C		24	А	
Pulsed drain current a			I _{DM}	91		
Linear derating factor				2	W/°C	
Single pulse avalanche energy b			E _{AS}	173	mJ	
Maximum power dissipation			P_{D}	250	W	
Operating junction and storage temperature range			T _J , T _{stg}	-55 to +150	°C	
Drain-source voltage slope $T_J = 125 ^{\circ}\text{C}$		dv/dt	100	V/ns		
Reverse diode dv/dt ^d			12	V/ns		
Soldering recommendations (peak temperatur	e) ^c	For 10 s		260	°C	

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature
- b. V_{DD} = 120 V, starting T_J = 25 °C, L = 28.2 mH, R_q = 25 Ω , I_{AS} = 3.5 A
- c. 1.6 mm from case
- d. $I_{SD} \le I_D$, di/dt = 100 A/ μ s, starting $T_J = 25$ °C



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THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Maximum junction-to-ambient	R_{thJA}	=	62	°C/W	
Maximum junction-to-case (drain)	R_{thJC}	-	0.5	C/VV	

PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		650	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference to 25 °C, I _D = 1 mA		-	0.63	-	V/°C
Gate-source threshold voltage (N)	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		3.0	-	5.0	V
Oala a sana lada a sa	I _{GSS}	V _{GS} = ± 20 V		-	-	± 100	nA
Gate-source leakage		,	V _{GS} = ± 30 V		-	± 1	μA
Zana mata waltana alusia awanat		$V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V}$		-	-	1	μА
Zero gate voltage drain current	I _{DSS}	V _{DS} = 520 V	V _{DS} = 520 V, V _{GS} = 0 V, T _J = 125 °C		-	10	
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 15 A	-	0.070	0.079	Ω
Forward transconductance	9 _{fs}	V _{DS} = 10 V, I _D = 19 A		-	16	-	S
Dynamic							
Input capacitance	C _{iss}	$V_{GS} = 0 \text{ V},$ $V_{DS} = 100 \text{ V},$ $f = 100 \text{ KHz}$		-	2904	-	pF
Output capacitance	C _{oss}			-	106	-	
Reverse transfer capacitance	C _{rss}			-	2	-	
Effective output capacitance, energy related	$C_{o(er)}$	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		-	115	-	
Effective output capacitance, time related	C _{o(tr)}			-	772	-	
Total gate charge	Qg			-	53	80	
Gate-source charge	Q _{gs}	V _{GS} = 10 V	$V_{GS} = 10 \text{ V}$ $I_D = 19 \text{ A}, V_{DS} = 520 \text{ V}$		20	-	nC
Gate-drain charge	Q _{gd}				19	-	
Turn-on delay time	t _{d(on)}	$V_{DD} = 520 \text{ V}, I_{D} = 19 \text{ A}, \ V_{GS} = 10 \text{ V}, R_{g} = 9.1 \Omega$		-	29	58	
Rise time	t _r			-	53	106	
Turn-off delay time	t _{d(off)}			_	42	84	ns
Fall time	t _f			-	29	58	
Gate input resistance	R_g	f = 1 MHz, open drain		0.3	0.6	1.2	Ω
Drain-Source Body Diode Characteristic	s						
Continuous source-drain diode current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	35	
Pulsed diode forward current	I _{SM}			-	-	91	_ A
Diode forward voltage	V _{SD}	T _J = 25 °C, I _S = 19 A, V _{GS} = 0 V		-	-	1.2	V
Reverse recovery time	t _{rr}	T _J = 25 °C, I _F = I _S = 19 A, di/dt = 100 A/ μ s, V _R = 25 V		-	447	894	ns
Reverse recovery charge	Q _{rr}			-	7	14	μC
Reverse recovery current	I _{RRM}			_	25	-	A



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

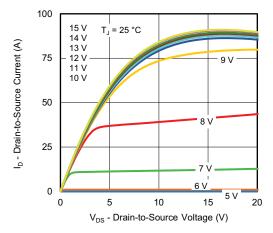


Fig. 1 - Typical Output Characteristics

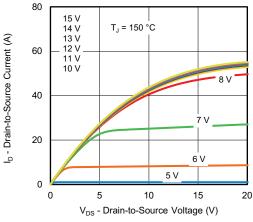


Fig. 2 - Typical Output Characteristics

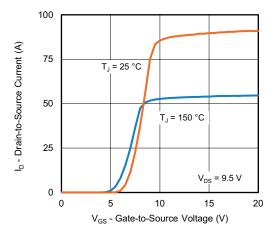


Fig. 3 - Typical Transfer Characteristics

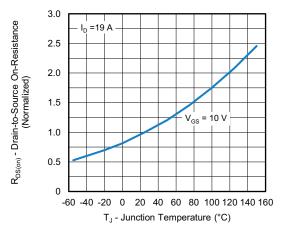


Fig. 4 - Normalized On-Resistance vs. Temperature

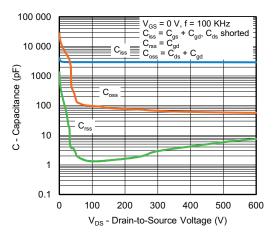


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

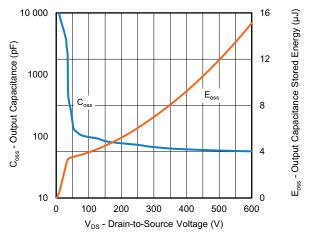


Fig. 6 - Coss and Eoss vs. VDS



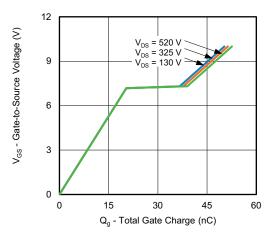


Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage

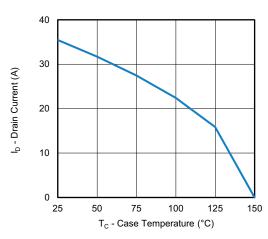


Fig. 9 - Maximum Drain Current vs. Case Temperature

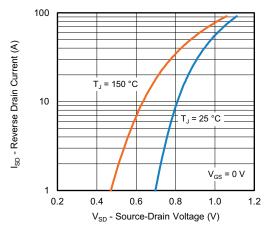


Fig. 8 - Typical Source-Drain Diode Forward Voltage

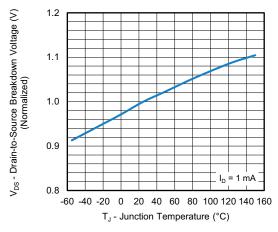


Fig. 10 - Temperature vs. Drain-to-Source Voltage

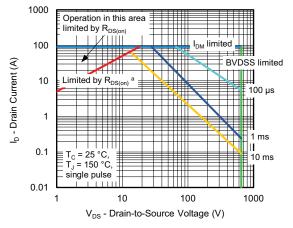


Fig. 11 - Maximum Safe Operating Area

Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



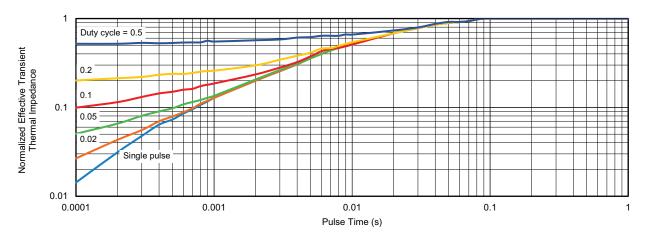


Fig. 12 - Normalized Transient Thermal Impedance, Junction-to-Case

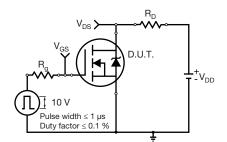


Fig. 13 - Switching Time Test Circuit

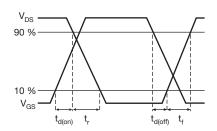


Fig. 14 - Switching Time Waveforms

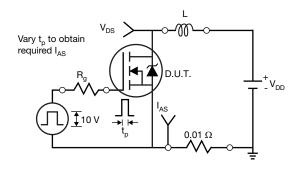


Fig. 15 - Unclamped Inductive Test Circuit

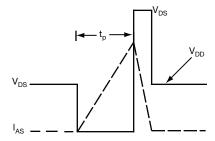


Fig. 16 - Unclamped Inductive Waveforms

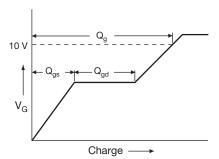


Fig. 17 - Basic Gate Charge Waveform

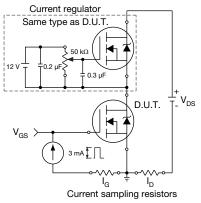


Fig. 18 - Gate Charge Test Circuit



Peak Diode Recovery dv/dt Test Circuit





Fig. 19 - For N-Channel

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