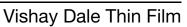
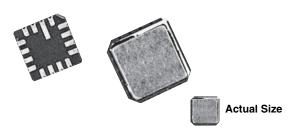
HALOGEN

FREE





Hermetic, 50 mil Pitch, Leadless Thin Film Chip Resistor, Surface Mount Network



Vishay Dale Thin Film offers a wide resistance range in 16, 20, and 24 terminal hermetic leadless chip carriers. The standard circuits in the ohmic ranges listed below will utilize the outstanding wraparound terminations developed for chip resistors. Should one of the standards not fit your application, consult the applications engineering group as we may be able to meet your requirements.

FEATURES

- High purity alumina substrate for high power dissipation
- Leach resistant terminations with nickel barrier
- 16, 20, 24 terminal gold plated wraparound true hermetic packaging
- Military/aerospace
- Hermetically sealed
- Isolated/bussed circuits
- · Ideal for military/aerospace applications
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details.

TYPICAL PERFORMANCE

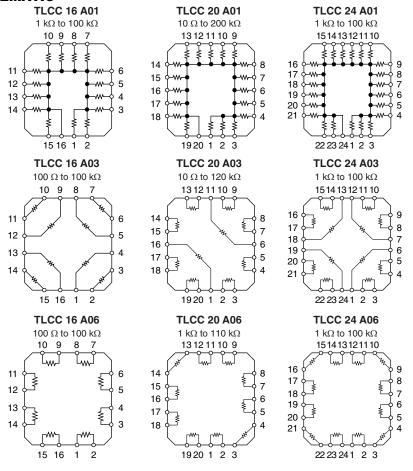
	ABSOLUTE	TRACKING	
TCR	25	5	
	ABSOLUTE	RATIO	
TOL.	0.1	NA	

Note

· Resistance range: Noted on schematics.

SCHEMATIC

Revision: 14-Jul-16



LCC 20B

1920 1

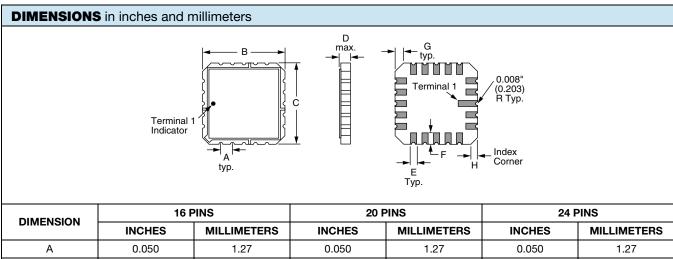
Document Number: 60012

Vishay Dale Thin Film

STANDARD ELECTRICAL SPECIFICATIONS					
TEST	SPECIFICATIONS	CONDITIONS			
Material	Passivated nichrome	-			
Pin/Lead Number	16, 20, 24	-			
Resistance Range	10 Ω to 250 k Ω per resistor	-			
TCR: Absolute	± 25 ppm/°C to ± 300 ppm/°C	-55 °C to +125 °C			
TCR: Tracking	± 5 ppm/°C	-55 °C to +125 °C			
Tolerance: Absolute	± 0.1 % to ± 1.0 %	+25 °C			
Tolerance: Ratio	N/a	-			
Power Rating: Resistor	50 mW max. = common circuits	Maximum at +70 °C			
	100 mW max. = isolated circuits	Maximum at +70 G			
Power Rating: Package	500 mW	Maximum at +70 °C			
Stability: Absolute	ΔR ± 0.05 %	2000 h at +70 °C			
Stability: Ratio	-	-			
Voltage Coefficient	< 5 ppm/V (typical)	-			
Working Voltage	100 V max. not to exceed √P x R	-			
Operating Temperature Range	-55 °C to +125 °C	-			
Storage Temperature Range	-55 °C to +150 °C	-			
Noise	< -30 dB	-			
Thermal EMF	0.008 μV/°C	-			
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C			
Shelf Life Stability: Ratio	-	-			

Note

• Tantalum nitride film is custom, consult factory.



DIMENSION	10 PINS		20 PINS		24 PINS	
	INCHES	MILLIMETERS	INCHES	MILLIMETERS	INCHES	MILLIMETERS
Α	0.050	1.27	0.050	1.27	0.050	1.27
В	0.300	7.26	0.350	8.89	0.400	10.16
С	0.300	7.26	0.350	8.89	0.400	10.16
D	0.077	1.96	0.077	1.96	0.077	1.96
E	0.025	0.635	0.025	0.635	0.025	0.635
F	0.050	1.27	0.050	1.27	0.050	1.27
G	0.040	1.02	0.040	1.02	0.040	1.02
Н	0.020	0.508	0.020	0.508	0.020	0.508



Vishay Dale Thin Film

MECHANICAL SPECIFICATIONS				
Resistive Element	Passivated nichrome			
Substrate Material	Alumina			
Body	Ceramic			
Terminals	Gold over nickel			
Marking Resistance to Solvents	Per MIL-PRF-83401			
Tin Lead Option	Sn63			
Lead (Pb)-free Option	96.5 % Sn, 3.0 % Ag, 0.5 % Cu			
Tin Lead and Lead (Pb)-free	Hot solder dip			

GLOBAL PART NUMBER INFORMATION								
	New Global Part Numbering: TLCC20AE1002BUF							
	T L C C 2 0 A E 1 0 0 2 B U F							
T L	СС	T 1	6 A 0	1 K 1	0 0	3 K U F		
GLOBAL MODEL (4 or 5 digits)	TERMINAL COUNT (1)	SCHEMATICS (4 or 5 digits)	TCR CHARACTERISTICS	RESISTANCE	TOLERANCE	PACKAGING		
LCC	20	A = Isolated	E = 25 ppm/°C First 3 digits are		B = 0.1 %	TAPE AND REEL		
(Tin lead)		resistors B = Resistor to	H = 50 ppm/°C K = 100 ppm/°C	significant figures and the last digit	D = 0.5 % F = 1 %	T0 = 100 min., 100 mult T1 = 1000 min., 1000 mult		
LCCT	20	common bus	M = 300 ppm/°C	specifies the	G = 2 %	T3 = 300 min., 300 mult		
(Lead				number of zeros to	J = 5 %	T5 = 500 min., 500 mult		
(Pb)-free) (e1)				follow.	K = 10 % S = Special	TF = Full reel 2000 TS = 100 min., 1 mult		
TLCC	16	A01 = Resistor to		Example: $10R0 = 10 \Omega$		UF = TUBED		
(Tin lead)	20 24	common		$10R0 = 10 \Omega$ $12R5 = 12.5 \Omega$		OF = TOBED		
TLCCT	24	bus A03 = Isolated		$1000 = 100 \Omega$				
(Lead	16	parallel		$1001 = 1000 \Omega$				
(Pb)-free)	20	resistor						
(e1)	24	A06 = Isolated adjacent						
		resistor						
Historical Part Number example: LC20BK1003J (for reference purposes only)								
LC		20	В	К	1003	J		
SERIES		PINS	SCHEMATIC	TCR CHARACTERISTIC	RESISTANCE	E TOLERANCE		

Note

⁽¹⁾ LCC or LCCT only available in 20 pin size.



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Vishay

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