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Vishay Siliconix

# Automotive N-Channel 40 V (D-S) 175 °C MOSFET



Marking code: Q048

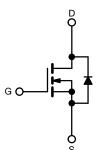
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0045			
I <sub>D</sub> (A)	110			
Configuration	Single			
Package	PowerPAK 1212-8SLW			

#### **FEATURES**

- TrenchFET® Gen IV power MOSFET
- AEC-Q101 qualified
- 100 % Rq and UIS tested
- Wettable flank terminals
- Low thermal resistance with 0.75 mm profile
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>



COMPLIANT HALOGEN FREE



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	40	V	
Gate-source voltage		V <sub>GS</sub>	± 20	V	
Continuous drain current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	110		
	T <sub>C</sub> = 125 °C		63		
Continuous source current (diode conduction)		I <sub>S</sub>	103	А	
Pulsed drain current <sup>a</sup>		I <sub>DM</sub>	271		
Single pulse avalanche current	L = 0.1 mH	I <sub>AS</sub>	23		
Single pulse avalanche energy	L = 0.1 IIII	E <sub>AS</sub>	26	mJ	
Maximum power dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	P <sub>D</sub>	113	W	
	T <sub>C</sub> = 125 °C		37	l vv	
Operating junction and storage temperature range Soldering recommendations (peak temperature) c		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	
			260		

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount b	$R_{thJA}$	81	°C/W	
Junction-to-case (drain)		R <sub>thJC</sub>	1.32		

#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %
- b. When mounted on 1" square PCB (FR4 material)
- c. See solder profile (<a href="www.vishay.com/doc?73257">www.vishay.com/doc?73257</a>). A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection



# Vishay Siliconix

PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static			<u> </u>				
Drain-source breakdown voltage	$V_{DS}$	$V_{GS} = 0$ , $I_D = 250 \mu A$		40	-	-	V
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		2.3	2.8	3.3	V
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = 40 V	-	-	1	μΑ
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 40 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 40 V, T <sub>J</sub> = 175 °C	-	-	150	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 \text{ V}$	20	-	-	Α
Drain-source on-state resistance a		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A	-	0.0034	0.0045	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A, T <sub>J</sub> = 125 °C	-	-	0.0076	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C	-	-	0.0095	
Forward transconductance b	9 <sub>fs</sub>	$V_{DS}$	= 15 V, I <sub>D</sub> = 10 A	-	60	-	S
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>		V <sub>DS</sub> = 25 V, f = 1 MHz	-	1394	1952	pF
Output capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V		-	474	664	
Reverse transfer capacitance	C <sub>rss</sub>			-	32	45	
Total gate charge <sup>c</sup>	Qg		/ V <sub>DS</sub> = 20 V, I <sub>D</sub> = 3 A	-	23	35	nC
Gate-source charge c	$Q_{gs}$	V <sub>GS</sub> = 10 V		-	7	-	
Gate-drain charge <sup>c</sup>	$Q_{gd}$			-	5	-	
Gate resistance	$R_g$	f = 1 MHz		1.1	2.2	3.3	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>	$V_{DD} = 20 \text{ V}, \text{ R}_L = 6.67 \Omega$ $I_D \cong 3 \text{ A}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega$		-	15	23	
Rise time <sup>c</sup>	t <sub>r</sub>			-	6	9	
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>			-	23	35	ns
Fall time <sup>c</sup>	t <sub>f</sub>			-	7	11	
Source-Drain Diode Ratings and Charac	teristic <sup>b</sup>		<u> </u>				
Pulsed current <sup>a</sup>	I <sub>SM</sub>			-	-	271	Α
Forward voltage	V <sub>SD</sub>	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0 V		-	0.82	1.1	V
Body diode reverse recovery time	t <sub>rr</sub>	$V_{DD}=32$ V, $I_{FM}=3$ A, di/dt = 100 A/μs, R = 10 $\Omega$ , L = 0.3 mH, pulse width = 2 μs		-	29	58	ns
Body diode reverse recovery charge	$Q_{rr}$			-	21	42	nC
Reverse recovery fall time	ta			-	14	-	
Reverse recovery rise time	t <sub>b</sub>			-	15	-	ns
Body diode peak reverse recovery current	I <sub>RM(REC)</sub>			-	-1.3	-	Α

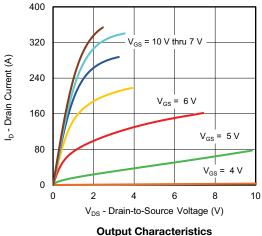
#### Notes

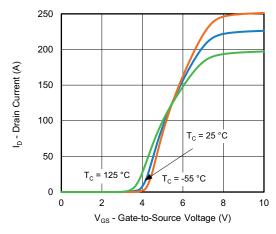
- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

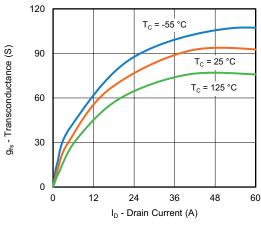


## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

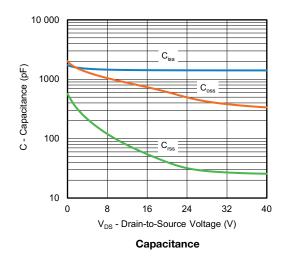


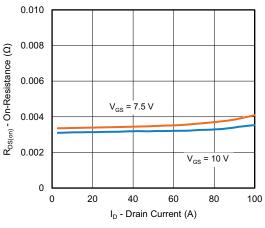


**Transfer Characteristics** 

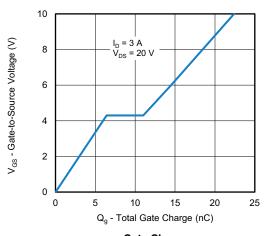


**Transconductance** 





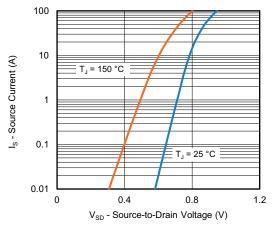
On-Resistance vs. Drain Current



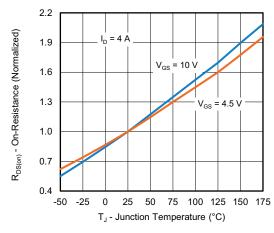
**Gate Charge** 



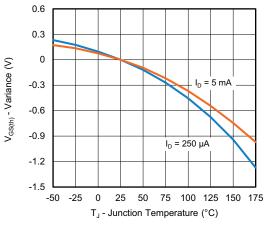
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



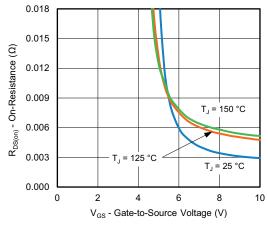
**Source Drain Diode Forward Voltage** 



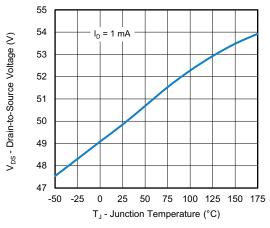
On-Resistance vs. Junction Temperature



**Threshold Voltage** 



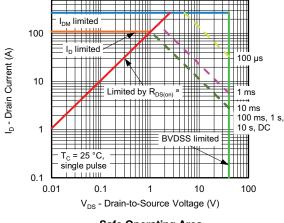
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



## **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



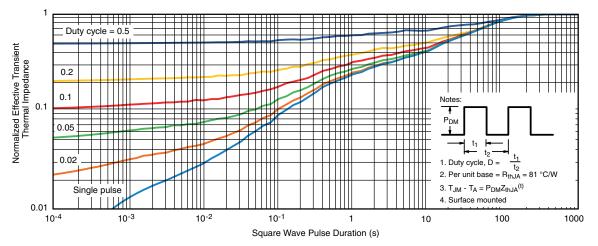
#### Safe Operating Area

#### Note

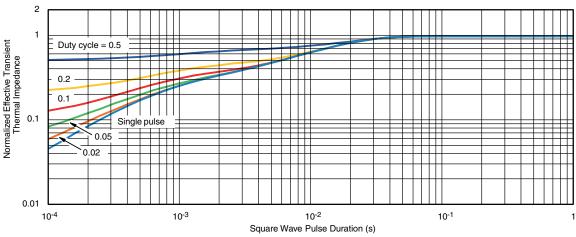
a.  $V_{GS} > \mbox{minimum} \ V_{GS}$  at which  $R_{DS(on)}$  is specified



## THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Case (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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## RECOMMENDED MINIMUM PADS FOR PowerPAK® 1212-8 Single



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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