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Vishay Siliconix

Automotive N-Channel 20 V (D-S) 175 °C MOSFET

SOT-23 (TO-236)

Top View

Marking Code: 9PYXX

$\begin{array}{c|ccccc} \textbf{PRODUCT SUMMARY} \\ V_{DS} \ (V) & 20 \\ R_{DS(on)} \ (\Omega) \ \text{at } V_{GS} = 4.5 \ V & 0.030 \\ R_{DS(on)} \ (\Omega) \ \text{at } V_{GS} = 2.5 \ V & 0.034 \\ R_{DS(on)} \ (\Omega) \ \text{at } V_{GS} = 1.8 \ V & 0.038 \\ I_D \ (A) & 6 \\ Configuration & Single \\ \end{array}$

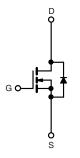
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



N-Channel MOSFET

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	SQ2310CES (for detailed order number please see www.vishay.com/doc?79771)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	20	V
Gate-source voltage		V_{GS}	± 8	V
Continuous drain current	T _C = 25 °C	1	6	
	T _C = 125 °C	I _D	3.5	
Continuous source current (diode conduction)		I _S	2.5	А
Pulsed drain current ^a		I _{DM}	24	
Single pulse avalanche current	L = 0.1 mH	I _{AS}	10	
Single pulse avalanche energy	L = U. I MIH	E _{AS}	5	mJ
Maximum power dissipation	T _C = 25 °C	D	2	W
	T _C = 125 °C	P_{D}	0.6	VV
Operating junction and storage temperature range		T _J , T _{stq}	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount b	R _{thJA}	175	°C/W	
Junction-to-foot (drain)		R_{thJF}	75	- C/VV	

Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. When mounted on 1" square PCB (FR-4 material)



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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				l		•	·	
Drain-source breakdown voltage	V _{DS}	V _{GS} =	= 0 V, I _D = 250 μA	20	-	-	.,	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	· V _{GS} , I _D = 250 μA	0.4	0.6	1	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$		-	-	± 100	nA	
		V _{GS} = 0 V	V _{DS} = 20 V	-	-	1		
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 20 V, T _J = 125 °C	-	-	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 20 V, T _J = 175 °C	-	-	150	1	
On-state drain current a	I _{D(on)}	V _{GS} = 4.5 V	V _{DS} ≥ 5 V	10	-	-	Α	
		V _{GS} = 4.5 V	I _D = 5 A	-	0.022	0.030		
		V _{GS} = 4.5 V	I _D = 5 A, T _J = 125 °C	-	-	0.043	1	
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = 4.5V$	I _D = 5 A, T _J = 175 °C	-	-	0.051	Ω	
	. ,	V _{GS} = 2.5 V	I _D = 4 A	-	0.025	0.034	V nA μA A Ω S pF nC Ω ns nc ns	1
		V _{GS} = 1.8 V	I _D = 3.6 A	-	0.029	0.038		
Forward transconductance b	9 _{fs}	V _{DS}	= 15 V, I _D = 4 A	-	25	-	S	
Dynamic ^b				l		•		
Input capacitance	C _{iss}			-	426	590		
Output capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = 10 V, f = 1 MHz	-	106	140	pF	
Reverse transfer capacitance	C_{rss}	1		-	52	70		
Total gate charge ^c	Qg			-	4.95	8.5		
Gate-source charge ^c	Q _{gs}	$V_{GS} = 4.5 \text{ V}$	$V_{DS} = 10 \text{ V}, I_{D} = 5 \text{ A}$	-	0.65	-	nC	
Gate-drain charge ^c	Q _{gd}	1		-	0.93	-		
Gate resistance	R_g		f = 1 MHz	4	9.4	16	Ω	
Turn-on delay time ^c	t _{d(on)}			-	6	11		
Rise time ^c	t _r	V _{DD} =	= 10 V, $R_L = 2.5 \Omega$	=.	19	30		
Turn-off delay time ^c	t _{d(off)}	$I_D \cong 4 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		-	29	45	115	
Fall time ^c	t _f	1		-	6	10		
Source-Drain Diode Ratings and Character	eristics ^b							
Pulsed current ^a	I _{SM}			-	-	24	Α	
Forward voltage	V _{SD}	I _F =	2.8 A, V _{GS} = 0 V		0.70	1.2	V	
Body diode reverse recovery time	t _{rr}			-	7	14	ns	
Body diode reverse recovery charge	Qrr	1 - 4 4	λ, di/dt = 100 A / μs	-	2	4	nC	
Reverse recovery fall time	ta] I _F = 4 P	a, αί/αι = 100 A / μs	-	7	-		
Reverse recovery rise time	t _b	1		-	3	-	ns	
Body diode peak reserve recovery current	I _{RM(REC)}			-	-0.6	-	Α	

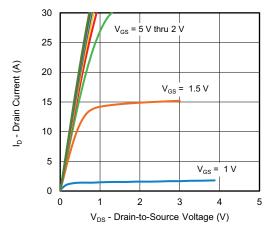
Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

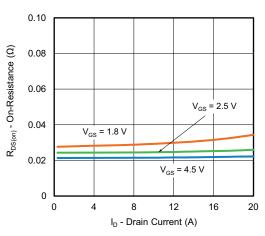
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



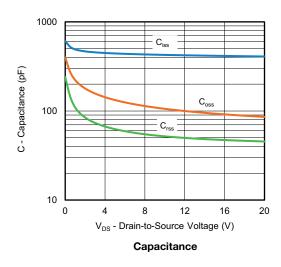
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

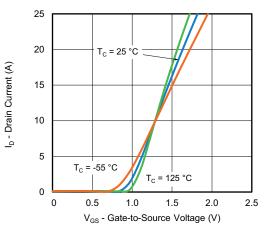


Output Characteristics

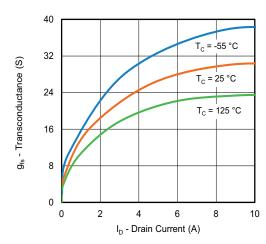


On-Resistance vs. Drain Current

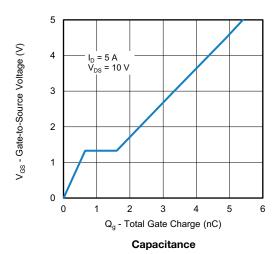




Transfer Characteristics

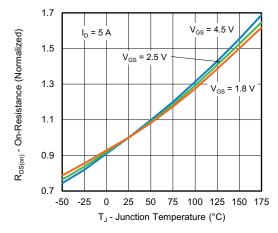


Transconductance

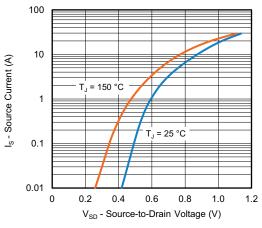




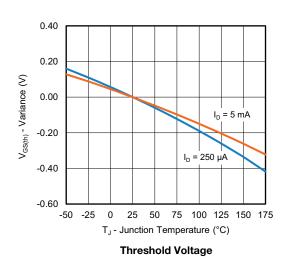
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

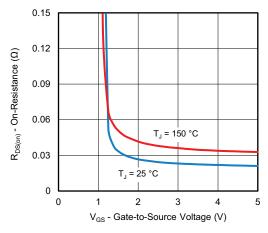


On-Resistance vs. Junction Temperature

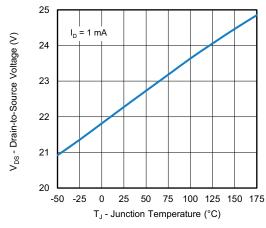


Source Drain Diode Forward Voltage

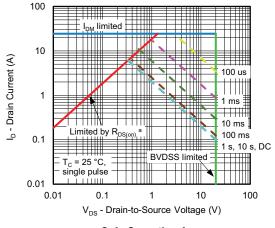




On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



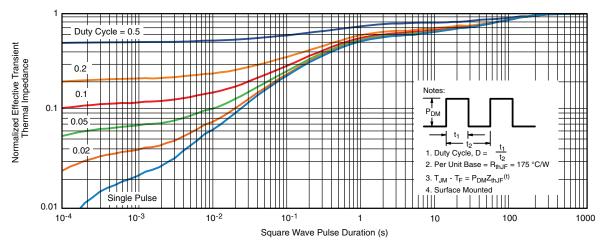
Safe Operating Area

Note

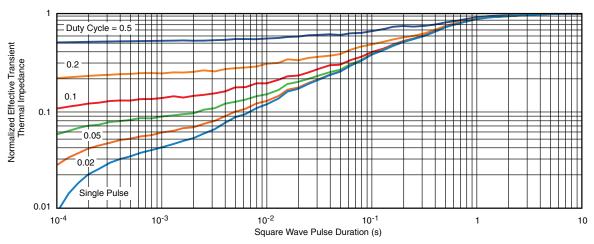
a. V_{GS} > minimum V_{GS} at which $R_{DS(ON)}$ is specified



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62145.



SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.64 Ref 0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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