

## Vishay General Semiconductor

# Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low  $V_F = 0.34 \text{ V}$  at  $I_F = 2.5 \text{ A}$ 



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 5.0 A				
$V_{RRM}$	45 V				
I <sub>FSM</sub>	100 A				
$V_F$ at $I_F = 5.0 \text{ A}$	0.41 V				
T <sub>OP</sub> max. (AC mode)	150 °C				
T <sub>J</sub> max. (DC forward current)	200 °C				
Package	ITO-220AB				
Circuit configuration	Common cathode				

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses

ROHS COMPLIANT

FREE

High efficiency operation

Solder dip 275 °C max. 10 s, per JESD 22-B106

T<sub>.1</sub> 200 °C max. in solar bypass mode application

Material categorization: for definitions of com-

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### **TYPICAL APPLICATIONS**

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

## **MECHANICAL DATA**

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VFT1045CBP	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	45	V	
Maximum average forward rectified current (fig. 1)	per device	1 (1)	10	^	
	per diode	I <sub>F(AV)</sub> <sup>(1)</sup>	5.0	A	
Peak forward surge current 8.3 ms single half sine-wasuperimposed on rated load per diode	I <sub>FSM</sub>	100	А		
Isolation voltage from terminal to heatsink, t = 1 min	V <sub>AC</sub>	1500	V		
Operating junction and storage temperature range (AC mode)		T <sub>OP</sub> , T <sub>STG</sub>	-40 to +150	°C	
Junction temperature in DC forward current without reverse bias, $t \le 1\ h$		T <sub>J</sub> <sup>(2)</sup>	≤ 200	°C	

#### Notes

(1) With heatsink

(2) Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 2.5 A$	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.44	-	V	
	I <sub>F</sub> = 5.0 A			0.49	0.58		
	I <sub>F</sub> = 2.5 A	T <sub>A</sub> = 125 °C		0.34	=		
	I <sub>F</sub> = 5.0 A			0.41	0.50		
Reverse current per diode	$V_R = 45 \text{ V}$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	1 (2)	=	500	μΑ		
		T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	5	15	mA	

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	VFT1045CBP	UNIT		
Typical thermal resistance	per diode	$R_{ hetaJC}$	6.5	°C/W	
	per device		5.0		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	VFT1045CBP-M3/4W	1.75	4W	50/tube	Tube	

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

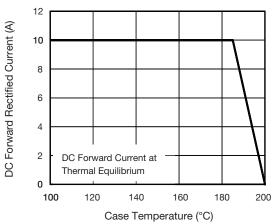


Fig. 1 - Maximum Forward Current Derating Curve

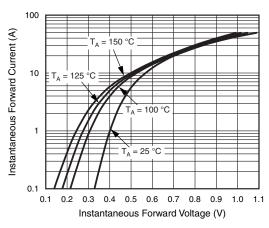


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

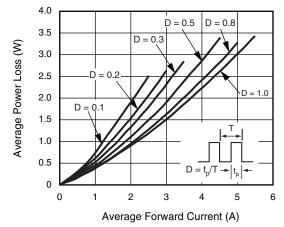


Fig. 2 - Forward Power Loss Characteristics Per Diode

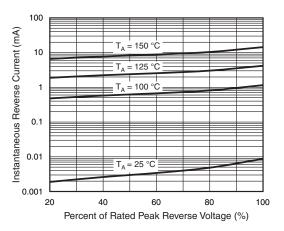


Fig. 4 - Typical Reverse Characteristics Per Diode



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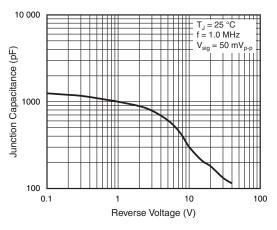


Fig. 5 - Typical Junction Capacitance Per Diode

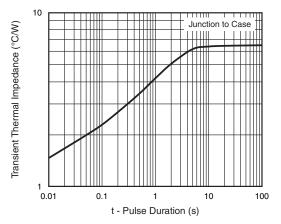
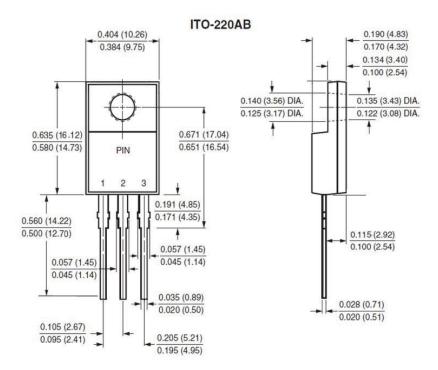


Fig. 6 - Typical Transient Thermal Impedance Per Diode

## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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