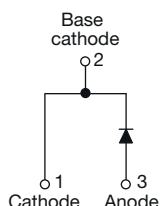


# High Voltage, Input Rectifier Diode, 10 A


**TO-220AC 2L**


## FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature
- Glass passivated pellet chip junction
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

## APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

## DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

## PRIMARY CHARACTERISTICS

$I_{F(AV)}$	10 A
$V_R$	800 V to 1200 V
$V_F$ at $I_F$	1.1 V
$I_{FSM}$	160 A
$T_J$ max.	150 °C
Package	TO-220AC 2L
Circuit configuration	Single

## OUTPUT CURRENT IN TYPICAL APPLICATIONS

APPLICATIONS	SINGLE-PHASE BRIDGE	THREE-PHASE BRIDGE	UNITS
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	12.0	16.0	A

## MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Sinusoidal waveform	10	A
$V_{RRM}$		800/1200	V
$I_{FSM}$		160	A
$V_F$	10 A, $T_J = 25$ °C	1.1	V
$T_J$		-40 to +150	°C

## VOLTAGE RATINGS

PART NUMBER	$V_{RRM}$ , MAXIMUM PEAK REVERSE VOLTAGE V	$V_{RSM}$ , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	$I_{RRM}$ AT 150 °C mA
VS-10ETS08-M3	800	900	0.5
VS-10ETS12-M3	1200	1300	

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$	$T_C = 105$ °C, 180° conduction half sine wave	10	A
Maximum peak one cycle non-repetitive surge current	$I_{FSM}$	10 ms sine pulse, rated $V_{RRM}$ applied	135	
		10 ms sine pulse, no voltage reapplied	160	
Maximum $I^2t$ for fusing	$I^2t$	10 ms sine pulse, rated $V_{RRM}$ applied	91	A <sup>2</sup> s
		10 ms sine pulse, no voltage reapplied	130	
Maximum $I^2\sqrt{t}$ for fusing	$I^2\sqrt{t}$	$t = 0.1$ ms to 10 ms, no voltage reapplied	1300	A <sup>2</sup> √s



## ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum forward voltage drop	$V_{FM}$	10 A, $T_J = 25\text{ }^{\circ}\text{C}$	1.1	V
Forward slope resistance	$r_t$	$T_J = 150\text{ }^{\circ}\text{C}$	20	$\text{m}\Omega$
Threshold voltage	$V_{F(TO)}$		0.82	V
Maximum reverse leakage current	$I_{RM}$	$T_J = 25\text{ }^{\circ}\text{C}$	0.05	mA
		$T_J = 150\text{ }^{\circ}\text{C}$	0.50	

## THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	$T_J, T_{Stg}$		-40 to +150	$^{\circ}\text{C}$
Maximum thermal resistance, junction to case	$R_{thJC}$	DC operation	2.5	$^{\circ}\text{C}/\text{W}$
Maximum thermal resistance, junction to ambient (PCB mount)	$R_{thJA}$		62	
Soldering temperature	$T_S$		240	$^{\circ}\text{C}$
Approximate weight			2	g
			0.07	oz.
Marking device		Case style TO-220AC 2L	10ETS08	
			10ETS12	

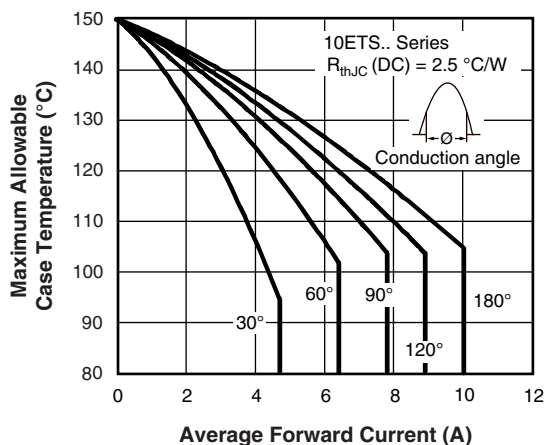


Fig. 1 - Current Rating Characteristics

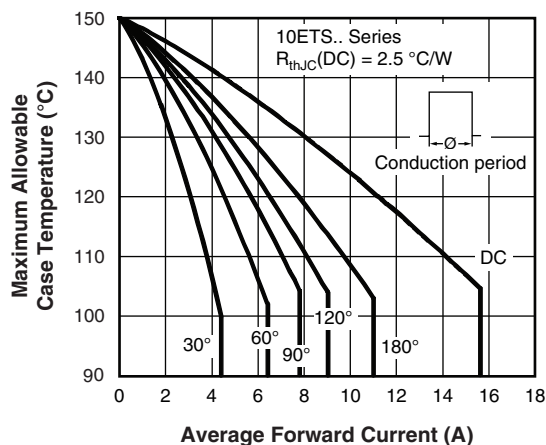


Fig. 2 - Current Rating Characteristics

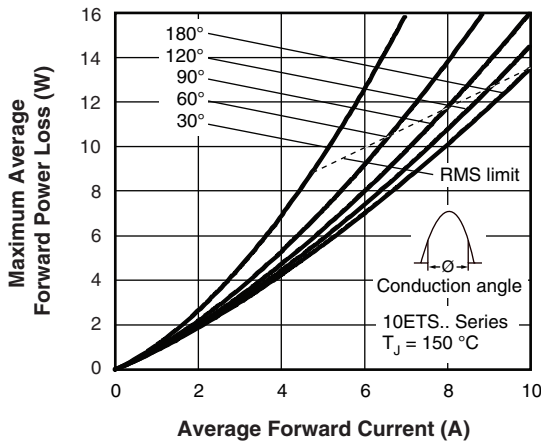


Fig. 3 - Forward Power Loss Characteristics

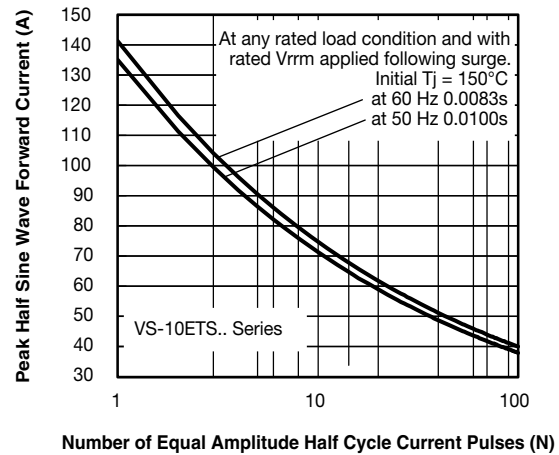


Fig. 5 - Maximum Non-Repetitive Surge Current

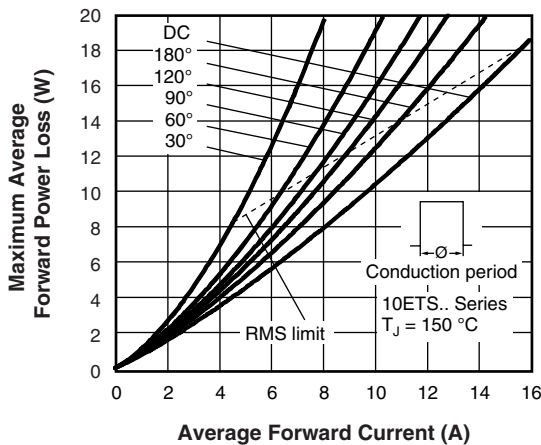


Fig. 4 - Forward Power Loss Characteristics

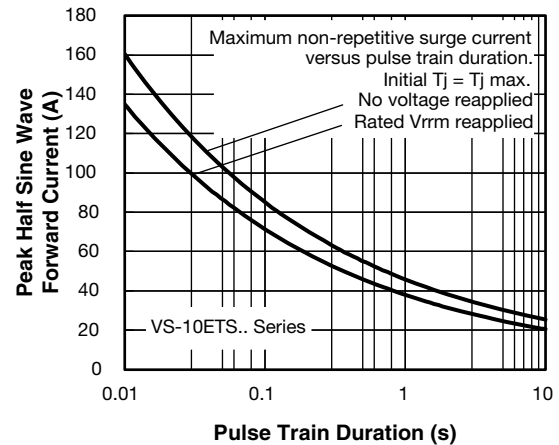


Fig. 6 - Maximum Non-Repetitive Surge Current

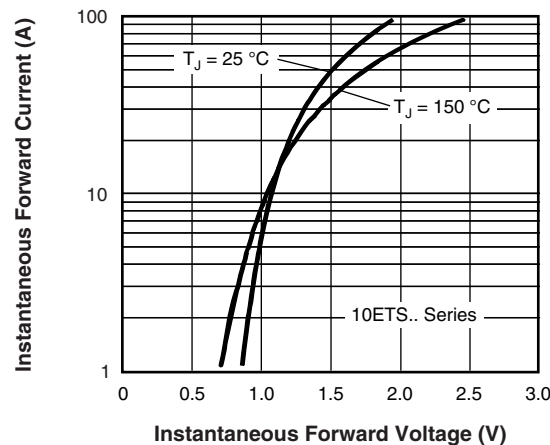


Fig. 7 - Forward Voltage Drop Characteristics

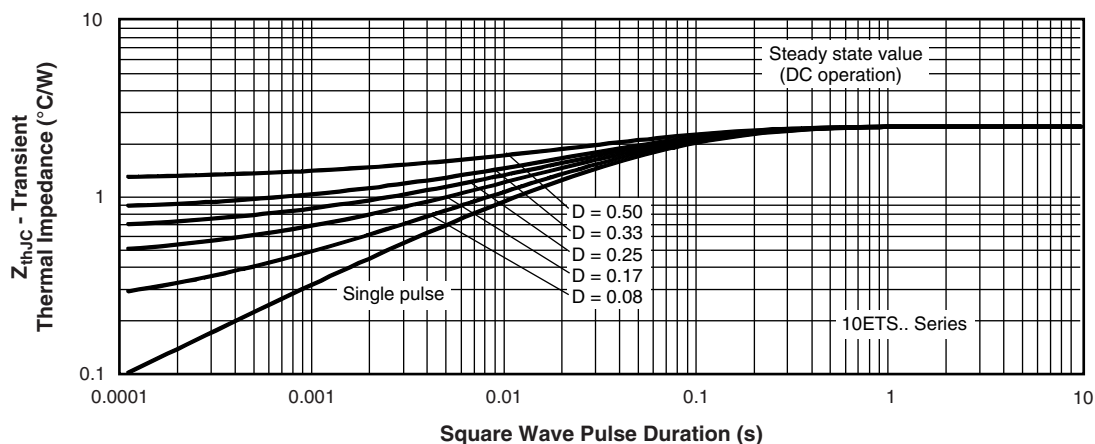


Fig. 8 - Thermal Impedance  $Z_{th,JC}$  Characteristics

## ORDERING INFORMATION TABLE

**Device code**

<b>VS-</b>	<b>10</b>	<b>E</b>	<b>T</b>	<b>S</b>	<b>12</b>	<b>-M3</b>
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1 2 3 4 5 6 7

- 1 - Vishay Semiconductors product
- 2 - Current rating (10 = 10 A)
- 3 - Circuit configuration:  
E = single
- 4 - Package:  
T = 2L TO-220AC
- 5 - Type of silicon:  
S = standard recovery rectifier
- 6 - Voltage code  $\times 100 = V_{RRM}$ 

08 = 800 V

12 = 1200 V
- 7 - Environmental digit:  
-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)		
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-10ETS08-M3	50	Antistatic plastic tubes
VS-10ETS12-M3	50	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?96156">www.vishay.com/doc?96156</a>
Part marking information	<a href="http://www.vishay.com/doc?95391">www.vishay.com/doc?95391</a>





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