

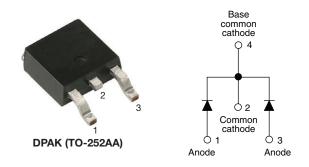
RoHS

COMPLIANT

HALOGEN

FREE

High Performance Schottky Rectifier, 2 x 6 A



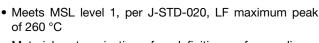
PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 6 A			
V _R	100 V			
V _F at I _F	0.65 V			
I _{RM}	4 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	6 mJ			
Circuit configuration	Common cathode			
Package	DPAK (TO-252AA)			

FEATURES

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability



- · Center tap configuration
- · Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- of 260 °C
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



DESCRIPTION

The VS-12CWQ10FNHM3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	12	A		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	330	A		
V _F	6 A _{pk} , T _J = 125 °C (per leg)	0.65	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-12CWQ10FNHM3	UNITS		
Maximum DC reverse voltage	V_R	100	V		
Maximum working peak reverse voltage	V_{RWM}	100	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	per leg		50 % duty cycle at T _C = 135 °C, rectangular waveform		6	А	
See fig. 5	per device	I _{F(AV)}			12		
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated	330		
non-repetitive surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse load condition and with rated V _{RRM} applied		110	Α	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6	mJ	
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α	



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
		6 A	T _{.1} = 25 °C	0.80	· V	
Maximum forward	V (1)	12 A	- IJ=25 C	0.95		
voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	6 A	T _J = 125 °C	0.65		
		12 A	1J = 125 C	0.78		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	•		mA	
See fig. 2	'RM ` '	T _J = 125 °C	V _R = Rated V _R	4	I IIIA	
Threshold voltage	V _{F(TO)}			0.47	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum	20.68	mΩ		
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} , (test signal ra	183	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5	5.0	nH		

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance,	per leg	В	DC operation	3.0	°C/W	
junction to case	per device	R_{thJC}	See fig. 4	1.5	C/ VV	
Approximate weight				0.3	g	
Approximate weight				0.01	oz.	
Marking device			Case style DPAK (TO-252AA)	12CWQ	10FNH	

Note

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



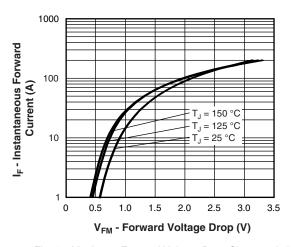


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

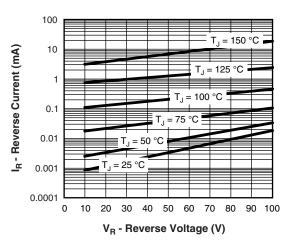


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

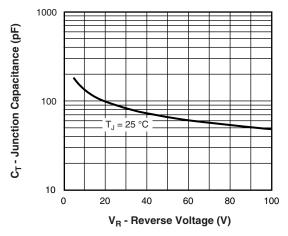


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

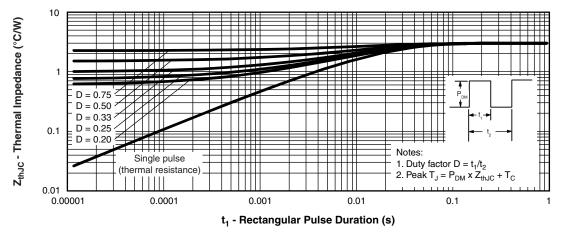


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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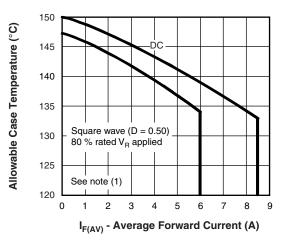


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

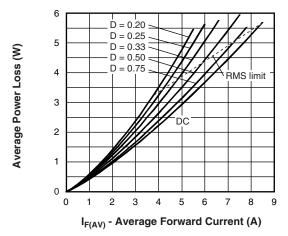


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

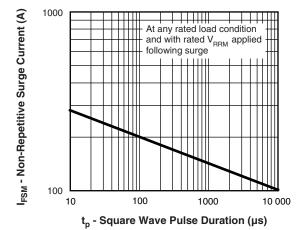


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

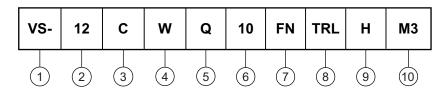
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Current rating (12 A)

Center tap configuration

Package identifier:

W = DPAK

5 - Schottky "Q" series

6 - Voltage rating (10 = 100 V)

7 - FN = TO-252AA

8 - • None = Tube

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

9 - H = AEC-Q101 qualified

10 - Environmental digit:

M3 = Halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-12CWQ10FNHM3	75	3000	Antistatic plastic tube			
VS-12CWQ10FNTRHM3	2000	2000	13" diameter reel			
VS-12CWQ10FNTRRHM3	3000	3000	13" diameter reel			
VS-12CWQ10FNTRLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95519</u>				
Part marking information	www.vishay.com/doc?95518			
Packaging information	www.vishay.com/doc?95033			



DPAK (TO-252AA)



SYMBOL	MILLIN	IETERS	INC	NOTES	
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	1	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	4.93	-	0.194	-	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIMETERS		INC	INCHES		
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
е	2.29	BSC	0.090	BSC		
Н	9.40	10.41	0.370	0.410		
L	1.40	1.78	0.055	0.070		
L1	2.74 BSC		0.108	REF.		
L2	0.51 BSC		0.020	BSC		
L3	0.89	1.27	0.035	0.050	3	
L4	-	1.02	-	0.040		
L5	1.14	1.52	0.045	0.060	2	
	•	•		•		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (5) Outline conforms to JEDEC® outline TO-252AA, except for D1 dimension



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Vishay

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