RoHS

COMPLIANT

HALOGEN FREE



Vishay Siliconix



## **Load Switch with Level-Shift**



### Marking code: P

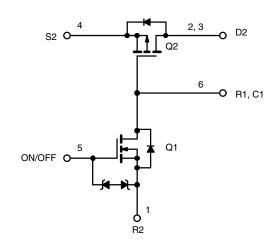
PRODUCT SUMMARY						
V <sub>DS2</sub> (V)	8					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 4.5 \text{ V}$	0.625					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 2.5 \text{ V}$	0.890					
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 1.8 \text{ V}$	1.250					
I <sub>D</sub> (A)	± 0.43					
Configuration	Level-Shift					

#### **DESCRIPTION**

The Si1040X includes a p- and n-channel MOSFET in a single SC-89-6 package. The low on-resistance p-channel TrenchFET is tailored for use as a load switch. The n-channel, with an external resistor, can be used as a level-shift to drive the p-channel load-switch. The n-channel MOSFET has internal ESD protection and can be driven by logic signals as low as 1.5 V. The Si1040X operates on supply lines from 1.8 V to 8 V, and can drive loads up to 0.43 A.

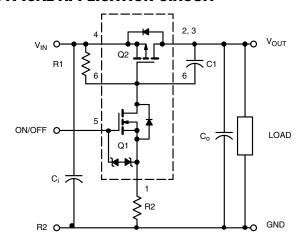
#### **FEATURES**

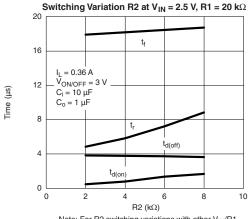
- TrenchFET® power MOSFET
- 1.8 V to 8 V input
- 1.5 V to 8 V logic level control
- Smallest LITTLE FOOT® package: 1.6 mm x 1.6 mm
- 2000 V ESD protection on input switch, V<sub>ON/OFF</sub>
- Adjustable slew-rate
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



ORDERING INFORMATION			
Package	SC-89		
Lead (Pb)-free and halogen-free	Si1040X-T1-GE3		

#### TYPICAL APPLICATION CIRCUIT





Note: For R2 switching variations with other  $V_{IN}/R1$ combinations See Typical Characteristics



#### www.vishay.com

# Vishay Siliconix

COMPONENTS					
R1	Pull-up resistor	Typical 10 k $\Omega$ to 1 m $\Omega$ <sup>a</sup>			
R2	Optional slew-rate control	Typical 0 to 100 kΩ <sup>a</sup>			
C1	Optional slew-rate control	Typical 1000 pF			

#### Note

a. Minimum R1 value should be at least 10 x R2 to ensure Q1 turn-on

The Si1040X is ideally suited for high side load switching in portable applications. The integrated n-channel level-shift device saves space by reducing external components. The slew rate is set externally so that rise-times can be tailored to different load types.

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Input voltage		V <sub>IN</sub>	8	V
ON/OFF voltage	V <sub>ON/OFF</sub>	8	V	
Load current (	Continuous <sup>a, b</sup>	IL	± 0.43	А
Load current	Pulsed b, c		± 1	
Continuous intrinsic diode conduction <sup>a</sup>	I <sub>S</sub>	-0.15		
Maximum power dissipation <sup>a</sup>		P <sub>D</sub>	0.174	W
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
ESD rating, MIL-STD-883D human body model (100 pF, 1500 $\Omega$ )		ESD	2	kV

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient (continuous current) a	R <sub>thJA</sub>	600	720	°C/W		
Maximum junction-to-foot (Q2)	R <sub>thJC</sub>	450	540	C/VV		

#### Note

a. Surface mounted on 1" x 1" FR4 board

SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
OFF Characteristics							
Reverse leakage current	I <sub>FL</sub>	$V_{IN} = 8 \text{ V}, V_{ON/OFF} = 0 \text{ V}$	-	-	1	μA	
Diode forward voltage	$V_{SD}$	$I_{S} = -0.15 \text{ A}$	-	0.85	1.2	V	
ON Characteristics							
Input voltage range	$V_{IN}$		1.8	-	8	V	
		$V_{ON/OFF} = 1.5 \text{ V}, V_{IN} = 4.5 \text{ V}, I_D = 0.43 \text{ A}$	-	0.500	0.625		
On-resistance (p-channel) at 1 A	R <sub>DS(on)</sub>	$V_{ON/OFF} = 1.5 \text{ V}, V_{IN} = 2.5 \text{ V}, I_D = 0.36 \text{ A}$	-	0.710	0.890	Ω	
		$V_{ON/OFF} = 1.5 \text{ V}, V_{IN} = 1.8 \text{ V}, I_D = 0.3 \text{ A}$	-	1	1.25		
On-state (p-channel) drain current	I <sub>D(on)</sub>	$V_{IN-OUT} \le 0.2 \text{ V}, V_{IN} = 5 \text{ V}, V_{ON/OFF} = 1.5 \text{ V}$	1	-	-	Α	
		$V_{IN-OUT} \le 0.3 \text{ V}, V_{IN} = 3 \text{ V}, V_{ON/OFF} = 1.5 \text{ V}$	0.8	-	-	_ ^	

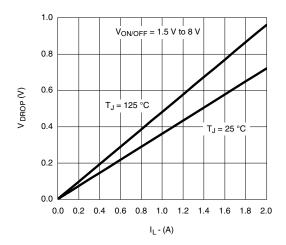
### Notes

- a. Surface mounted on FR4 board
- b.  $V_{IN} = 8 \text{ V}, V_{ON/OFF} = 8 \text{ V}, T_A = 25 \,^{\circ}\text{C}$
- c. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%$

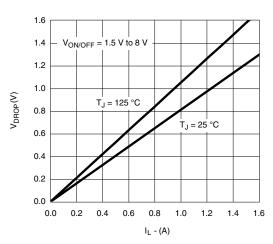
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



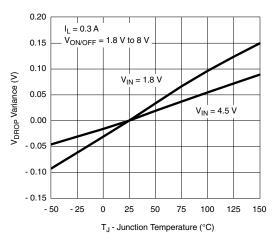
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



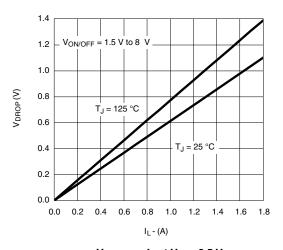
 $V_{DROP}$  vs.  $I_L$  at  $V_{IN}$  = 4.5 V



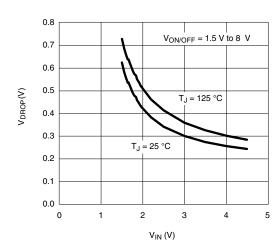
 $V_{DROP}$  vs.  $I_L$  at  $V_{IN}$  = 1.8 V



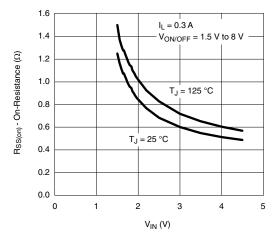
V<sub>DROP</sub> Variance vs. Junction Temperature



 $V_{DROP}$  vs.  $I_L$  at  $V_{IN}$  = 2.5 V



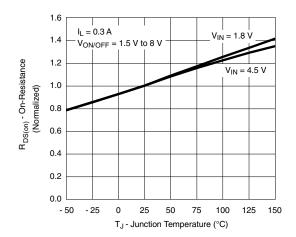
 $V_{DROP}$  vs.  $I_L$  at  $V_{IN}$  = 0.5 V



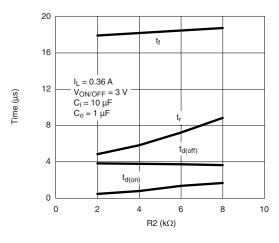
On-Resistance vs. Input Voltage



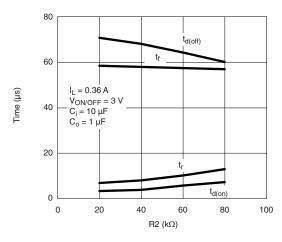
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



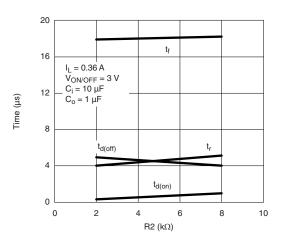
Normalized On-Resistance vs. Junction Temperature



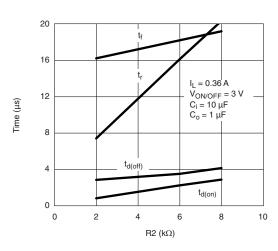
Switching Variation R2 at V<sub>IN</sub> = 2.5 V, R1 = 20 k $\Omega$ 



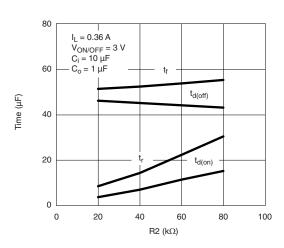
Switching Variation R2 at V<sub>IN</sub> = 4.5 V, R1 = 300 k $\Omega$ 



Switching Variation R2 at V<sub>IN</sub> = 4.5 V, R1 = 20 k $\Omega$ 



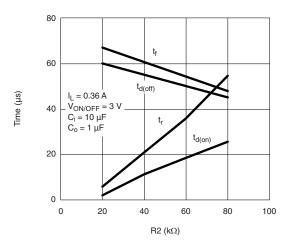
Switching Variation R2 at  $V_{IN}$  = 1.8 V, R1 = 20 k $\Omega$ 



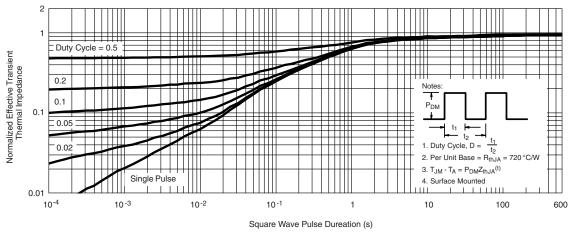
Switching Variation R2 at  $V_{IN}$  = 2.5 V, R1 = 300 k $\Omega$ 



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Switching Variation R2 at V<sub>IN</sub> = 1.8 V, R1 = 300 k $\Omega$ 

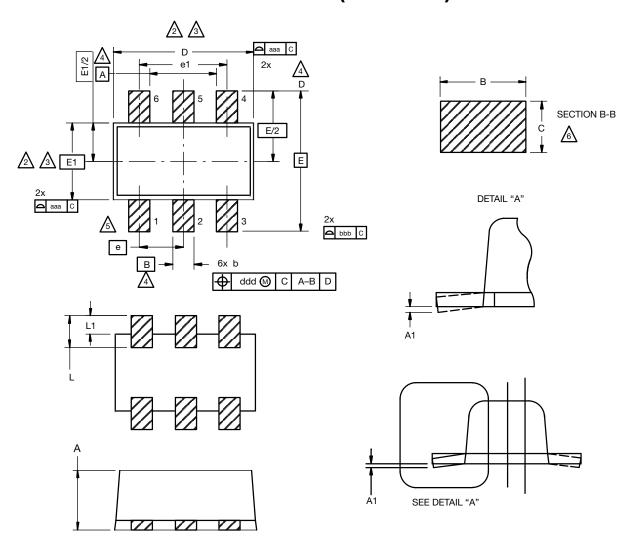


Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?71809">www.vishay.com/ppg?71809</a>.



# **SC-89 6-Leads (SOT-563F)**



#### Notes

1. Dimensions in millimeters.

Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.

Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.

ADatums A, B and D to be determined 0.10 mm from the lead tip.

 $\Delta$ Terminal numbers are shown for reference only.

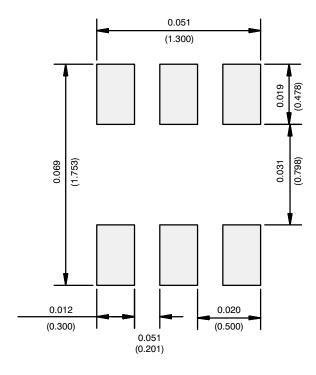
These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIM.	MILLIMETERS					
	MIN.	NOM.	MAX.			
Α	0.56	0.58	0.60			
A1	0	0.02	0.10			
b	0.15	0.22	0.30			
С	0.10	0.14	0.18			
D	1.50	1.60	1.70			
E	1.50	1.60	1.70			
E1	1.15	1.20	1.25			
е	0.45	0.50	0.55			
e1	0.95	1.00	1.05			
L	0.25	0.35	0.50			
L1	0.10	0.20	0.30			
C14-0439-Rev. C, 11-Aug-14 DWG: 5880						

Revision: 11-Aug-14 1 Document Number: 71612



## **RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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