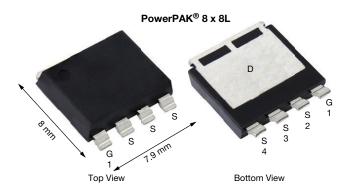


Vishay Siliconix

AUTOMOTIVE GRADE

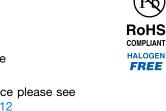
Automotive N-Channel 30 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	30			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.00052			
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5 \text{ V}$	0.0007			
I _D (A)	445			
Configuration	Single			
Package	PowerPAK 8 x 8L			

FEATURES

- TrenchFET® Gen IV power MOSFET
- AEC-Q101 qualified
- 100 % R_q and UIS tested
- Thin 1.6 mm package
- · Very low thermal resistance
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



G S S

N-Channel MOSFET

ORDERING INFORMATION	
Package	PowerPAK 8 x 8L
Lead (Pb)-free and halogen-free	SQJQ130EL (for detailed order number please see www.vishay.com/doc?79776)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage	Prain-source voltage		30	V	
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	- I _D	445		
	T _C = 125 °C		445		
Continuous source current (diode conduction)		I _S	545	Α	
Pulsed drain current ^b		I _{DM}	445		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	86		
Single pulse avalanche energy	L = U.1 IIIII	E _{AS}	374	mJ	
Maximum power dissipation	T _C = 25 °C	В	600	W	
	T _C = 125 °C	P _D	200		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C	
Soldering recommendations (peak temperature) ^d			260		

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount c	R_{thJA}	44	°C/W	
Junction-to-case (drain)		R _{thJC}	0.25	[C/W	

Notes

- a. Package limited
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- c. When mounted on 1" square PCB (FR4 material)
- d. See solder profile (www.vishay.com/doc?73257). The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection



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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static		<u> </u>		l	•	•	
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0$, $I_D = 250 \mu A$		30	-	-	V
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	- V _{GS} , I _D = 250 μA	1.5	2.0	2.5	V
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		V _{GS} = 0 V V _{DS} = 30 V		-	-	1	
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 30 V, T _J = 125 °C	-	-	200	μΑ
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 175 °C	-	-	330	1
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	100	-	-	Α
		V _{GS} = 10 V	I _D = 20 A	-	0.00045	0.00052	Ω
Drain actures on state registeres 3	B	V _{GS} = 4.5 V	I _D = 20 A	-	0.0006	0.0007	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = 10 V	I _D = 20 A, T _J = 125 °C	-	-	0.0008	
		V _{GS} = 10 V	I _D = 20 A, T _J = 175 °C	-	-	0.0009	
Forward transconductance b	9fs	V_{DS}	= 15 V, I _D = 60 A	-	360	-	S
Dynamic ^b							
Input capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	-	16 675	23 345	pF
Output capacitance	Coss	$V_{GS} = 0 V$		-	6850	9560	
Reverse transfer capacitance	C _{rss}			-	715	1000	
Total gate charge c	Q_g		10 V V _{DS} = 15 V, I _D = 30 A	-	310	455	nC
Gate-source charge ^c	Q _{gs}	V _{GS} = 10 V		-	53	-	
Gate-drain charge ^c	Q _{gd}			-	56	-	
Gate resistance	R_g	f = 1 MHz		0.9	1.9	2.9	Ω
Turn-on delay time ^c	t _{d(on)}			-	22	33	
Rise time ^c	t _r	V_{DD} = 15 V, R_L = 0.5 Ω I_D \cong 30 A, V_{GEN} = 10 V, R_g = 1 Ω		-	30	45	ns
Turn-off delay time ^c	t _{d(off)}			-	109	164	
Fall time ^c	t _f			-	57	86	
Source-Drain Diode Ratings and Cha	aracteristics b						
	t _{rr}			-	40	-	
Reverse recovery time	ta		-	44	-	ns	
-	t _b	V _{DD} = 24 V, I _{FM} = 20 A, di/dt = 100 A/μs		-	83		166
Reverse recovery charge	Q _{rr}			-	156	312	nC
Reverse recovery current	I _{RM}	1		-	-	3.4	Α
Pulsed current a	I _{SM}			-	-	1600	Α
		I _F = 50 A, V _{GS} = 0			1		

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

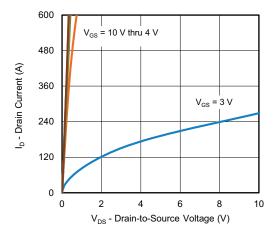


Fig. 1 - Output Characteristics

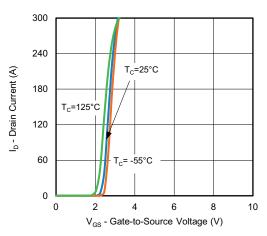


Fig. 2 - Transfer Characteristics

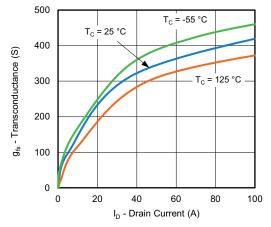


Fig. 3 - Transconductance

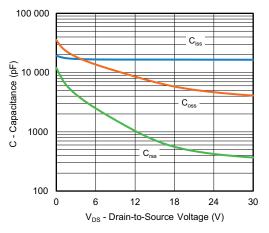


Fig. 4 - Capacitance

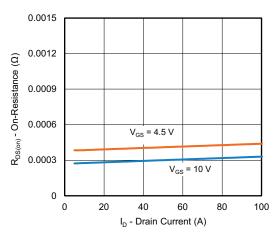


Fig. 5 - On-Resistance vs. Drain Current

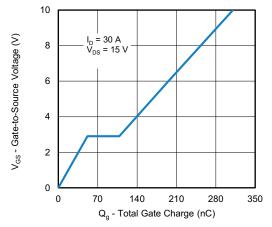


Fig. 6 - Gate Charge



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

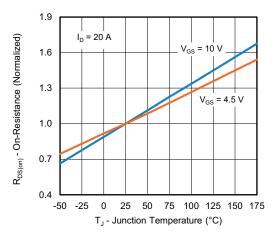


Fig. 7 - On-Resistance vs. Junction Temperature

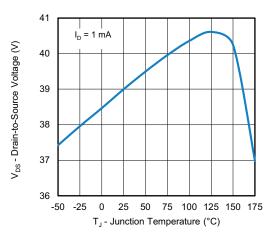


Fig. 8 - Drain Source Breakdown vs. Junction Temperature

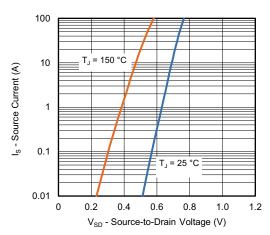


Fig. 9 - Source Drain Diode Forward Voltage

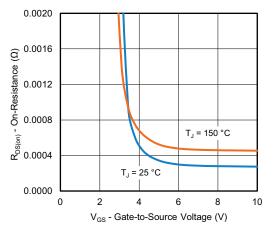


Fig. 10 - On-Resistance vs. Gate-to-Source Voltage

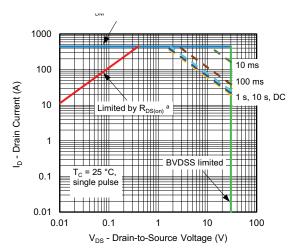


Fig. 11 - Safe Operating Area

Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

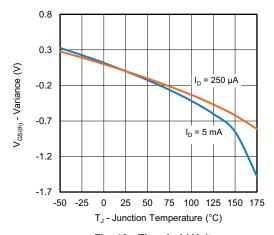


Fig. 12 - Threshold Voltage

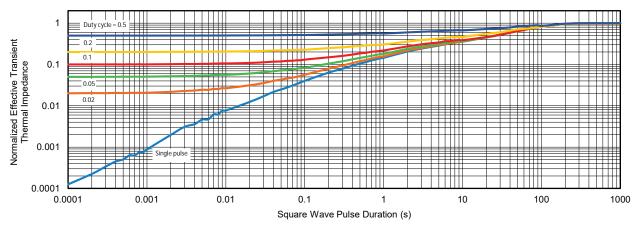


Fig. 13 - Normalized Thermal Transient Impedance, Junction-to-Ambient

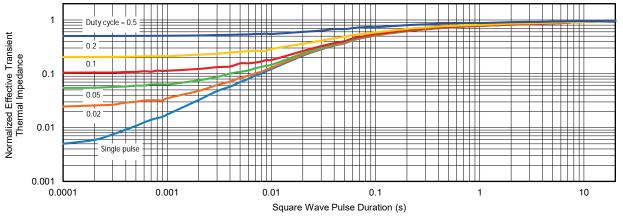


Fig. 14 - Normalized Thermal Transient Impedance, Junction-to-Case

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PowerPAK® 8 x 8L BWL Case Outline 2



MAX.
0.067
0.005
0.030
0.043
0.046
0.277
0.012
0.315
0.272
0.022
0.106
0.080
0.319
0.249
0.174
0.202
0.157
0.033
0.030
0.045
0.020
0.017
0.026
0.079
5°

ECN: S19-0643-Rev. B, 05-Aug-2019

DWG: 6073

Note

Millimeter will govern



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