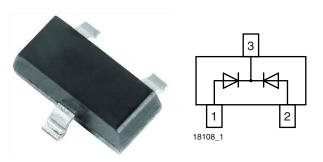


Dual Common Cathode Small Signal High Voltage Switching Diode



LINKS TO ADDITIONAL RESOURCES





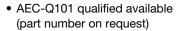


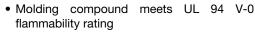




FEATURES

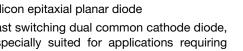
- Silicon epitaxial planar diode
- · Fast switching dual common cathode diode, especially suited for applications requiring high voltage capability







- please see www.vishav.com/doc?99912





RoHS

HALOGEN FREE **GREEN**

- Base P/N-G3 green, commercial grade
- · Material categorization: for definitions of compliance

MECHANICAL DATA

Case: SOT-23

Weight: approx. 9.2 mg Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE							
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY	
GSD2004C-G	GSD2004C-G3-08	no	DBK	Common anode	3 000 (8 mm tape on 7" reel)	15 000	
	GSD2004C-G3-18	no	DBK		10 000 (8 mm tape on 13" reel)	10 000	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Continuous reverse voltage		V_{R}	240	V	
Peak repetitive reverse voltage		V_{RRM}	300	V	
Forward current (continuous) (1)		I _F	400	mA	
Peak repetitive forward current (1)		I _{FRM}	625	mA	
Non-repetitive peak forward current (1)	t _p = 1 μs	I	4	Α	
	t _p = 1 s	IFSM	1	Α	
Power dissipation	on FR-4 board with recommended soldering footprint	В	300	mW	
rowei dissipation	Infinite heatsink	P _{tot}	500	mW	

Note

(1) Infinite heatsink

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Typical thermal resistance junction to ambient air	according to JEDEC [®] 51-3 on FR-4 board with recommended soldering footprint	R _{thJA}	420	K/W		
Thermal resistance junction to lead	Infinite heatsink	R_{thJL}	250	K/W		
Junction temperature		Tj	150	°C		
Storage temperature range		T _{stg}	-65 to +150	°C		
Operating temperature range		T _{op}	-55 to +150	°C		



ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	I _R = 100 μA	V_{BR}	300			V
Laslana aumant	V _R = 240 V	I _R			100	nA
Leakage current	V _R = 240 V, T _j = 150 °C	I _R			100	μΑ
Converd veltage	I _F = 20 mA	V _F		0.83	0.87	V
Forward voltage	I _F = 100 mA	V _F			1	V
Diode capacitance	$V_F = V_R = 0$, $f = 1$ MHz	C _D			5	pF
Reverse recovery time	$I_F = I_R = 30$ mA, $i_R = 3$ mA, $R_L = 100~\Omega$	t _{rr}			50	ns

TYPICAL CHARACTERISICS (T_{amb} = 25 °C, unless otherwise specified)

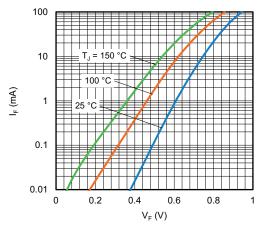


Fig. 1 - Forward Current vs. Forward Voltage

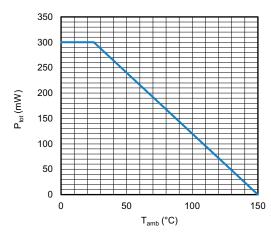


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

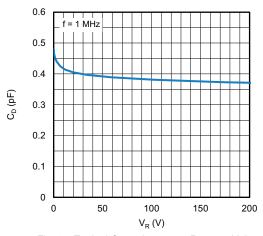


Fig. 3 - Typical Capacitance vs. Reverse Voltage

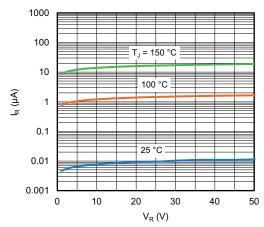
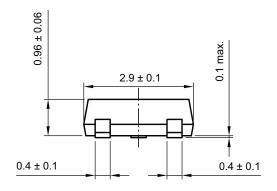
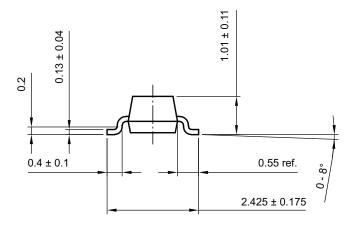


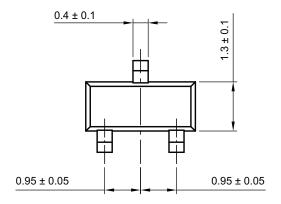
Fig. 4 - Typical Reverse Leakage Current vs. Reverse Voltage



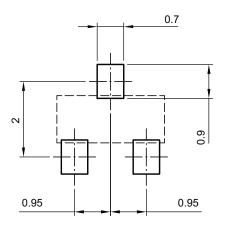
PACKAGE DIMENSIONS in millimeters: **SOT-23**







footprint recommendation:



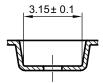
Created - Date: 18-Oct-2021 Rev. 01 - Date: 18-Jan-2022 S8-V-3929.01-009 (4)



CARRIER TAPE SOT-23

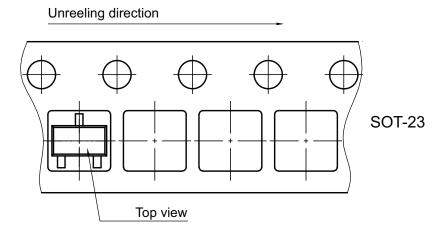
A-A Section 0.229 ± 0.013 0.229 ± 0.013 0.229 ± 0.013 0.22 ± 0.13 0.22 ± 0.13

B-B Section



Created Date: 04-Feb-2010 Rev. Date: 07-Feb-2022 S8-V-3929.01-005 (4)

ORIENTATION IN CARRIER TAPE SOT-23



Created Date: 04-Feb-2010 Rev. Date: 07-Nov-2022 S8-V-3929.01-005 (4)



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