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Vishay Siliconix

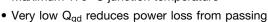
# N-Channel 80 V (D-S) MOSFET



PRODUCT SUMMARY				
V <sub>DS</sub> (V)	80			
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 10 \text{ V}$	0.0024			
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS} = 7.5 \text{ V}$	0.0028			
Q <sub>g</sub> typ. (nC)	151.2			
I <sub>D</sub> (A)	150 <sup>d</sup>			
Configuration	Single			

#### **FEATURES**

- TrenchFET® power MOSFET
- Maximum 175 °C junction temperature



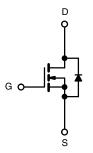


- 100 % R<sub>a</sub> and UIS tested
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **APPLICATIONS**

through  $V_{\text{plateau}}$ 

- Power supply
  - Secondary synchronous rectification
- DC/DC converter
- Power tools
- · Motor drive switch
- DC/AC inverter
- · Battery management
- OR-ing / e-fuse



N-Channel MOSFET

ORDERING INFORMATION			
Package	TO-220		
Lead (Pb)-free and halogen-free	SUP60020E-GE3		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V <sub>DS</sub>	80	V	
Gate-source voltage		V <sub>GS</sub>	± 20	V	
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C		150 <sup>d</sup>		
	T <sub>C</sub> = 70 °C	l <sub>D</sub>	150 <sup>d</sup>	A	
Pulsed drain current (t = 100 μs)		I <sub>DM</sub>	500		
Avalanche current		I <sub>AS</sub>	60		
Single avalanche energy <sup>a</sup> L = 0.1 mH		E <sub>AS</sub>	180	mJ	
Maximum power dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	В	375 <sup>b</sup>	W	
	T <sub>C</sub> = 125 °C	P <sub>D</sub>	125 <sup>b</sup>	vv	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-ambient (PCB mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W	
Junction-to-case (drain)	R <sub>thJC</sub>	0.4		

### Notes

- a. Duty cycle  $\leq$  1 %
- b. See SOA curve for voltage derating
- c. When mounted on 1" square PCB (FR4 material)
- d. Package limited



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	80	-	-	V
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2	-	4	
Gate-body leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA
Zero gate voltage drain current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1	_
	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	150	μΑ
		V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C	-	-	5	mA
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	120	-	-	Α
During and the social and a		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A	-	0.00200	0.00240	Ω
Drain-source on-state resistance a	R <sub>DS(on)</sub>	V <sub>GS</sub> = 7.5 V, I <sub>D</sub> = 20 A	-	0.00215	0.00280	
Forward transconductance a	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	-	115	-	S
Dynamic <sup>b</sup>						
Input capacitance	C <sub>iss</sub>		-	10 680	-	
Output capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 40 \text{ V}, f = 1 \text{ MHz}$	-	1180	-	рF
Reverse transfer capacitance	C <sub>rss</sub>		-	50	-	
Total gate charge <sup>c</sup>	Qg		-	151.2	227	
Gate-source charge c	$Q_{gs}$	$V_{DS} = 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 41.7 \text{ A}$	-	48.4	-	nC
Gate-drain charge <sup>c</sup>	$Q_{gd}$		-	24	-	
Output charge	Q <sub>oss</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V	-	138	207	
Gate resistance	$R_{g}$	f = 1 MHz	0.34	1.7	3.4	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>		-	30	60	
Rise time <sup>c</sup>	t <sub>r</sub>	$\begin{aligned} V_{DD} &= 40 \text{ V}, \text{ R}_L = 1.2 \Omega \\ I_D &\cong 33.3 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_g = 1 \Omega \end{aligned}$	-	13	26	
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>		-	50	100	ns -
Fall time <sup>c</sup>	t <sub>f</sub>		-	15	30	
<b>Drain-Source Body Diode Ratings</b>	and Characte	ristics <sup>b</sup> (T <sub>C</sub> = 25 °C)				
Pulsed current (t = 100 μs)	I <sub>SM</sub>		-	-	250	Α
Forward voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0 V	-	0.75	1.5	V
Reverse recovery time	t <sub>rr</sub>		-	80	160	ns
Peak reverse recovery charge	I <sub>RM(REC)</sub>	I <sub>F</sub> = 33.3 A, di/dt = 100 A/µs	-	4	6	Α
Reverse recovery charge	Q <sub>rr</sub>		-	0.182	0.275	μC
Reverse recovery fall time	t <sub>a</sub>		-	44	-	ne
Reverse recovery rise time	t <sub>b</sub>		-	36	-	ns

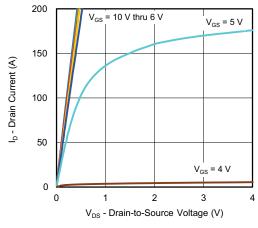
#### Notes

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

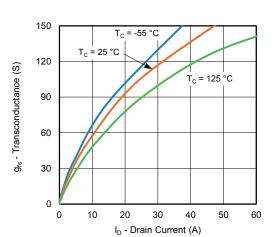
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



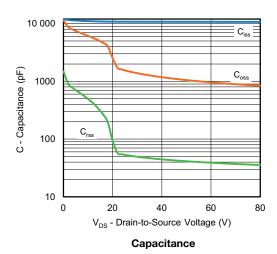
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)

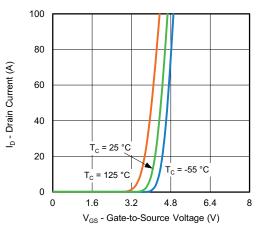


### **Output Characteristics**

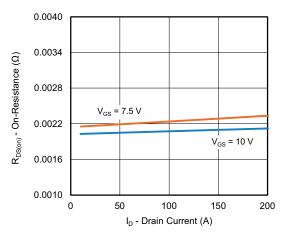


Transconductance

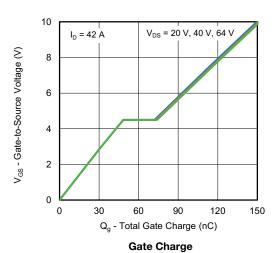




**Transfer Characteristics** 

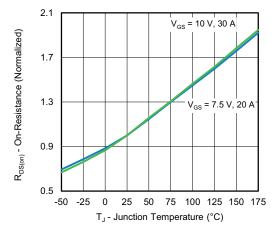


On-Resistance vs. Drain Current

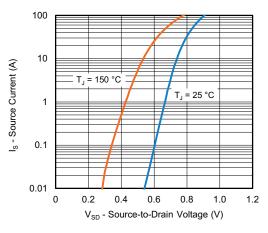




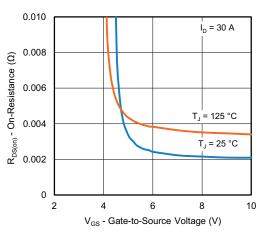
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



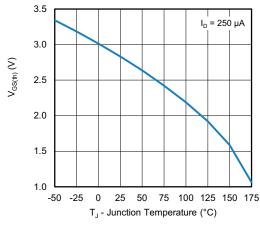
On-Resistance vs. Junction Temperature



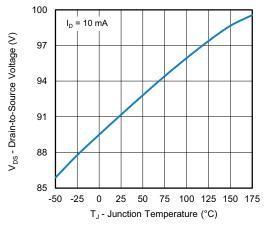
**Source Drain Diode Forward Voltage** 



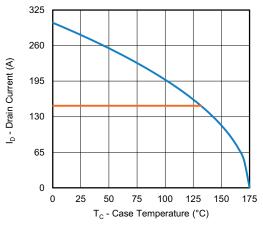
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



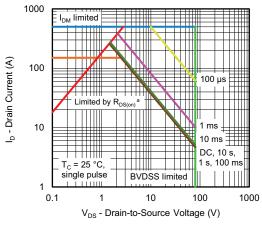
Drain Source Breakdown vs. Junction Temperature

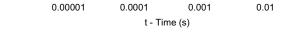


**Current De-rating** 



### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)





Single Pulse Avalanche Current Capability vs. Time

150 °C

25 °C

100

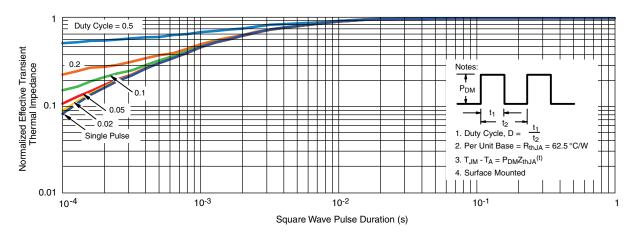
10

I<sub>DAV</sub> - Drain Current Avalanche (A)

#### Safe Operating Area

### Note

a.  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified



Normalized Thermal Transient Impedance, Junction-to-Case

### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25  $^{\circ}\text{C})$  Normalized Transient Thermal Impedance Junction to Case (25  $^{\circ}\text{C})$
  - are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg277056">www.vishay.com/ppg277056</a>.





## **TO-220AB**



	D2

	MILLIMETERS		INC	HES
DIM.	MIN.	MAX.	MIN.	MAX.
А	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
С	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
D2	12.19	12.70	0.480	0.500
Е	10.04	10.51	0.395	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
ØΡ	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: T14-0413-Rev. P, 16-Jun-14 DWG: 5471				

#### Note

 $<sup>^{\</sup>star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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