

RoHS

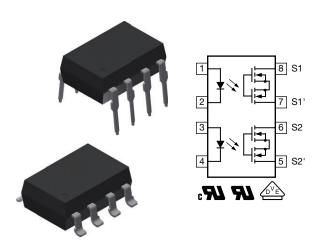
COMPLIANT

HALOGEN FREE

GREEN

(5-2008)

Dual 1 Form A Solid-State Relay



LINKS TO ADDITIONAL RESOURCES







DESCRIPTION

The VOR2142 is a 400 V dual channel normally open optically isolated solid-state relay (SPST - 1 form A). Based on hybrid architecture which allows fast switching times with a wide operating ambient temperature range. A high efficient GaAlAs IRED enables low forward current on the input side. On the output side high performance MOSFET switches provide a low R_{ON} and can switch both DC and AC signals.

FEATURES

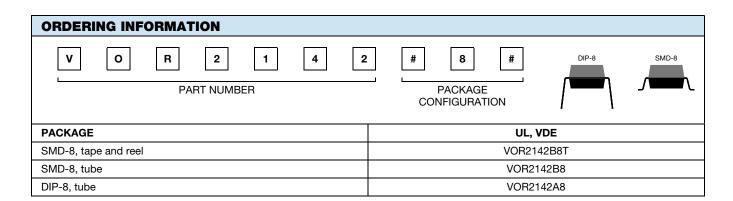
- Isolation test voltage 5300 V_{RMS}
- Typical R_{ON} 22 Ω
- Load voltage 400 V
- Load current 140 mA
- Clean bounce free switching
- Low power consumption
- Wide temperature range
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- · General telecom switching
- Metering
- Security equipment
- Instrumentation
- Industrial controls
- Battery management systems
- · Automatic test equipment

AGENCY APPROVALS

- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)





ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	CONDITIONS	SYMBOL	VALUE	UNIT
INPUT				
IRED continuous forward current		I _F	50	mA
IRED reverse voltage		V _R	5	V
Input power dissipation		P _{diss}	80	mW
Junction temperature		T _j	125	°C
ОUТРUТ				
DC or peak AC load voltage		V_L	400	V
Continuous DC load current at 25 °C, one channel		ΙL	140	mA
Continuous DC load current at 25 °C, two channels		ΙL	100	mA
SSR output power dissipation		P _{diss}	550	mW
Junction temperature		Tj	125	°C
SSR			<u> </u>	
Ambient temperature range		T _{amb}	-40 to +100	°C
Storage temperature range		T _{stg}	-40 to +150	°C
Soldering temperature	t = 10 s max.	T _{sld}	260	°C

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

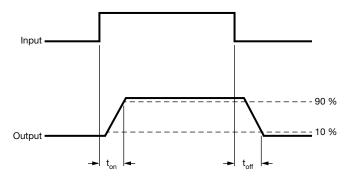
ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
IRED forward current, switch turn-on	$I_L = 100 \text{ mA}, t = 10 \text{ ms}$	I _{Fon}	ı	0.25	2	mA
IRED forward current, switch turn-off	$V_{L} = \pm 350 \text{ V}$	I _{Foff}	0.05	0.15	-	mA
IRED forward voltage	I _F = 10 mA	V _F	-	1.4	1.6	V
IRED reverse current	V _R = 5 V	I _R	-	-	10	μΑ
OUTPUT						
On-resistance	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	R _{ON}	-	22	27	Ω
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	1	5000	-	GΩ
Off state leakage assument	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Io	-	< 1	100	nA
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 400 \text{ V}$	Io	-	6	500	nA
0.15.15.55.51	I _F = 0 mA, V _L = 1 V, 1 MHz	Co	-	39	-	pF
Output capacitance	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}, 1 \text{ MHz}$	Co	-	6	=	pF
TRANSFER						
Capacitance (input to output)	V _{IO} = 1 V	C _{IO}	-	1	=	pF

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{on}	-	0.13	0.5	ms
Turn-off time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{off}	-	0.05	0.2	ms





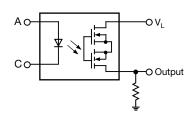


Fig. 1 - Timing Schematic

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40 / 100 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	5300	V _{RMS}
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	8000	V _{peak}
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	890	V _{peak}
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	640	mW
Input safety current		I _{SI}	240	mA
Safety temperature		T _S	175	°C
Creepage distance	DIP-8		≥7	mm
Clearance distance	DIP-8		≥ 7	mm
Creepage distance	SMD-8		≥ 8	mm
Clearance distance	SMD-8		≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	V_{IORM} x 1.875 = V_{PR} , 100 % production test with t_{M} = 1 s, partial discharge < 5 pC	V_{PR}	1669	V _{peak}
Input to output test voltage, method A	V_{IORM} x 1.6 = V_{PR} , 100 % sample test with t_M = 10 s, partial discharge < 5 pC	V _{PR}	1424	V _{peak}

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



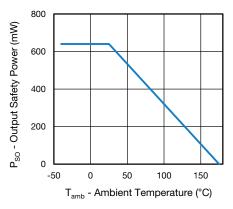


Fig. 2 - Safety Power Dissipation vs. Ambient Temperature

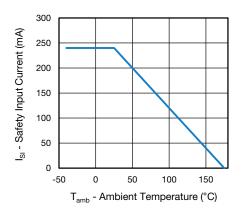


Fig. 3 - Safety Input Current vs. Ambient Temperature

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

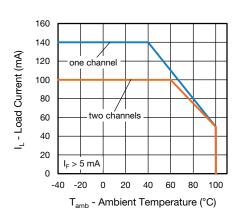


Fig. 4 - Maximum Load Current vs. Ambient Temperature

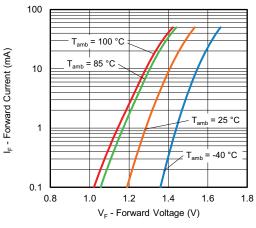


Fig. 6 - Forward Current vs. Forward Voltage

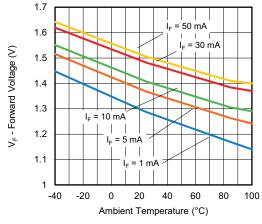


Fig. 5 - Forward Voltage vs. Ambient Temperature

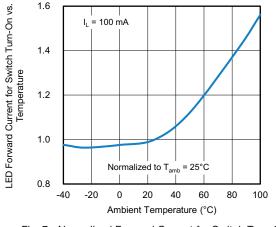


Fig. 7 - Normalized Forward Current for Switch Turn-On vs.
Ambient Temperature

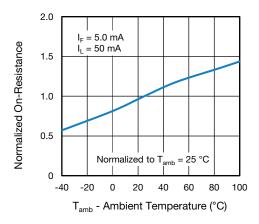


Fig. 8 - Normalized On-Resistance vs. Ambient Temperature

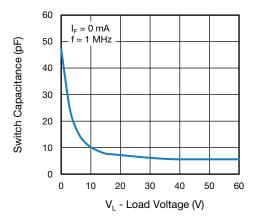


Fig. 9 - Output Capacitance vs. Load Voltage

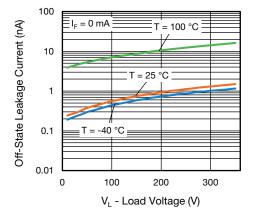


Fig. 10 - Off-State Leakage Current vs. Load Voltage

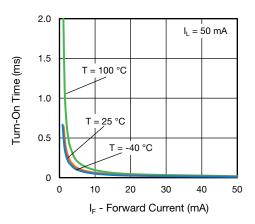


Fig. 11 - Turn-On Time vs. Forward Current

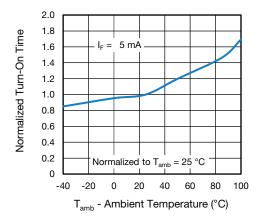


Fig. 12 - Normalized Turn-On Time vs. Ambient Temperature

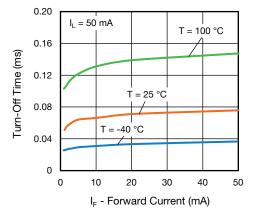


Fig. 13 - Turn-Off Time vs. Forward Current



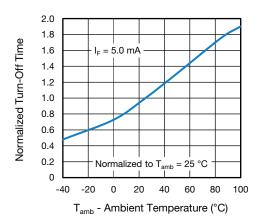
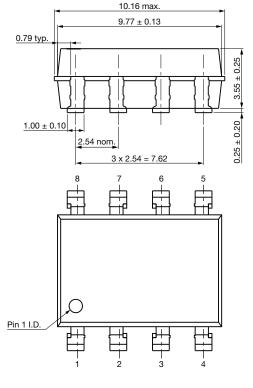
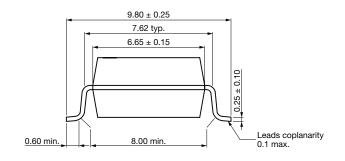


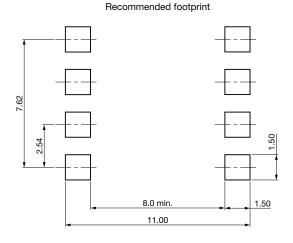
Fig. 14 - Normalized Turn-Off Time vs. Ambient Temperature

PACKAGE DIMENSIONS (in millimeters)

SMD-8



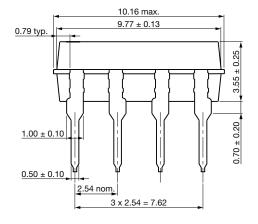


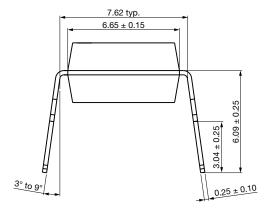


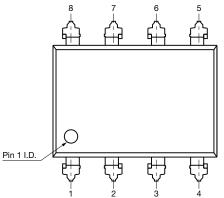
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Vishay Semiconductors

DIP-8







PACKAGE MARKING (example)

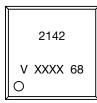


Fig. 15 - VOR2142

Notes

- XXXX = LMC (lot marking code)
- Tape and reel suffix (TR) is not part of the package marking



PACKING INFORMATION (in millimeters)

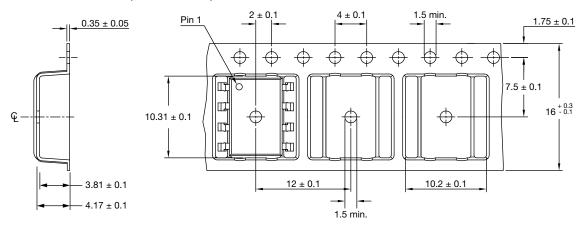


Fig. 16 - Tape and Reel Packing

TAPE AND REEL PACKING		
TYPE	UNITS/REEL	
SMD-8	1000	

TUBE PACKING			
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX
SMD-8	50	40	2000
DIP-8	50	40	2000

SOLDER PROFILES

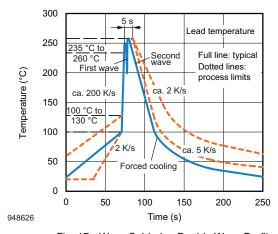


Fig. 17 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

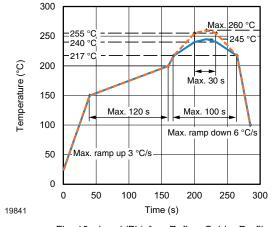


Fig. 18 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



Footprint and Schematic Information

Vishay Semiconductors

Footprint and Schematic Information for VOR2142

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC	
VOR2142A8	www.snapeda.com/parts/VOR2142A8/Vishay/view-part	
VOR2142B8	www.snapeda.com/parts/VOR2142B8/Vishay/view-part	
VOR2142B8T	www.snapeda.com/parts/VOR2142B8T/Vishay/view-part	

For technical issues and product support, please contact optocoupleranswers@vishav.com.





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