

# **High Performance Schottky Rectifier, 120 A**





HALF-PAK (D-67)

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	120 A			
$V_{R}$	30 V			
Package	HALF-PAK (D-67)			
Circuit configuration	Single diode			

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

The VS-122NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN			
I <sub>F(AV)</sub>	Rectangular waveform	120	Α		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	18 000	Α		
V <sub>F</sub>	120 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.47	V		
T <sub>J</sub>	Range	-55 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-122NQ030PbF	UNITS	
Maximum DC reverse voltage	$V_{R}$	30	V	
Maximum working peak reverse voltage	$V_{RWM}$	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 115 °C, rectangular waveform		120	Α
Maximum peak one cycle non-repetitive surge current	l=a	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	18 000	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	2000	^
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 11 A, L = 1 mH		54	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		12	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	120 A	T <sub>J</sub> = 25 °C	0.57	
		240 A		0.75	V
		120 A	- T <sub>J</sub> = 125 °C	0.47	
		240 A		0.67	
Maximum reverse leakage	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	10	mA
current per leg See fig. 2	'RM'''	T <sub>J</sub> = 125 °C		560	IIIA
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		7400	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width  $<300~\mu s,$  duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage te	mperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C	
Maximum thermal resistance, junc	ction to case	R <sub>thJC</sub>	DC operation See fig. 4	0.38	°C/W	
Typical thermal resistance, case to	o heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
				1.06	oz.	
Mounting toward	minimum			3 (26.5)		
Mounting torque	maximum		Name to be signed and the second	4 (35.4)	N⋅m	
Terminal torque -	minimum		Non-lubricated threads 3.4 (30)	3.4 (30)	(lbf · in)	
	maximum			5 (44.2)		
Case style				HALF-PAK module		

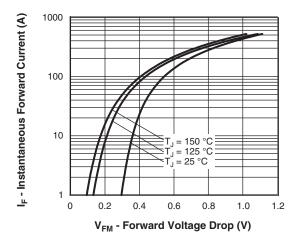


Fig. 1 - Maximum Forward Voltage Drop Characteristics

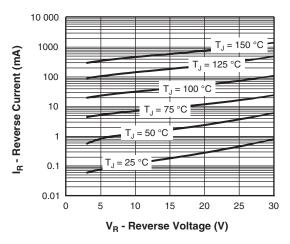


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



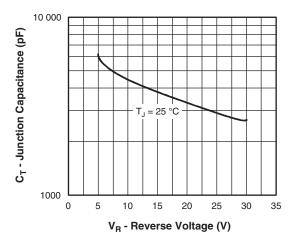


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

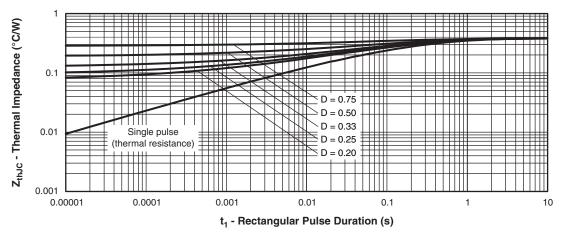


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

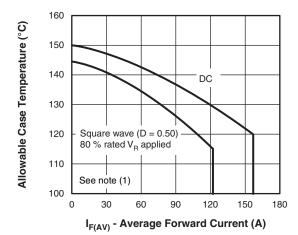


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

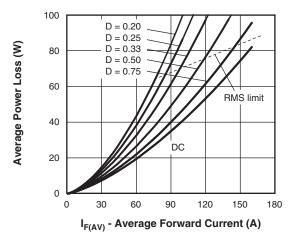
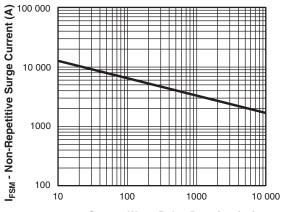


Fig. 6 - Forward Power Loss Characteristics



t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

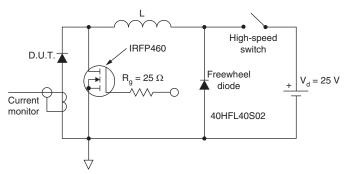


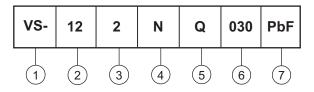
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$ 

#### **ORDERING INFORMATION TABLE**

Device code



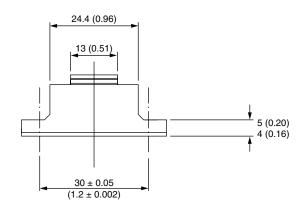
- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- Product silicon identification
- 4 N = not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (030 = 30 V)
- 7 Lead (Pb)-free

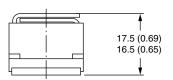
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			

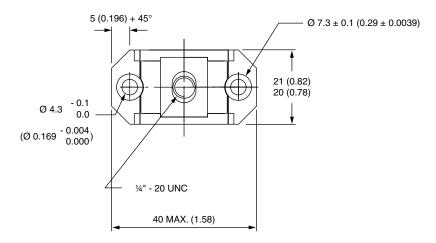


### **D-67 HALF-PAK**

### **DIMENSIONS** in millimeters (inches)









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