COMPLIANT

HALOGEN

FREE



Vishay Semiconductors

High Performance Schottky Rectifier, 1.0 A



SMB (DO-214AA)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	1.0 A			
V_R	30 V			
V _F at I _F	0.420 V			
I _{RM} max.	15 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	3.0 mJ			
Package	SMB (DO-214AA)			
Circuit configuration	Single			

FEATURES

- Small foot print, surface mountable
- · Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-10BQ030HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	Α	
V _{RRM}		30	V	
I _{FSM}	t _p = 5 ms sine	430	А	
V _F	1.0 A _{pk} , T _J = 125 °C	0.30	V	
T _J	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-10BQ030HM3	UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	I _{F(AV)} 50 % duty cycle at T _L = 106 °C, rectangular waveform		1.0	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	430	Α
non-repetitive surge current See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	90	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 6 \text{mH}$		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		Α	



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop		1 A	T _{.1} = 25 °C	0.420	V
	V _{FM} ⁽¹⁾	2 A	- IJ = 25 C	0.470	
	V _{FM} (*)	1 A	T 105 %C	0.300	
		2 A	T _J = 125 °C	0.370	
		T _J = 25 °C		0.5	
Maximum reverse leakage current	urrent I _{RM} ⁽¹⁾	T _J = 100 °C	V _R = Rated V _R	5.0	mA
		T _J = 125 °C		15	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC operation	25	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (DO-214AA)	1	E

Notes

⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB

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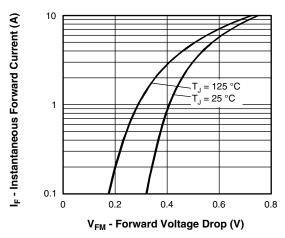


Fig. 1 - Maximum Forward Voltage Drop Characteristics

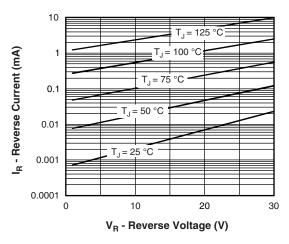


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

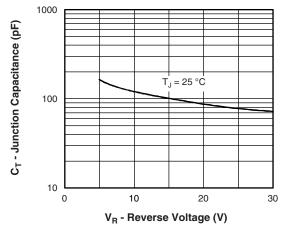
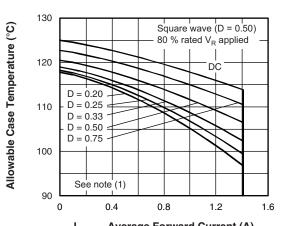


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



I_{F(AV)} - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature

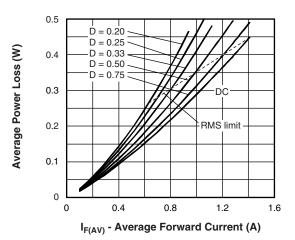


Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current

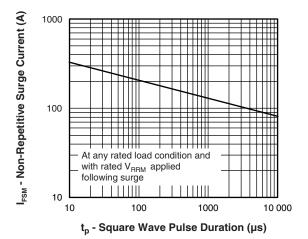


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

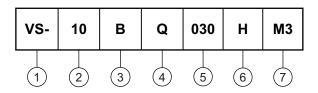
 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$



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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating

3 - B = single lead diode

4 - Q = Schottky "Q" series

5 - Voltage rating (030 = 30 V)

6 - H = AEC-Q101 qualified

7 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-10BQ030HM3/5BT	5BT	3200	13" diameter plastic tape and reel	

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95401		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



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SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout





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