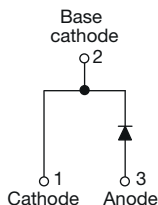


Fast Soft Recovery Rectifier Diode, 10 A



2L TO-220AC



FEATURES

- Glass passivated pellet chip junction
- Meets JESD 201 class 1A whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- On-board and off-board EV/HEV battery chargers
- Input rectification

DESCRIPTION

The VS-10ETF06THM3 fast soft recovery rectifier series has been optimized for combined short reverse recovery time and low forward voltage drop.

The glass passivation ensures stable reliable operation in the most severe temperature and power cycling conditions.

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	10 A
V_R	600 V
V_F at I_F	1.2 V
I_{FSM}	130 A
t_{rr}	50 ns
T_J max.	150 °C
Snap factor	0.6
Package	2L TO-220AC
Circuit configuration	Single

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
V_{RRM}		600	V
$I_{F(AV)}$	Sinusoidal waveform	10	A
I_{FSM}		130	
t_{rr}	1 A, 100 A/μs	50	ns
V_F	10 A, $T_J = 25$ °C	1.2	V
T_J		-40 to +150	°C

VOLTAGE RATINGS

PART NUMBER	V_{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V_{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I_{RRM} AT 150 °C mA
VS-10ETF06THM3	600	700	3

ABSOLUTE MAXIMUM RATINGS

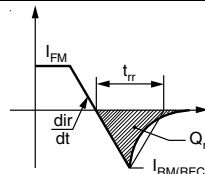
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$	$T_C = 128$ °C, 180° conduction half sine wave	10	A
Maximum peak one cycle non-repetitive surge current	I_{FSM}	10 ms sine pulse, rated V_{RRM} applied	110	
		10 ms sine pulse, no voltage reapplied	130	
Maximum I^2t for fusing	I^2t	10 ms sine pulse, rated V_{RRM} applied	60	A ² s
		10 ms sine pulse, no voltage reapplied	85	
Maximum $I^2\sqrt{t}$ for fusing	$I^2\sqrt{t}$	$t = 0.1$ to 10 ms, no voltage reapplied	850	A ² √s

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V_{FM}	10 A, $T_J = 25^\circ\text{C}$		1.2	V
Forward slope resistance	r_t	$T_J = 150^\circ\text{C}$		23.5	$\text{m}\Omega$
Threshold voltage	$V_{F(TO)}$			0.85	V
Maximum reverse leakage current	I_{RM}	$T_J = 25^\circ\text{C}$	$V_R = \text{Rated } V_{RRM}$	0.1	mA
		$T_J = 150^\circ\text{C}$		3.0	

RECOVERY CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Reverse recovery time	t_{rr}	I_F at 10 A _{pk} 25 A/ μs 25 $^\circ\text{C}$	200	ns
Reverse recovery current	I_{rr}		2.75	A
Reverse recovery charge	Q_{rr}		0.32	μC
Snap factor	S		0.6	


THERMAL - MECHANICAL SPECIFICATIONS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +150	°C
Maximum thermal resistance junction to case	R _{thJC}	DC operation	1.5	°C/W
Maximum thermal resistance junction to ambient	R _{thJA}		62	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum		6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style 2L TO-220AC	10ETF06TH	

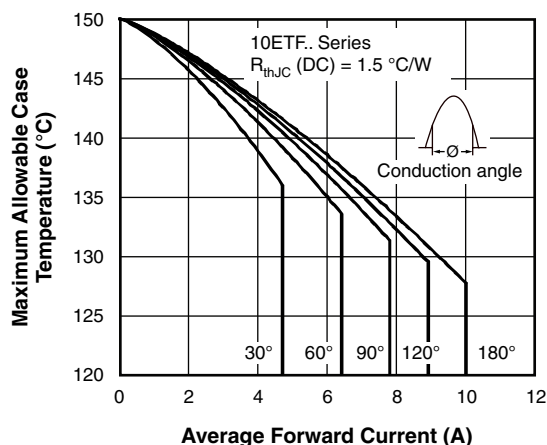


Fig. 1 - Current Rating Characteristics

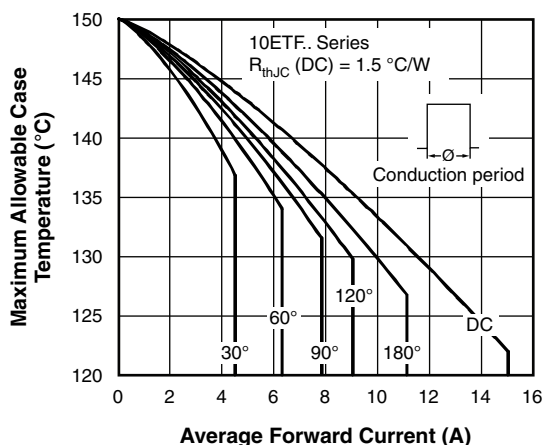


Fig. 2 - Current Rating Characteristics

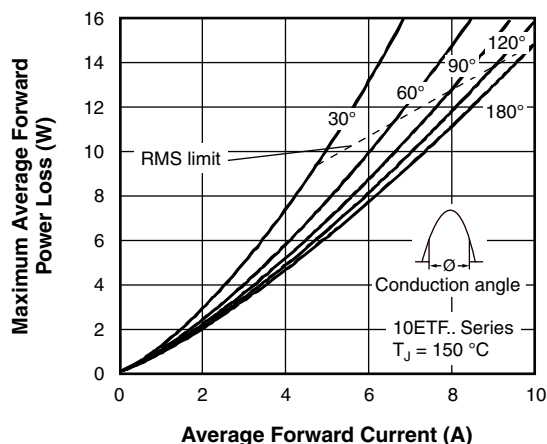


Fig. 3 - Forward Power Loss Characteristics

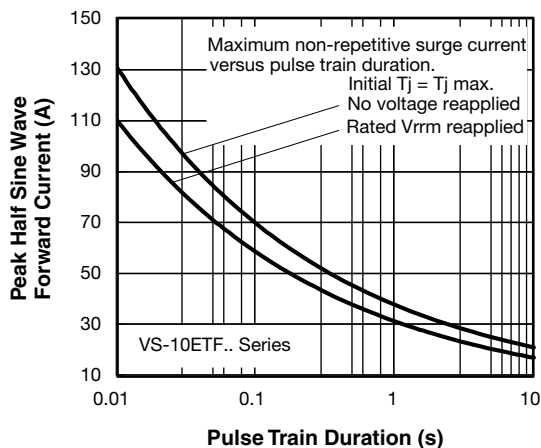


Fig. 6 - Maximum Non-Repetitive Surge Current

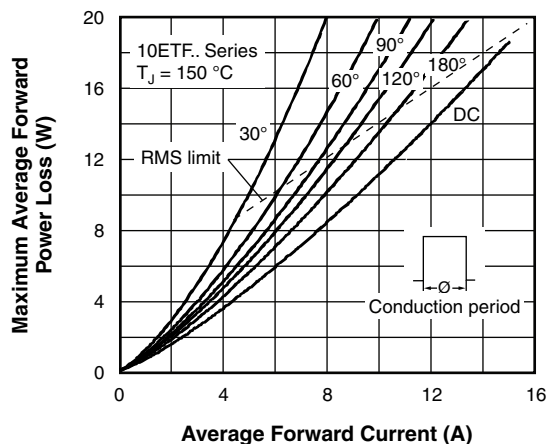


Fig. 4 - Forward Power Loss Characteristics

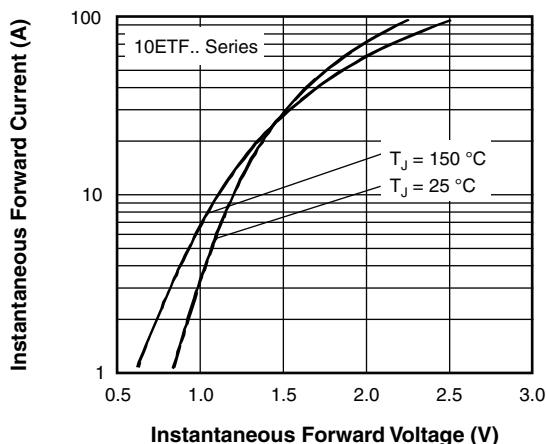


Fig. 7 - Forward Voltage Drop Characteristics

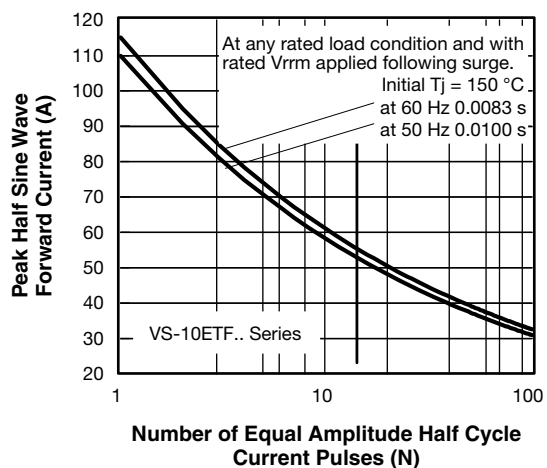
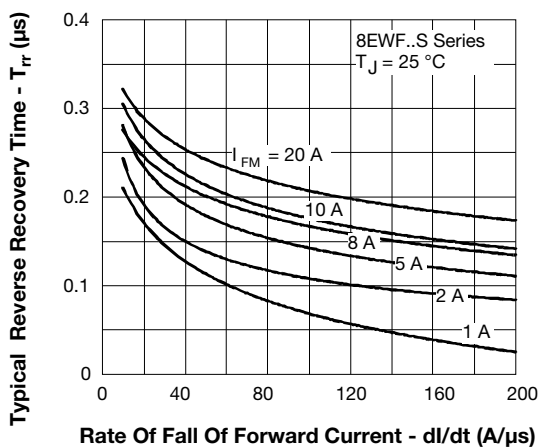
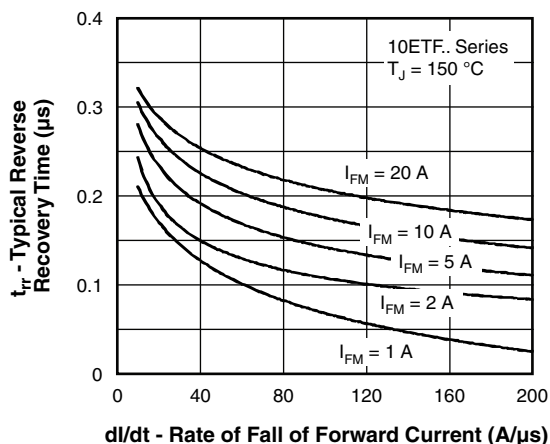
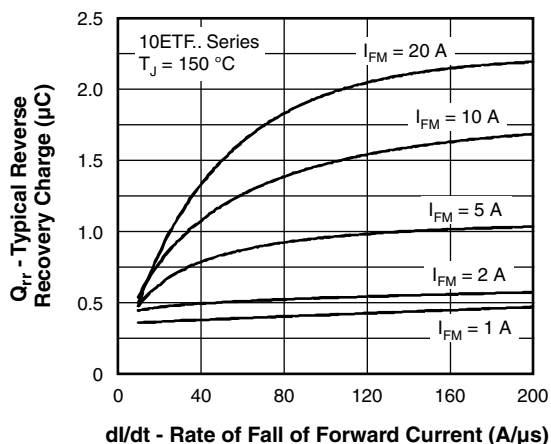
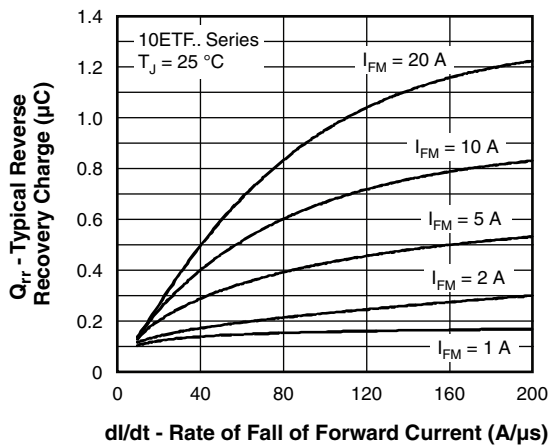
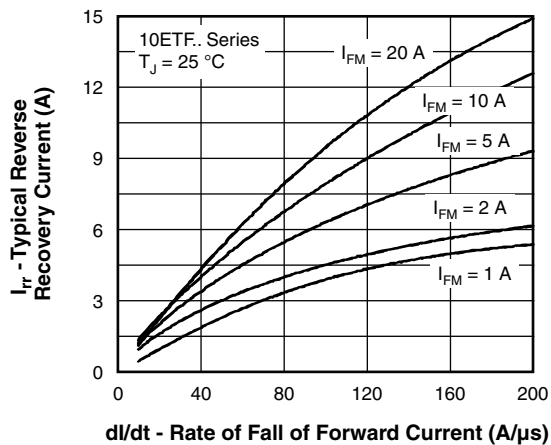
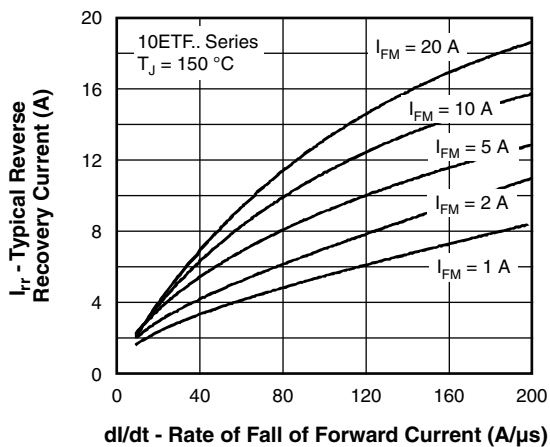
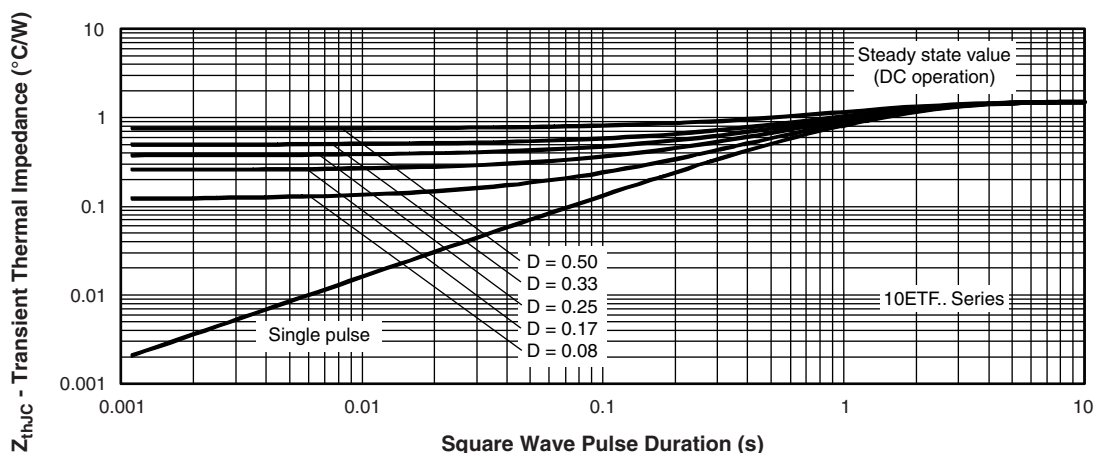


Fig. 5 - Maximum Non-Repetitive Surge Current


Fig. 8 - Recovery Time Characteristics, $T_J = 25\text{ }^{\circ}\text{C}$


Fig. 9 - Recovery Time Characteristics, $T_J = 150^\circ C$

Fig. 11 - Recovery Charge Characteristics, $T_J = 150^\circ C$

Fig. 10 - Recovery Charge Characteristics, $T_J = 25^\circ C$

Fig. 12 - Recovery Current Characteristics, $T_J = 25^\circ C$

Fig. 13 - Recovery Current Characteristics, $T_J = 150^\circ C$


Fig. 14 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code	VS-	10	E	T	F	06	T	H	M3
	①	②	③	④	⑤	⑥	⑦	⑧	⑨

- 1** - Vishay Semiconductors product
- 2** - Current rating (10 = 10 A)
- 3** - Circuit configuration:
E = 2L TO-220AC
- 4** - Package:
T = TO-220
- 5** - Type of silicon:
F = fast soft recovery rectifier
- 6** - Voltage code x 100 = V_{RRM} ——— **06 = 600 V**
- 7** -
 - None = TO-220AB
 - T = true pin TO-220
- 8** - H = AEC-Q101 qualified
- 9** - Environmental digit:
M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

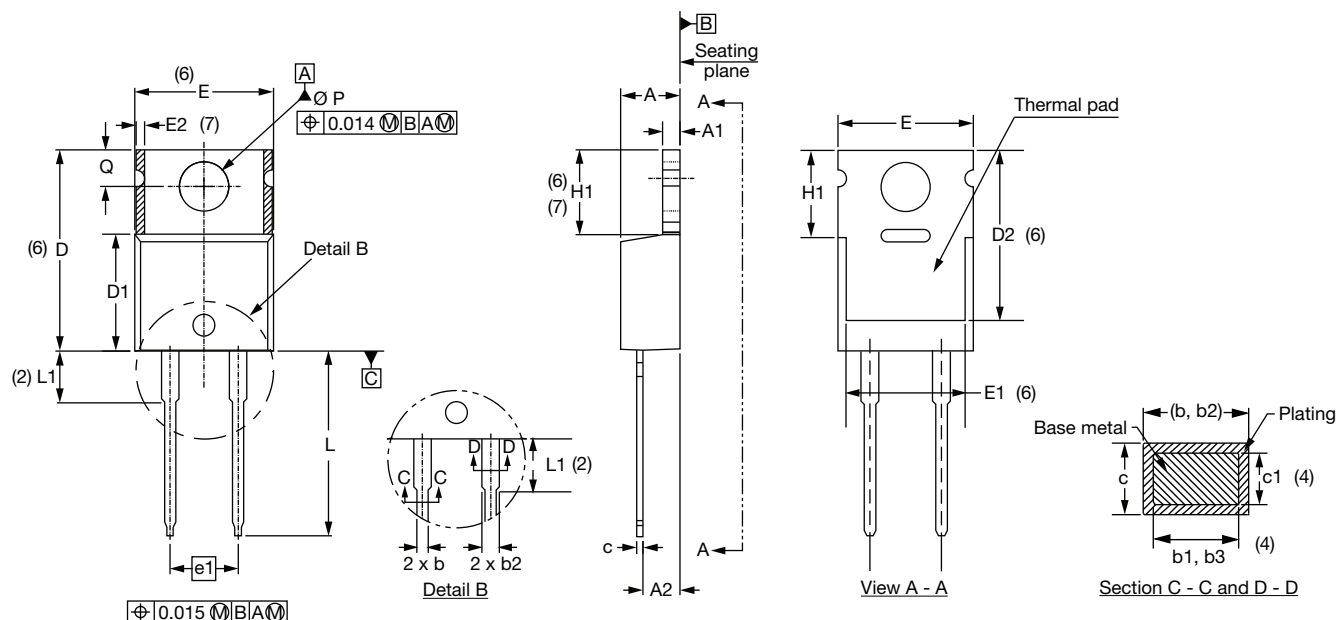
ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-10ETF06THM3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96069
Part marking information	www.vishay.com/doc?95391



TO-220AC 2L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
c	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
E	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
e1	4.88	5.28	0.192	0.208	
H1	5.84	6.86	0.230	0.270	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
Ø P	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	

Notes

- Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension and finish uncontrolled in L1
- Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1, b3 and c1 apply to base metal only
- Controlling dimension: inches
- Thermal pad contour optional within dimensions E, H1, D2 and E1
- Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- Outline conforms to JEDEC® TO-220, except D2, where JEDEC® minimum is 0.480"



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