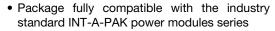


Three Phase Bridge (Power Modules), 60/70 A



| PRIMARY CHARACTERISTICS | | | | |
|-------------------------|--------------------|--|--|--|
| I _O | 60 A to 70 A | | | |
| V _{RRM} | 800 V to 1600 V | | | |
| Package | MTK | | | |
| Circuit configuration | Three phase bridge | | | |

FEATURES





- · High thermal conductivity package, electrically insulated case
- · Excellent power volume ratio, outline for easy connections to power transistor and IGBT modules
- 4000 V_{RMS} isolating voltage
- UL E78996 approved
- · Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION

A range of extremely compact, encapsulated three phase bridge rectifiers offering efficient and reliable operation. They are intended for use in general purpose and heavy duty applications.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | |
|-----------------------------------|-----------------|---|---------|-------------------|--|
| SYMBOL | CHARACTERISTICS | CHARACTERISTICS VALUES 60MTK VALUES 70MTK | | UNITS | |
| ı | | 60 (75) | 70 (90) | Α | |
| T _C | | 85 (61) | 85 (57) | °C | |
| ı | 50 Hz | 420 | 480 | А | |
| I _{FSM} | 60 Hz | 440 | 500 | | |
| 12. | 50 Hz | 870 | 1150 | 1.02- | |
| l ² t | 60 Hz | 790 | | kA ² s | |
| I ² √t | | 8700 | 11 500 | kA²√s | |
| V _{RRM} | Range | 800 to 1600 | | V | |
| T _{Stg} | Panca | -40 to 150 | | °C | |
| TJ | Range | -40 to | C | | |

ELECTRICAL SPECIFICATIONS

| VOLTAGE RATINGS | | | | | | |
|----------------------|-----------------|--|--|---|--|--|
| TYPE NUMBER | VOLTAGE CODE | V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V | V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V | I _{RRM} MAXIMUM AT T _J MAXIMUM mA | | |
| VS-60MTK VS-70MTK | 80 | 800 | 900 | | | |
| | 100 | 1000 | 1100 | | | |
| | 120 | 1200 | 1300 | 10 | | |
| | 140 | 1400 | 1500 | | | |
| | 160 | 1600 | 1700 | | | |





| FORWARD CONDUCTION | | | | | | | |
|---|---------------------|---|------------------------|--|-----------------|-----------------|-------------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | | VALUES 60MTK | VALUES 70MTK | UNITS |
| Maximum DC output | 1- | 120° rect. conduction angle | | 60 (75) | 70 (90) | Α | |
| current at case temperature | I _O | | | 85 (61) | 85 (57) | °C | |
| Maximum peak, one-cycle forward, non-repetitive surge current | I _{FSM} | t = 10 ms | No voltage | | 420 | 480 | А |
| | | t = 8.3 ms | reapplied | | 440 | 500 | |
| | | t = 10 ms | 100 % V _{RRM} | | 350 | 400 | |
| | | t = 8.3 ms | reapplied | Initial | 370 | 420 | |
| Maximum I ² t for fusing | l ² t | t = 10 ms | No voltage | T _J = T _J maximum | 870 | 1150 | kA ² s |
| | | t = 8.3 ms | reapplied | | 790 | 1050 | |
| | | t = 10 ms | 100 % V _{RRM} | | 610 | 800 | |
| | | t = 8.3 ms | reapplied | | 560 | 730 | |
| Maximum I ² √t for fusing | I²√t | t = 0.1 ms to 10 ms, no voltage reapplied | | 8700 | 11 300 | A²√s | |
| Low level value of threshold voltage | V _{F(TO)1} | (16.7 % x π x I _{F(AV)} < I < π · I _{F(AV)}), T _J maximum | | 0.85 | 0.86 | V | |
| High level value of threshold voltage | V _{F(TO)2} | $(I > \pi \times I_{F(AV)})$, T_J maximum | | | 1.07 | 1.08 | V |
| Low level value of forward slope resistance | r _{f1} | (16.7 % x π x $I_{F(AV)}$ < I < $\pi \cdot I_{F(AV)}$), T_J maximum | | 8.04 | 7.35 | | |
| High level value of forward slope resistance | r _{f2} | $(I > \pi \times I_{F(AV)})$, T_J maximum | | 7.08 | 6.53 | mΩ | |
| Maximum forward voltage drop | V_{FM} | $I_{pk} = 100 \text{ A}, T_J = 25 \text{ °C}, t_p = 400 \mu \text{s single junction}$ | | 1.75 | 1.55 | | |
| RMS isolation voltage | V _{ISOL} | $T_J = 25$ °C, all terminal shorted f = 50 Hz, t = 1 s | | 00 | V | | |

| THERMAL AND MECHANICAL SPECIFICATIONS | | | | | | | |
|---|--|--|---|------------|-----------------|-------|-----|
| PARAMETER | | SYMBOL | TEST CONDITIONS | | VALUES 70MTK | UNITS | |
| Maximum junction operating a storage temperature range | nd | T _J , T _{Stg} | | -40 to 150 | | °C | |
| | | | DC operation per module | 0.37 | 0.29 | | |
| Maximum thermal resistance, junction to case | | | DC operation per junction | 2.22 | 1.75 | | |
| | R _{thJC} | 120° rect. conduction angle per module | 0.40 | 0.34 | K/W | | |
| | | 120° rect. conduction angle per junction | 2.42 | 2.01 | | | |
| Maximum thermal resistance, case to heatsink per module | R _{thCS} Mounting surface smooth, flat and grease | | Mounting surface smooth, flat and greased | 0.03 | | | |
| Mounting torque ± 10 % | to heatsink | | A mounting compound is recommended and the | 4 t | 0 6 | Nm | |
| | to terminal | | torque should be rechecked after a period of 3 h to allow for the spread of the compound. | | | | o 4 |
| Approximate weight | | | Lubricated threads. | 176 | | g | |

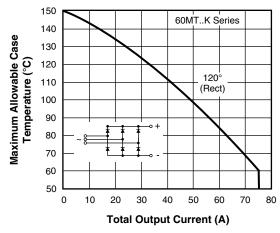
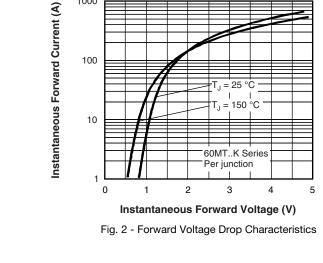
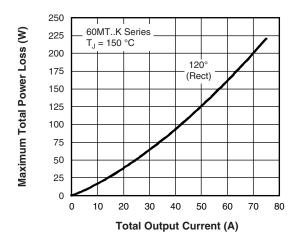


Fig. 1 - Current Ratings Characteristics



1000



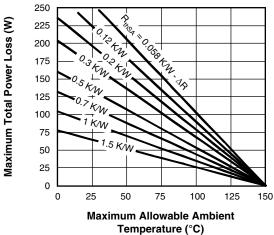


Fig. 3 - Total Power Loss Characteristics

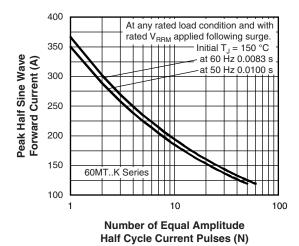


Fig. 4 - Maximum Non-Repetitive Surge Current

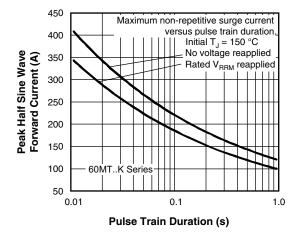


Fig. 5 - Maximum Non-Repetitive Surge Current

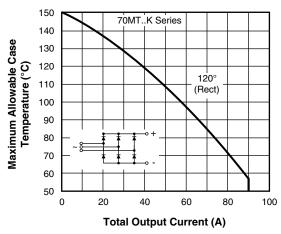


Fig. 6 - Current Ratings Characteristics

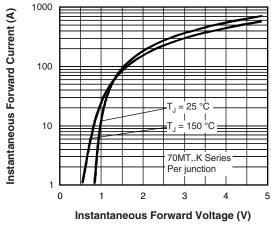


Fig. 7 - Forward Voltage Drop Characteristics

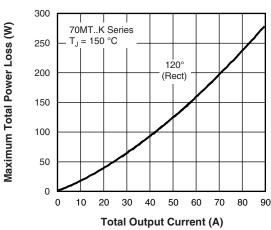
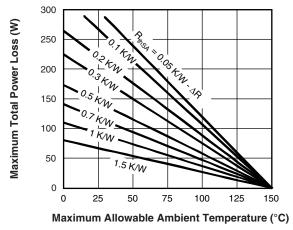


Fig. 8 - Total Power Loss Characteristics



450 At any rated load condition and with rated V_{RRM} applied following surge. 400 Initial T₁ = 150 °C Peak Half Sine Wave at 60 Hz 0.0083 s Forward Current (A) 350 at 50 Hz 0.0100 s 300 250 200 150 70MT. K Series 100 100 **Number of Equal Amplitude** Half Cycle Current Pulses (N)

Fig. 9 - Maximum Non-Repetitive Surge Current

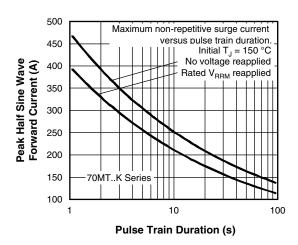


Fig. 10 - Maximum Non-Repetitive Surge Current

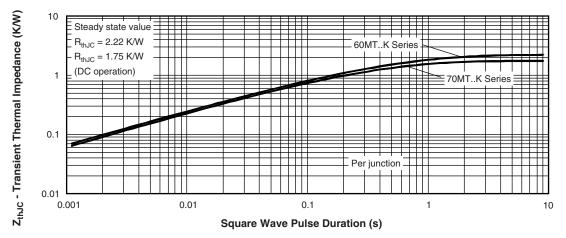


Fig. 11 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code VS- 7 0 MT 160

1 2 3 4 5 6

- 1 Vishay Semiconductors product
- 2 Current rating code: 6 = 60 A (average)
 - 7 = 70 A (average)

K

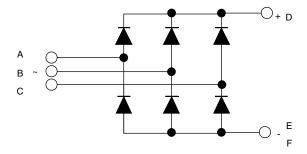
PbF

- Three phase diodes bridge
- Essential part number
- 5 Voltage code x 10 = V_{RRM} (see Voltage Ratings table)
- 6 PbF = Lead (Pb)-free

Note

• To order the optional hardware go to www.vishay.com/doc?95172

CIRCUIT CONFIGURATION

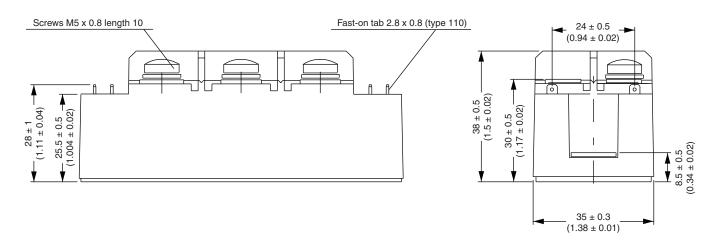


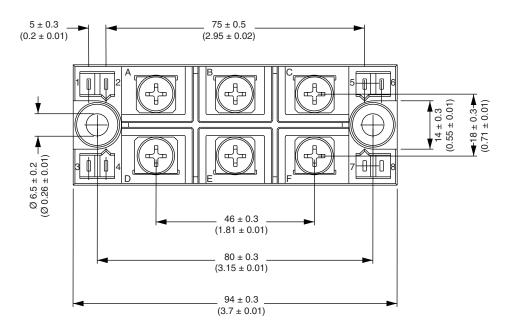
| LINKS TO RELATED DOCUMENTS | | | | |
|----------------------------|--------------------------|--|--|--|
| Dimensions | www.vishay.com/doc?95004 | | | |



MTK (with and without optional barrier)

DIMENSIONS WITH OPTIONAL BARRIERS in millimeters (inches)

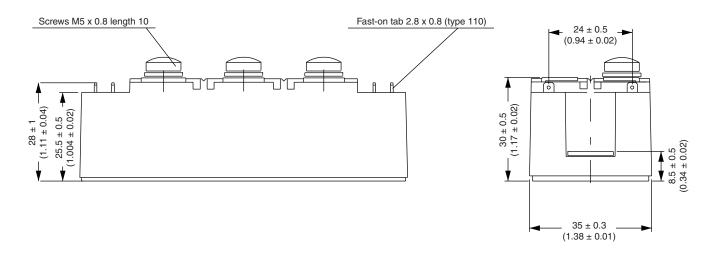


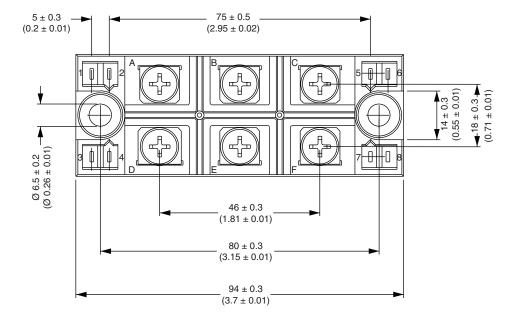


Vishay Semiconductors MTK (with and without optional barrier)



DIMENSIONS WITHOUT OPTIONAL BARRIERS in millimeters (inches)







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Vishay

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