



Surface Mount Transformers/Inductors, Gapped and Ungapped Custom Configurations Available



ELECTRICAL SPECIFICATIONS

(Multiple winds are connected in parallel)

Inductance Range: 10 µH to 68 000 µH, measured at 0.10 V RMS at 10 kHz without DC current, using an HP 4263A or HP 4284A impedance analyzer



DC Resistance Range: 0.03Ω to 24.1Ω , measured at $+25 \degree C \pm 5 \degree C$

Rated Current Range: 2.29 amps to 0.07 amps

Dielectric Withstanding Voltage: 500 V RMS, 60 Hz, 5 seconds

Pad Layout Pad Layout O.045 [1.14] Pad Dimensions Typical, 10 places O.079 [2.01] O.045 [1.14] Typical 4 places Typical 10.52] Typical 4 places O.414 [10.52] Typical 4 places O.472 [11.99] Max. O.248 [6.30] Max. O.525 [13.34] Amax.
0.045 [1.14] Pad Dimensions Typical, 10 places 0.079 [2.01] 0.045 1.14] Typical 4 places 0.414 [10.52] Ill.66] Reference Only Dimensional Outline 0.248 [6.30] Max. 0.525 [13.34] 0.525 [13.34]
0.472 [11.99] Max. 10 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
0.472 [11.99] Max. 10 0.248 [6.30] Max. 0.525 [13.34]
0.079 V 0.437 [11.10] 0.028 [2.01]
Foot print Diagram

NOTE: Pad layout guidelines per MIL-STD-(printed wiring for electronic equipment).

Tolerances: $xx \pm 0.01$ " [± 0.25 mm]; $xxx \pm 0.005$ " [± 0.12 mm]

The underside of these components contains metal and thus should not come in contact with active circuit traces.

STANDARD ELECTRICAL SPECIFICATIONS						
MODEL	IND. (µH)	IND. TOL.	SCHEMATIC LETTER	DCR MAX. (Ohms)	MAX. RATED* DC CURRENT (Amps)	SATURATING CURRENT** (Amps)
Ungapped Models (A) LPE5047ER151NU LPE5047ER221NU LPE5047ER331NU LPE5047ER471NU LPE5047ER681NU	150 220 330 470 680	±30% ±30% ±30% ±30% ±30%	A A A A	0.20 0.24 0.29 0.35 0.42	0.79 0.72 0.65 0.59 0.54	N/A N/A N/A N/A N/A
LPE5047ER102NU LPE5047ER152NU LPE5047ER222NU LPE5047ER32NU LPE5047ER472NU LPE5047ER682NU	1000 1500 2200 3300 4700 6800	±30% ±30% ±30% ±30% ±30%	A A A A	0.51 0.63 0.76 1.00 2.24 2.70	0.49 0.44 0.40 0.35 0.24 0.21	N/A N/A N/A N/A N/A
LPE5047ER103NU LPE5047ER153NU LPE5047ER223NU LPE5047ER333NU LPE5047ER473NU LPE5047ER683NU	10 000 15 000 22 000 33 000 47 000 68 000	±30% ±30% ±30% ±30% ±30%	A A A A	3.27 6.26 7.58 9.50 18.5 24.1	0.19 0.14 0.13 0.11 0.08 0.07	N/A N/A N/A N/A N/A N/A
Gapped Models (B) LPE5047ER100MG LPE5047ER150MG LPE5047ER220MG LPE5047ER330MG LPE5047ER470MG LPE5047ER680MG	10 15 22 33 47 68	±20% ±20% ±20% ±20% ±20%	B B B C D D	0.03 0.04 0.05 0.09 0.13 0.15	2.29 2.07 1.68 1.35 1.11 1.01	2.690 2.230 1.860 1.540 1.300 1.085
LPE5047ER101MG LPE5047ER151MG LPE5047ER221MG LPE5047ER331MG LPE5047ER471MG LPE5047ER681MG	100 150 220 330 470 680	±20% ±20% ±20% ±20% ±20%	D D E E E	0.24 0.37 0.55 0.85 1.29 1.96	0.81 0.65 0.53 0.43 0.35 0.28	0.900 0.740 0.610 0.500 0.420 0.350
LPE5047ER102MG LPE5047ER152MG LPE5047ER222MG LPE5047ER332MG LPE5047ER472MG	1000 1500 2200 3300 4700	±20 % ±20 % ±20 % ±20 % ±20 %	шшшшш	2.38 3.66 5.47 8.48 13.2	0.26 0.21 0.17 0.14 0.11	0.290 0.240 0.195 0.160 0.135

 $^{^*}$ DC current that will create a maximum temperature rise of 30 °C when applied at + 25 °C ambient. ** DC current that will typically reduce the initial inductance by 20 %

UNGAPPED MODELS: Highest possible inductance with the lowest DCR and highest Q capability. Beneficial in filter, impedance matching and line coupling devices.

GAPPED MODELS: Capable of handling large amounts of DC current, tighter inductance tolerance with better temperature stability than ungapped models. Beneficial in DC to DC converters or other circuits carrying DC currents or requiring inductance stability over a temperature range.

DESC	RIPT	ION					
LPE MODEL	5047 SIZE	1000 μH INDUCTANCE VALUE	± 30 % INDUCTANCE TOLER.	A Ance core pa	ER ACKAGE CODE	e2 JEDEC LEAD (Pb)-FREE STANDAR	Qĩ
GLOB	AL P	ART NUMBER					
	L	P E 5	0 4 7	PACKAGE CODE	INDUCTAN VALUE		

NOTE Series is also available with SnPb terminations by using package code RY for tape and reel (in place of ER) or SM for bulk (in place of EB).

Vishay Dale

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SC	HEMATIC (TOP	VIEW)			
	Schematic A	Schematic B	Schematic C	Schematic D	Schematic E
6 o	5	6 o o 5	6 ⊚ © 5	6 € ⊙ 5	6
7 o		7 0	7 0 0 4	7 ⊚	7 ⊚
8 0	> 03	8 0	8 0 3	8 0 0 3	8 0 0 3
9 о	> 02	9 0	9 0 2	9 0 2	9 0 0 2
10 o	11 000 1	10 0	10 0	10 0	6

NOTE: Schematic A is for Ungapped LPE Series

ENVIRONMENTAL PERFORMANCE				
TEST	CONDITIONS			
Thermal Cycling	Withstands - 55 °C to + 125 °C			
Operating Temperature	- 55 °C to + 125 °C*			
High Humidity	85 %			
Soldering Heat	Tested to + 230 °C			
Mechanical Shock	Per MIL-STD-202, Method 213 (100G)			
Vibration	Per MIL-STD-202, Method 204 (20G)			
Solderability	Per industry standards			

^{*} Must be checked in end use application

PART MARKING
- Vishay Dale
- Date code
- Marking code (Suffix of model #)
- Pin 1 indicator

PACKAGING

TAPE SPECIFICATIONS:

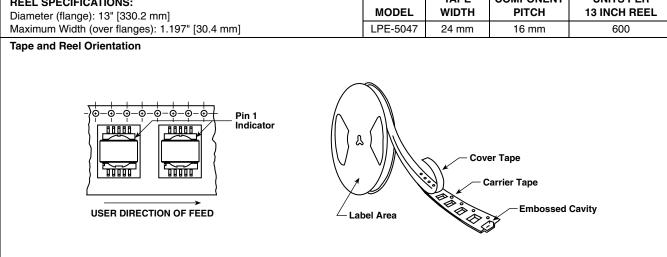
Carrier Tape Type: Conductive Cover Tape Type: Anti-static

Cover Tape Adhesion to Carrier: 40 ± 30 grams

REEL SPECIFICATIONS:

STANDARDS: All embossed carrier tape packaging will be accomplished in compliance with latest revision of EIA-481 "Taping of Surface Mount Components for Automatic Placement".

COMPONENT **UNITS PER TAPE MODEL** 13 INCH REEL **WIDTH PITCH**



NOTE: Top view shown with cover tape removed

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