



Surface Mount Transformers/Inductors, Gapped and Ungapped, Custom Configurations Available



FEATURES

 Material categorization: for definitions of compliance please www.vishay.com/doc?99912



ELECTRICAL SPECIFICATIONS

RoHS (multiple winds are connected in parallel) COMPLIANT Inductance Range: 10 µH to 330 000 µH, measured at 0.10 V_{RMS} at 10 kHz without DC current, using an HP 4263A or HP 4284A impedance analyzer **DC Resistance Range:** 0.03 Ω to 53.7 Ω , measured at +25 °C ± 5 °C

Rated Current Range: 3.00 A to 0.06 A

Dielectric Withstanding Voltage: 500 V_{RMS}, 60 Hz, 5 s

MODEL	IND. (µH)	IND. TOL.	SCHEMATIC LETTER	DCR MAX. (Ω)	MAX. RATED DC CURRENT (A) (1)	SATURATING CURRENT (A) (2)
LPE6562ER221NU	220	± 30 %	A	0.28	0.90	N/A
LPE6562ER331NU	330	± 30 %	Ä	0.34	0.81	N/A
LPE6562ER471NU	470	± 30 %	Ä	0.40	0.74	N/A
LPE6562ER681NU	680	± 30 %	Ä	0.48	0.67	N/A
LPE6562ER102NU	1000	± 30 %	Ä	0.59	0.61	N/A
LPE6562ER152NU	1500	± 30 %	Ä	0.72	0.55	N/A
LPE6562ER222NU	2200	± 30 %	Ä	0.87	0.50	N/A
LPE6562ER332NU	3300	± 30 %	Ä	1.07	0.45	N/A
LPE6562ER472NU	4700	± 30 %	Ä	1.27	0.41	N/A
PE6562ER682NU	6800	± 30 %	Ä	1.53	0.38	N/A
PE6562ER103NU	10 000	± 30 %	Ä	1.86	0.34	N/A
PE6562ER153NU	15 000	± 30 %	Ä	2.27	0.31	N/A
PE6562ER223NU	22 000	± 30 %	Ä	8.67	0.16	N/A
PE6562ER333NU	33 000	± 30 %	Ä	10.6	0.14	N/A
PE6562ER473NU	47 000	± 30 %	Ä	12.7	0.14	N/A
PE6562ER683NU	68 000	± 30 %	Â	15.2	0.13	N/A
PE6562ER104NU	100 000	± 30 %	Ä	18.5	0.12	N/A
PE6562ER154NU	150 000	± 30 %	Ä	37.7	0.08	N/A N/A
PE6562ER224NU	220 000	± 30 %	Ä	45.6	0.08 0.07	N/A N/A
PE6562ER334NU	330 000	± 30 % ± 30 %	Ä	53.7	0.07	N/A N/A
PE6562ER100MG	10	± 30 % ± 20 %	B	0.03	3.09	5.055
PE6562ER150MG	15	± 20 % ± 20 %		0.03	2.79	4.160
PE6562ER220MG	22	± 20 % ± 20 %		0.04	2.79 2.26	4.160 3.460
		± 20 % ± 20 %	B B B	0.05		
PE6562ER330MG	33				1.81	2.840
PE6562ER470MG	47	± 20 %	D	0.12	1.48 1.20	2.390
PE6562ER680MG	68	± 20 %	C	0.19		1.990
PE6562ER101MG	100	± 20 %	D	0.29	0.98	1.650
PE6562ER151MG	150	± 20 %	<u> </u>	0.45	0.78	1.350
PE6562ER221MG	220	± 20 %	<u> </u>	0.54	0.71	1.115
PE6562ER331MG	330	± 20 %	<u> </u>	0.84	0.57	0.912
PE6562ER471MG	470	± 20 %	E E E E	1.24	0.47	0.765
PE6562ER681MG	680	± 20 %		1.89	0.38	0.637
PE6562ER102MG	1000	± 20 %	E E E	2.91	0.31	0.526
PE6562ER152MG	1500	± 20 %		4.50	0.25	0.430
PE6562ER222MG	2200	± 20 %		6.90	0.20	0.355
PE6562ER332MG	3300	± 20 %		10.4	0.16	0.290
PE6562ER472MG	4700	± 20 %	E	15.7	0.13	0.243

Notes

- (1) DC current that will create a maximum temperature rise of 30 °C when applied at +25 °C ambient.
 (2) DC current that will typically reduce the initial inductance by 20 %.

 UNGAPPED MODELS: Highest possible inductance with the lowest DCR and highest Q capability. Beneficial in filter, impedance matching and line coupling devices

GAPPED MODELS: Capable of handling large amounts of DC current, tighter inductance tolerance with better temperature stability than ungapped models. Beneficial in DC/DC converters or other circuits carrying DC currents or requiring inductance stability over a temperature range.

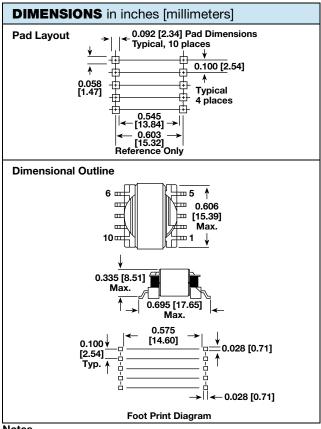
DESCRIPTION															
LPE	6562	1000 μH	ł		± 30	%		Α	El	R			e2		
MODEL	SIZE	INDUCTANCE	VALUE	INDUCTA	NCE	TOLERAN	ICE C	ORE	PACKAG	E CODE	JEDEC® I	LEAD (P	b)-FREE	STAND	ARD
GLOE	BAL P	ART NUMB	ER												
	L	РЕ	6	5	6	2	E	R		1 0	2	N	١	T	
P	RODUC	CT FAMILY	<u> </u>	SIZE			PACKA	GE CO	DDE IN	DUCTAN	CE VALUE	TC	DL.	CORE	

Note

Series is also available with SnPb terminations by using package code RY for tape and reel (in place of ER) or SM for bulk (in place of EB).



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Notes

- Pad layout guidelines per MIL-STD-275E (printed wiring for electronic equipment).
- Tolerances: $xx \pm 0.01$ " [± 0.25 mm]; $xxx \pm 0.005$ " [± 0.12 mm].
- The underside of these components contains metal and thus should not come in contact with active circuit traces.

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Pin 1 Indicator

SCHEMATIC (top view)							
	Schematic A	Schen	natic B	Schematic C			
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7 ©		7 6	~~~° ₀ 4	7 🗨	= ° 4		
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Note

· Schematic A is for ungapped LPE series.

ENVIRONMENTAL PERFORMANCE					
TEST	CONDITIONS				
Thermal Cycling	Withstands -55 °C to +125 °C				
Operating Temperature	-55 °C to +125 °C ⁽¹⁾				
High Humidity	85 %				
Soldering Heat	Tested to +230 °C				
Mechanical Shock	Per MIL-STD-202, method 213 (100G)				
Vibration	Per MIL-STD-202, method 204 (20G)				
Solderability	Per industry standards				

Note

(1) Must be checked in end use application.

PART MARKING

- Vishay Dale
- Date code

MODEL

LPE-6562

- Marking code (suffix of model #)

TAPE

WIDTH

32 mm

- Pin 1 indicator

PACKAGING

TAPE SPECIFICATIONS:

Carrier Tape Type: Conductive Cover Tape Type: Anti-static

Cover Tape Adhesion to Carrier: 40 g ± 30 g

REEL SPECIFICATIONS:

Tape and Reel Orientation

Diameter (flange): 13" [330.2 mm] Maximum Width (over flanges): 1.197" [30.4 mm]

STANDARDS: All embossed carrier tape packaging will be accomplished in compliance with latest revision of EIA-481 "Taping of Surface Mount Components for Automatic Placement"

COMPONENT PITCH

20 mm

UNITS PER 13"

REEL

300

۵			pe er Tape
Label Are	a		Embossed Cavity

Note

Top view shown with cover tape removed.

USER DIRECTION OF FEED



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