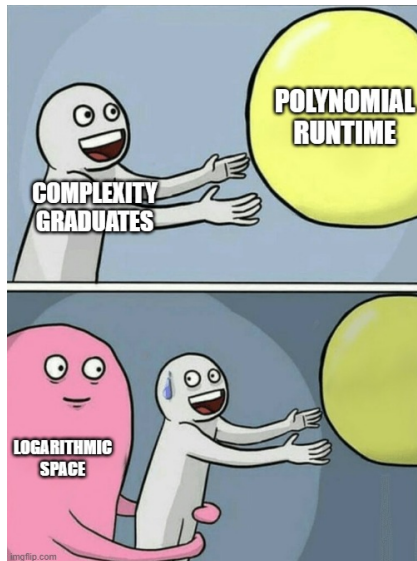


Lecture 13: Log space



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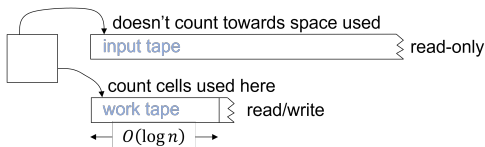
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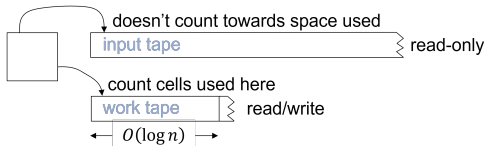
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Model: a 2-tape TM with a read-only tape for the input and a RW-tape for computation.



Credit: Sipser

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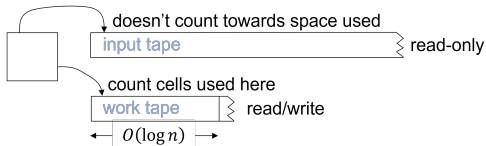


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Definition

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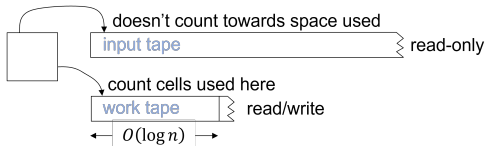


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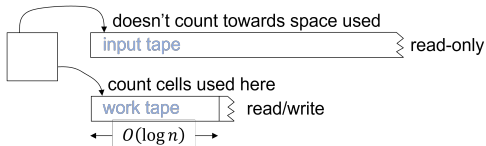
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Another example: $\{ww^R \mid w \in \Sigma^*\} \in \mathbf{L}$.

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But how is this implemented?

Our input size is $n = O(qw)$.

We are not allowed to keep w bits (for M) in memory!

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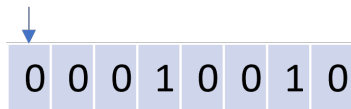
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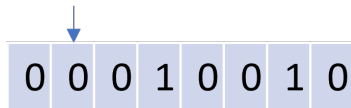


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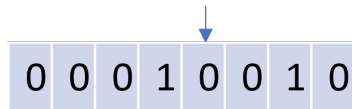
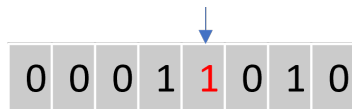


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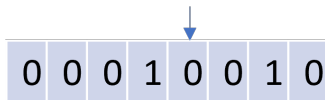
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$O(\log n)$ bits



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Notice that ℓ and v both require $O(\log n)$ bits and thus this can be implemented on an L machine. □

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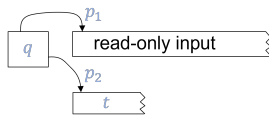
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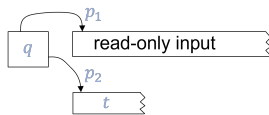
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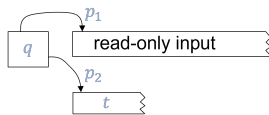
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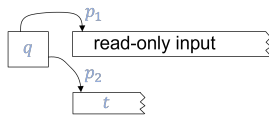
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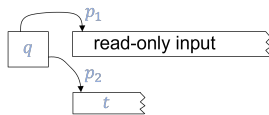
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- $t \in \Gamma^{O(\log n)}$ is the contents of the RW tape.

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$$|Q| \times n \times \log n \times |\Gamma|^{O(\log n)} = n^{O(1)}.$$



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Proof.

Immediate from Savitch's theorem. □

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To simulate a general $SPACE(\log^2 n)$ machine we need $2^{\Omega(\log^2 n)} = n^{\Omega(\log n)}$ time, which is not polynomial.

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We then run a BFS to check if c_Y is reachable from c_0 and terminate accordingly. □

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- It was shown that undirected STCON is in L .
- There are also P -complete problems (using L -reductions) such as max-flow.

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Answer on Mentimeter:



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Next time: hierarchy theorems and EXPTIME-completeness.