Semester: 3rd

Course Title: Compiler Design

Course Code: INF3036

Class	Unit	Topics
Class 1	Unit I: Introduction	What is Compiler?, Phases of Compiler
Class 2	Unit I: Introduction	Overview of working of a compiler
Class 3	Unit III: Syntax Analysis	Grammar representation, Derivation and parse tree
Class 4	Unit III: Syntax Analysis	Ambiguity and Possible elimination
Class 5	Unit III: Syntax Analysis	Top Down Parsing
Class 6	Unit III: Syntax Analysis	Recursive Descent and Predictive top down parsing
Class 7	Unit III: Syntax Analysis	Elimination of left recursion, Adopting Translation scheme by applying left recursion
Class 8	Unit III: Syntax Analysis	Left recursion elimination technique in productions containing semantic actions
Class 9	Unit II: Lexical Analysis	Regular expression, Regular Definitions
Class 10	Unit II: Lexical Analysis	Extensions of regular expressions
Class 11	Unit II: Lexical Analysis	Recognition of tokens
Class 12	Unit II: Lexical Analysis	Structure of Lex program
Class13	Unit II: Lexical Analysis	NFA, DFA, Conversion from NFA to DFA
Class 14	Unit II: Lexical Analysis	Conversion from NFA with null transition into NFA without null transition

Name: Dr Irani Hazarika Signature:

Semester: 3rd

Course Title: Compiler Design

Course Code: INF3036

Class	Unit	Topics
Class 15	Unit II: Lexical Analysis	Conversion from NFA with null transition into DFA
Class 16	Unit II: Lexical Analysis	Minimization of DFA
Class 17	Unit III: Syntax Analysis	Example of elimination of left recursion, left factoring, left factoring for dangling else grammar
Class 18	Unit III: Syntax Analysis	FIRST and FOLLOW of a grammar G
Class 19	Unit III: Syntax Analysis	LL(1) grammar, Construction of predictive parsing table for LL(1) grammar
Class 20	Unit III: Syntax Analysis	Parsing table for Dangling-else problem (non-LL(1) grammar)
Class 21	Unit III: Syntax Analysis	Moves of a predictive parser on an input
Class 22	Unit III: Syntax Analysis	Error recovery in predictive parser
Class 23	Unit III: Syntax Analysis	Bottom up parsing, Reductions, Handle Pruning
Class 24	Unit III: Syntax Analysis	Shift Reduce Parsing, Conflicts during Shift Reduce Parsing
Class 25	Unit III: Syntax Analysis	LR(0) Automaton
Class 26	Unit III: Syntax Analysis	Construction of LR(0) parsing table
Class 27	Unit III: Syntax Analysis	Construction of SLR Parsing table
Class 28	Unit III: Syntax Analysis	Canonical-LR

Name: Dr Irani Hazarika Signature:

Semester: 3rd

Course Title: Compiler Design

Course Code: INF3036

Class	Unit	Topics
Class 29	Unit III:	
	Syntax	LALR parser
	Analysis	
	Unit III: Syntax	Dangling -Else Ambiguity
	Analysis	Danging -Lise Amorganty
	Unit III:	
Class 31	Syntax	Yacc
	Analysis	
Class 32	Unit III:	
Class 32	Syntax	Yacc
	Analysis	
Class 33	Unit IV:	
	Code	Symbol table contents
	generation	
Class 34	Unit IV: Code	Type checking
	generation	Type checking
	Unit IV:	
Class 35	Code	Syntax directed translation
	generation	
	Unit IV:	
Class 36	Code	Forms of intermediate codes, Abstract Syntax Trees
Class 30	generation	
	Unit IV:	Directed Acyclic Graph
Class 37	Code	
	generation	
	Unit IV:	T1 11 1.
Class 38	Code generation	Three address code
	Unit IV:	
	Code	Intermediate code generation for different language constructs like arrays
Class 39	generation	same some generalism for anticione language construction like allays
C1 40	Unit IV:	Intermediate code generation for different language constructs like boolean expression
Class 40	Code	
	generation	
Class 41	Unit IV:	Intermediate code generation for different language constructs like if, ifelse, while, ca or switch, function call
	Code	
	generation	
Class 42	Unit V:	D
	Code	Basic blocks
	Optimisation	

Name: Dr Irani Hazarika Signature:

Semester: 3rd

Course Title: Compiler Design

Course Code: INF3036

Class	Unit	Topics
Class 43	Unit V:	
	Code	Common sub-expression elimination
	Optimisation	
Class 44	Unit V:	
Class 44	Code	Variable propagation
	Optimisation	
Class 45	Unit V:	
Class 43	Code	Code motion
	Optimisation	
Class 46	Unit V:	
	Code	Strength reduction, Elimination of dead code
Class 10	Optimisation	
	Unit V:	Loop optimization
Class 47	Code	
Class 17	Optimisation	
Class 48		
Class 49		
Class 50		
C1a55 30		

Name: Dr Irani Hazarika Signature: