

INSTRUCTION SET ARCHITECTURE

	[15:12]	[11:9]	[8:6]	[5:3]	[2:0]
AND	0010	SRC1	SRC2	DST	000
ADD	0011	SRC1	SRC2	DST	000
ANDI	0110	SRC1	DST	Imm6	
ADDI	0101	SRC1	DST	Imm6	
CMP	0111	OP1	OP2	000000	
JUMP	1000	Address12			
JE	1001	Address12			
JA	1010	Address12			
JB	1011	Address12			
JBE	1100	Address12			
JAE	1101	Address12			
LD	1110	DST	Address9		
ST	1111	SRC	Address9		

150117007 – Edanur Öztürk

150117044 – Sueda Bilen

150119841 – Zehra Kuru

150517059 – Özge Saltan