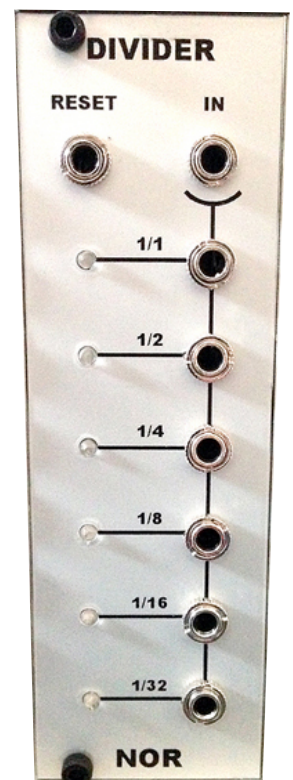


mix of two very similar schematics:  
Rönnberg and Kristian Blasol (MiaW)



The diagram shows a TO-92 package on the left, which is a small black plastic component with three leads. The top of the package is labeled "TO-92 Package". The top surface of the package is marked with "BC107" and "NPN". The leads are labeled "1 Collector", "2 Base", and "3 Emitter". To the right of the package is a circular cross-section diagram of the transistor. It shows a central "Base" region (indicated by a horizontal line) flanked by "Collector" and "Emitter" regions. Arrows indicate the flow of current from the collector to the emitter through the base.

Pin diagram of the 4024 14-pin CMOS divider. The chip is shown with pins 1 through 14. Pin 1 is labeled 'clock', pin 2 is 'reset', pin 3 is 'Q7 (÷128)', pin 4 is 'Q6 (÷64)', pin 5 is 'Q5 (÷32)', pin 6 is 'Q4 (÷16)', pin 7 is '0V', pin 8 is 'NC', pin 9 is 'Q3 (÷8)', pin 10 is 'NC', pin 11 is 'Q2 (÷4)', pin 12 is 'Q1 (÷2)', pin 13 is 'NC', and pin 14 is '+3 to +15V'. The number '4024' is printed in the center of the chip.

capacitors  
2u2  
1n  
47n x2  
100n  
220n  
(6pcs)

