1 Errata

These errata cover the following items associated with the PG1.0 BQ79600 engineering samples:

KNOWN BUGS:

1. **SPI interface:**
   - **Description:** BQ79600 SPI interface is not stable, it experiences high error rate.
   - **Work around:** Avoid using SPI interface in PG1.0, use UART interface.

2. **XFMR type:**
   - **Description:** For PG1.0 material of BQ7961x and BQ79600, we recommend to use the same PN for the Transformer Isolation component on both BMU and CMC side. The supported inductance value range for this silicon version is 500uH min and 1400uH max.
   - **Work Around:** Sumida CEP-99 (L=618uH) was successfully used to test communication amongst the devices. Alternatively, Pulse HMU1236NL (L=500uH) was successfully used to test communication amongst devices.

     For the final silicon revision of both devices, we continue to recommend to use the same PN for the Transformer Isolation component on both BMU and CMC sides, given that communication link is more robust with symmetrical interface.

     Please follow our design spec guideline w.r.t. the inductance value when choosing the type of transformer.

3. **Communication with BQ79606A (Cap isolation) and BQ79600 uses CEP-99:**
   - **Description:** Moderate communication error can be observed during reads or writes. This should trigger a communication fault or response timeout.
   - **Work Around:** If communication error is observed, clear the communication fault and send again the command that triggered the error.
   - **Note:** BQ79606A using XFMR and Cap+ choke have not been tested yet.

2 Revision History

REVISION	DATE	COMMENT	EDITOR
1.0	09/27/2019	Initial Revision	Spencer Hu
1.0.1	10/24/2019	Updated item 2	Spencer Hu