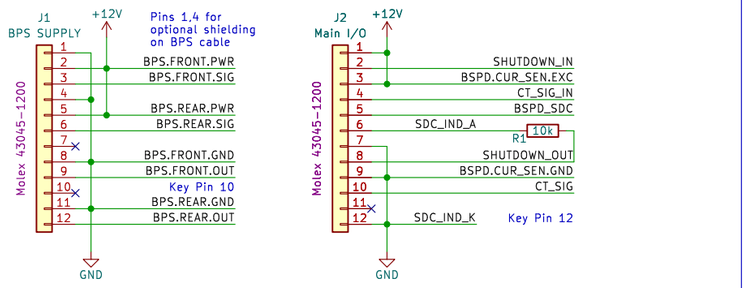
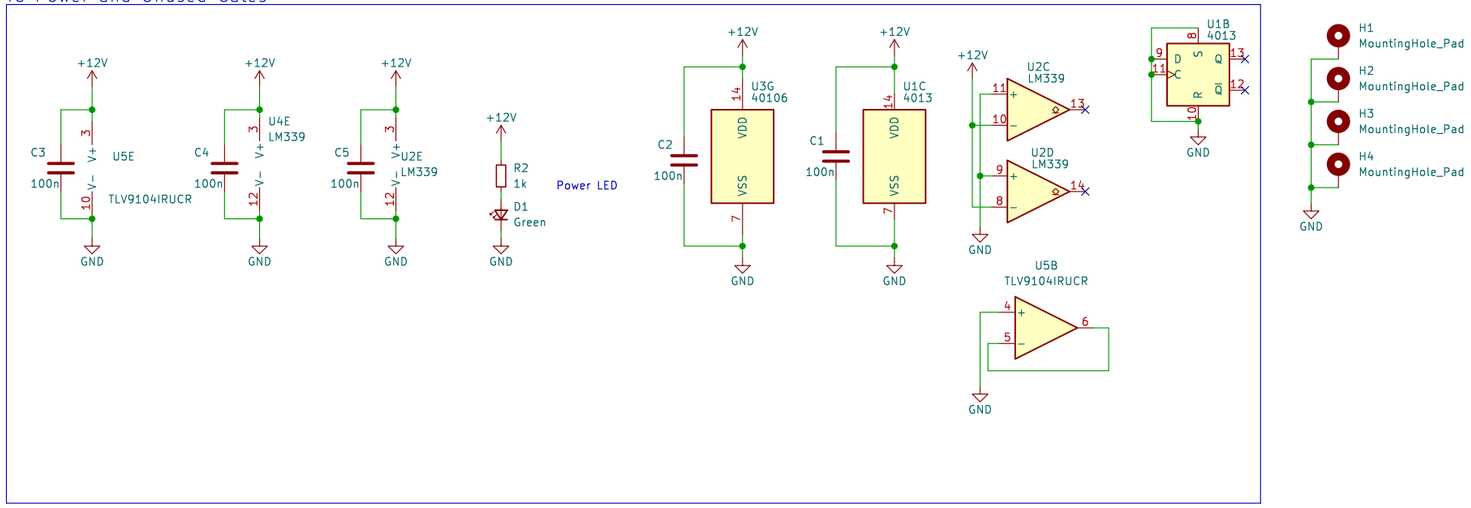
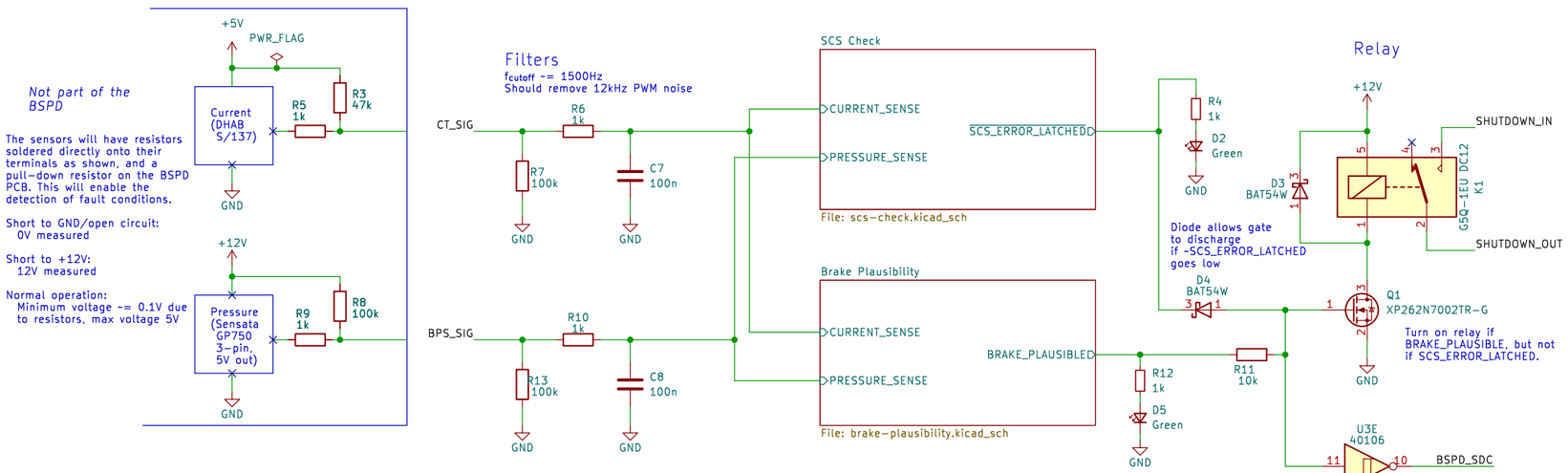
Connectors, I/O Conditioning



IC Power and Unused Gates

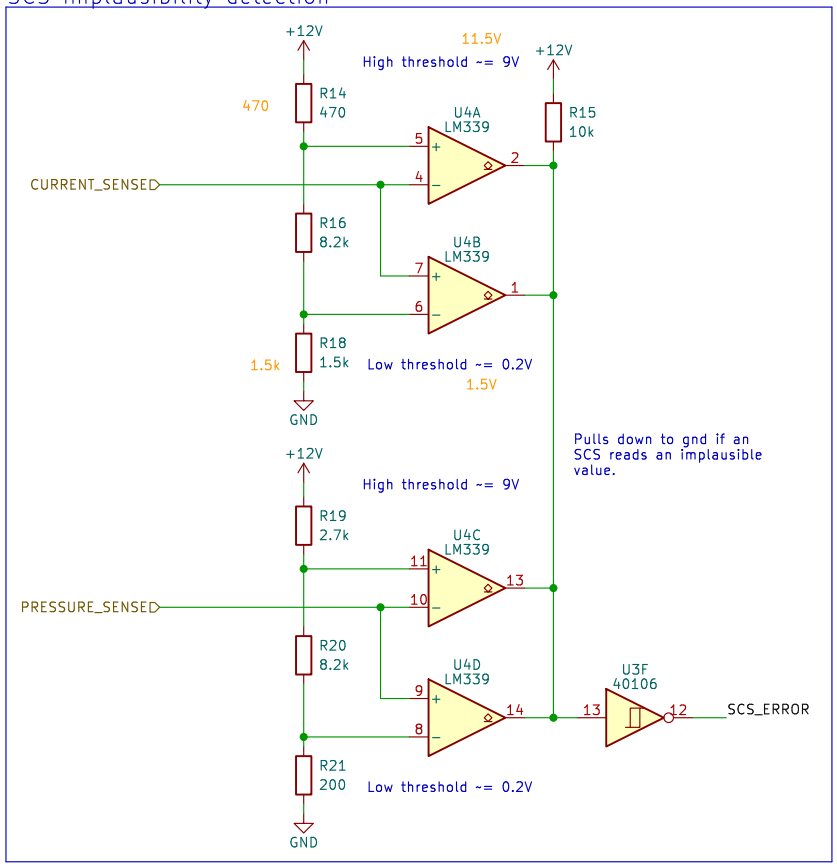




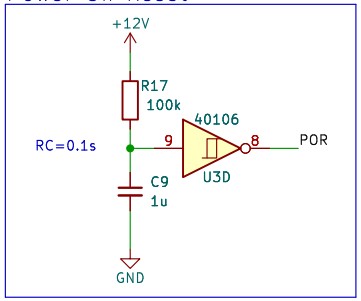
Vehicle: STAG 12	
Drawn By: Ray Wang, Joe Pater	
Checked By: Adam Eastman	
CAD Part:	
SUFST – Southampton University Formula Student Team	
Sheet: Root	
File: BSPD.kicad_sch	
Title: BSPD	
Size: A3	Date: 2026-01-27
KiCad E.D.A. 9.0.4	Id: 1/3

Rev: 1.3.0

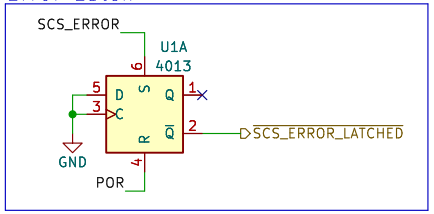
SCS implausibility detection



Power On Reset

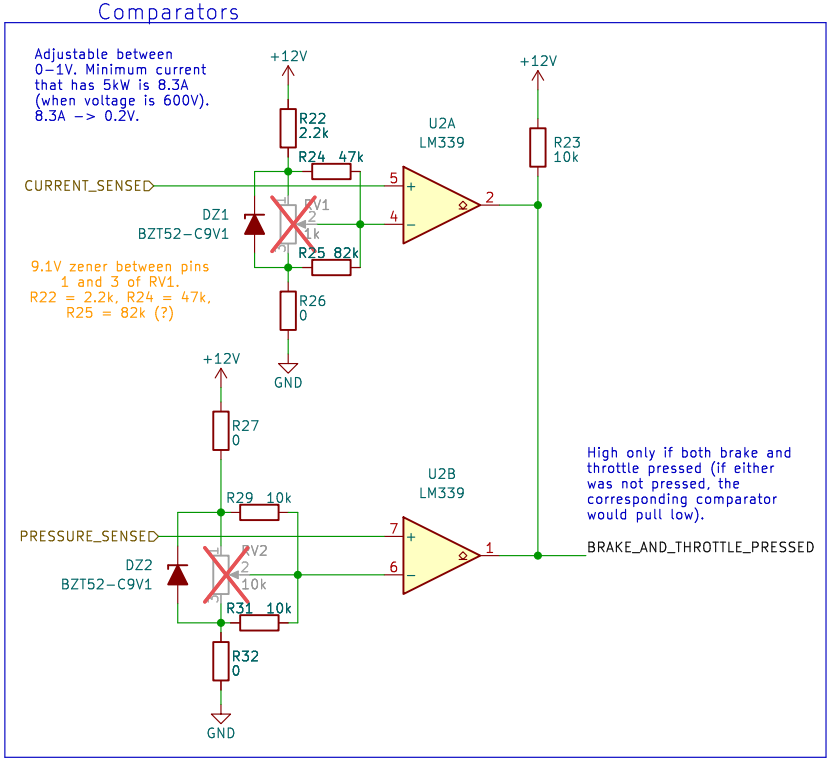


Error Latch

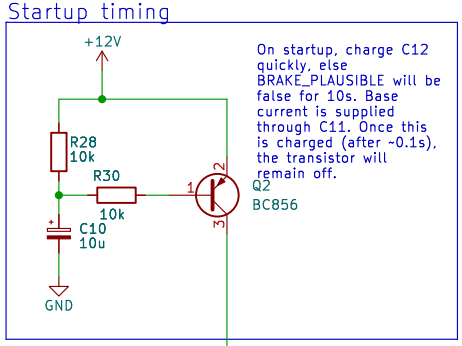


Vehicle: STAG 12		
Drawn By: Ray Wang, Joe Pater		
Checked By: Marek Frodyma, Tim Brewis		
CAD Part:		
SUFST – Southampton University Formula Student Team		
Sheet: SCS Check		
File: scs-check.kicad_sch		
Title: BSPD		
Size: A4	Date: 2025-12-14	**Rev: 1.3.0**
KiCad E.D.A. 9.0.4		Id: 4/3

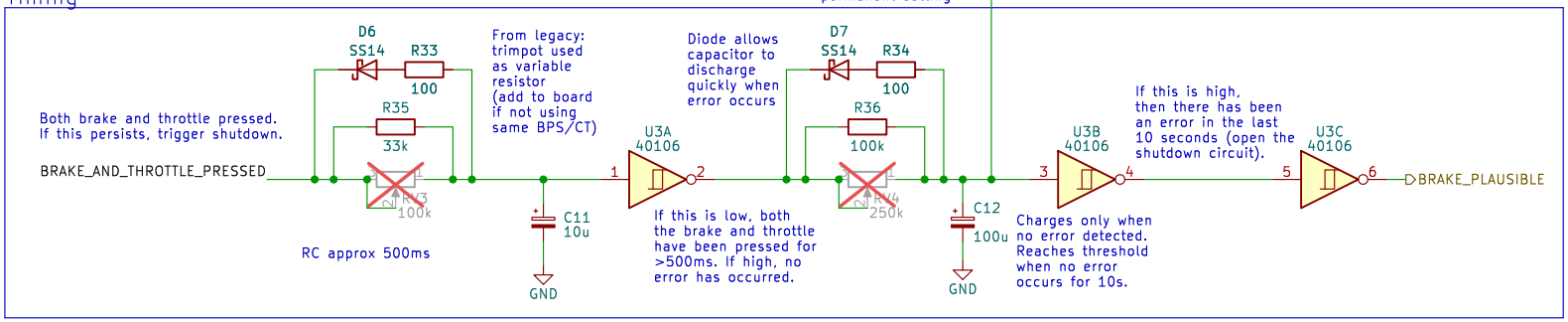
Comparators



Startup timing



Timing



Vehicle: STAG 12
Drawn By: Ray Wang, Joe Pater
Checked By: Marek Frodyma, Tim Brewis
CAD Part:
SUFST – Southampton University Formula Student Team

Sheet: Brake Plausibility
File: brake–plausibility.kicad_sch

Title: BSPD

Size: A4	Date: 2025–12–14	**Rev: 1.3.0**
KiCad E.D.A. 9.0.4		Id: 5/3