

Unit 1:

DESIGN CONCEPTS

I NOTES

① Digital Hardware

Logic circuits are used to build computer hardware and mobile products. These circuits are called digital hardware because of the manner in which it represents and processes information.

② Moore's Law

Gordon Moore, the chairman of Intel predicted that integrated circuit technology was progressing at an astounding rate doubling the number of transistors that could be placed on a chip every 1½ to 2 years.

The advent of ICs made it possible to place a number of transistors and thus an entire circuit on a single chip.

ICs are placed on semiconductor wafers and the wafer is cut to produce individual chips which are then placed in a special type of chip package.

③ Types of Chips

→ STANDARD CHIPS

These realize commonly used logic functions (usually involved in fewer than 100 transistors) and performs simple functions. Its functionality is fixed and cannot be changed.

→ Programmable Logic Device (PLD):-

These contain circuits that can be configured by the user to implement a wide range of different logic circuits. They include a collection of programmable switches which help in configuring the chip in different ways. They can usually be programmed multiple times.

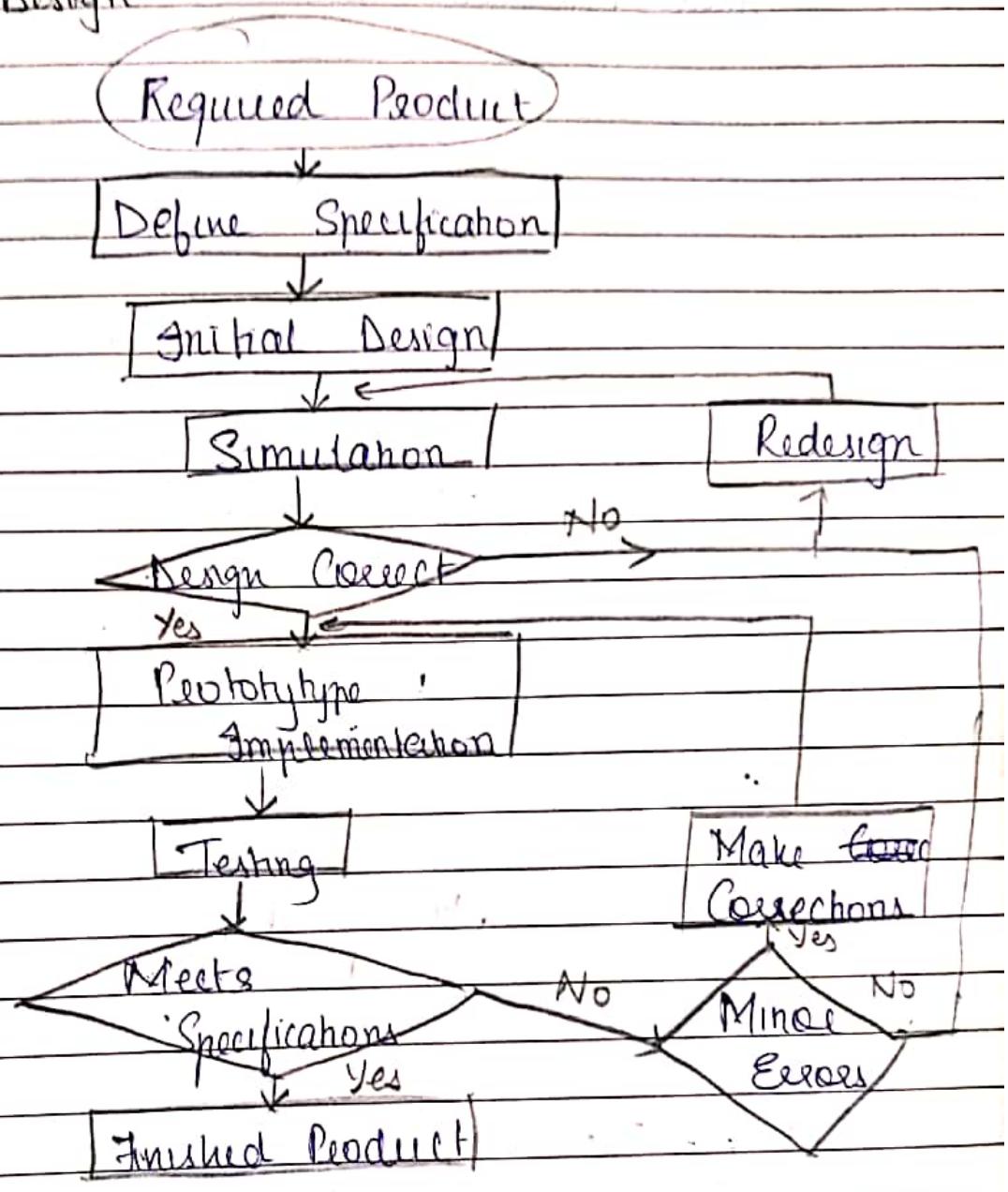
→ Custom Design Chips

Designing a chip from the beginning is also possible i.e. the chip is first designed and then it is fabricated using appropriate technology.

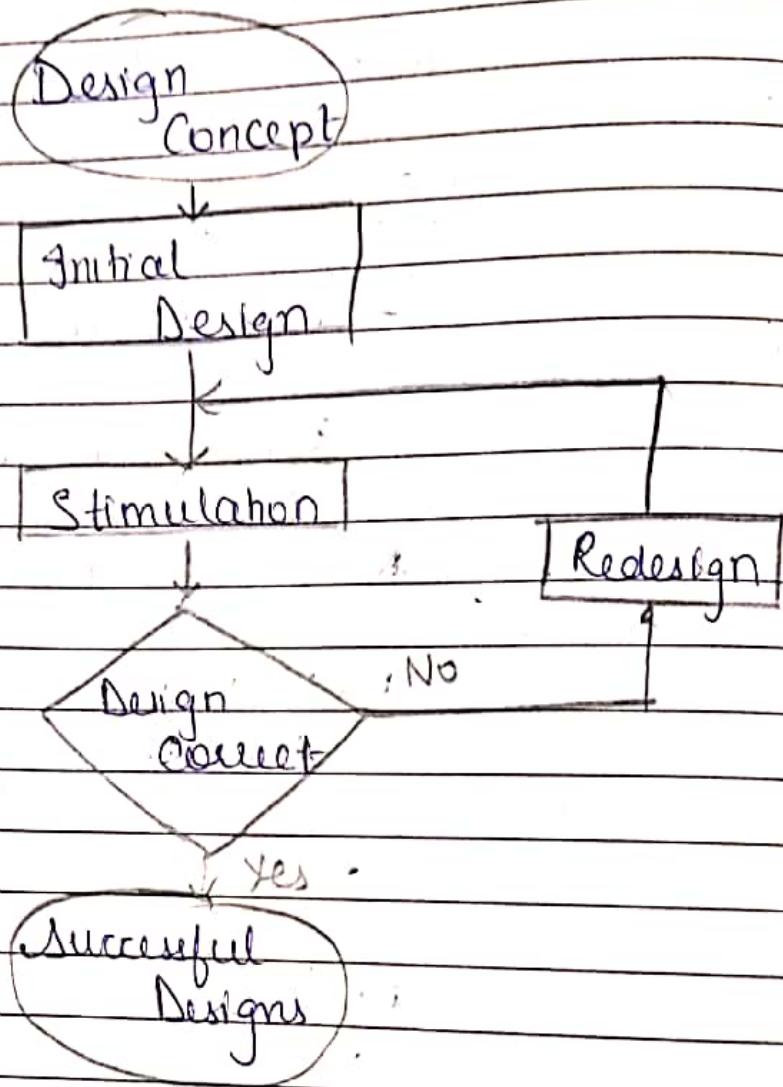
Valuable chip area is saved and speed is also improved due to the absence of switches when compared to PLDs.

These are intended for specific applications and are sometimes called application specific integrated circuits (ASIC).

(4) General Design



⑤ Digital Hardware Design



⑥ Logic Circuits

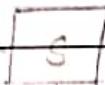
Logic circuits perform digital operations and are usually implemented as electronic circuits where the signal values are restricted. In binary logic circuits they are only two values - zero and one.

① Variables and functions

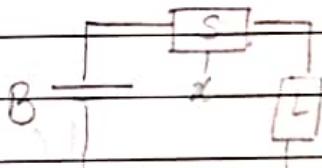
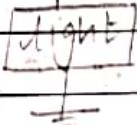
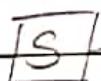
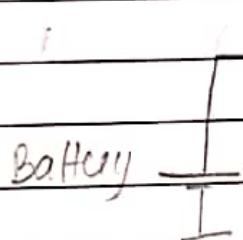
The simplest binary element is a switch that has two stages - on and off. If the switch is controlled by an input variable x then when $x=0$ the switch is open and when $x=1$, the switch is closed.

$$x=0$$

$$x=1$$



$$x$$

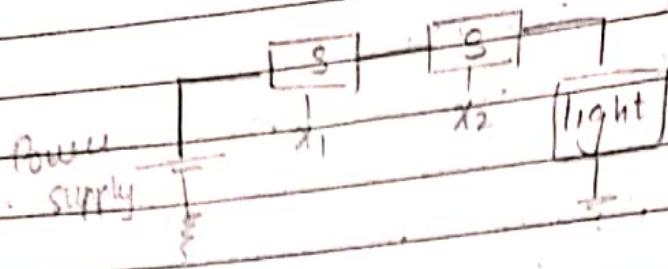


$$B$$

$$L(x) = x$$

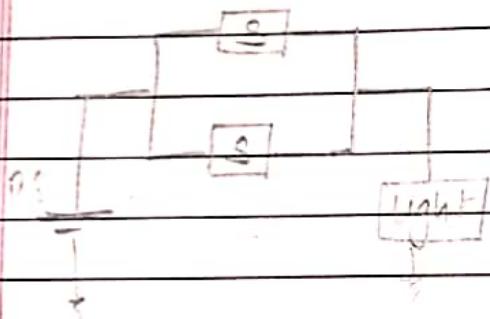
x is an input variable and $L(x)$ is a logic function

- ⑧ Using two switches to control a light
- > SERIES (AND)



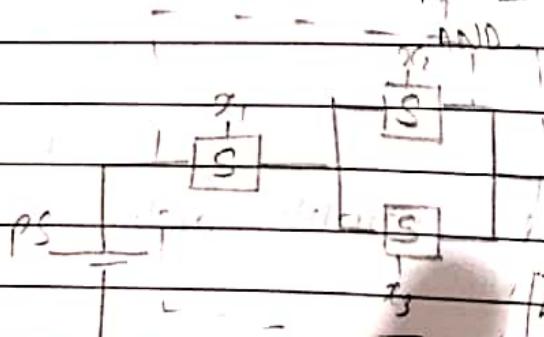
x_1	x_2	$L(x_1, x_2)$
0	0	0
0	1	0
1	0	0
1	1	1

▷ PARALLEL (OR)

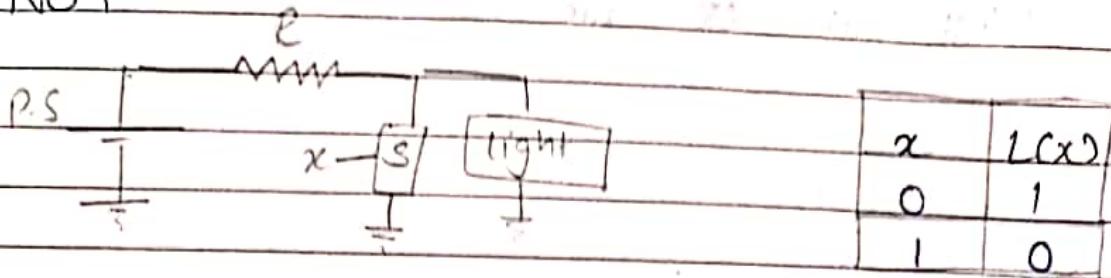


x_1	x_2	$L(x_1, x_2)$
0	0	0
0	1	1
1	0	1
1	1	1

$$L(x_1, x_2, x_3) = x_1 \cdot (x_2 + x_3)$$



⇒ NOT



② Logic Gates and Networks

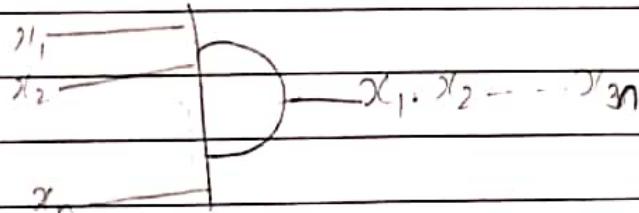
A logic gate has one or more inputs and one output that is a function of its inputs. The logic operations are implemented electronically with transistors resulting in a circuit element that is called a logic gate.

AND

2 input AND gate

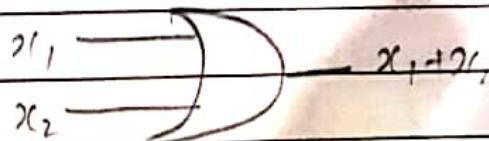


Multi-input AND gate

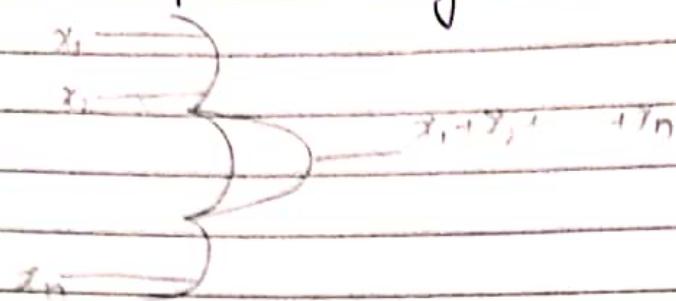


⇒ OR

2 input OR gate



Mult-input OR gate

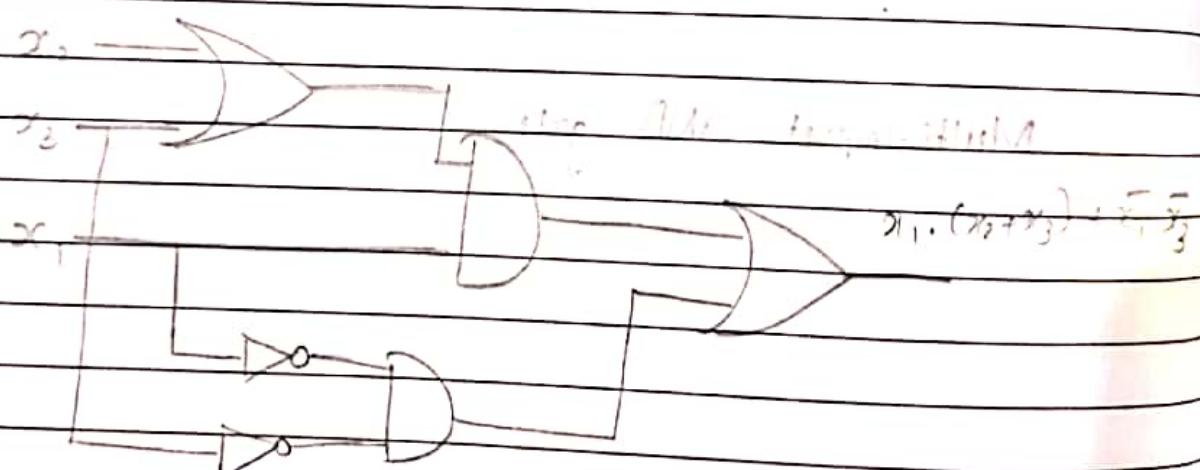


⇒ NOT

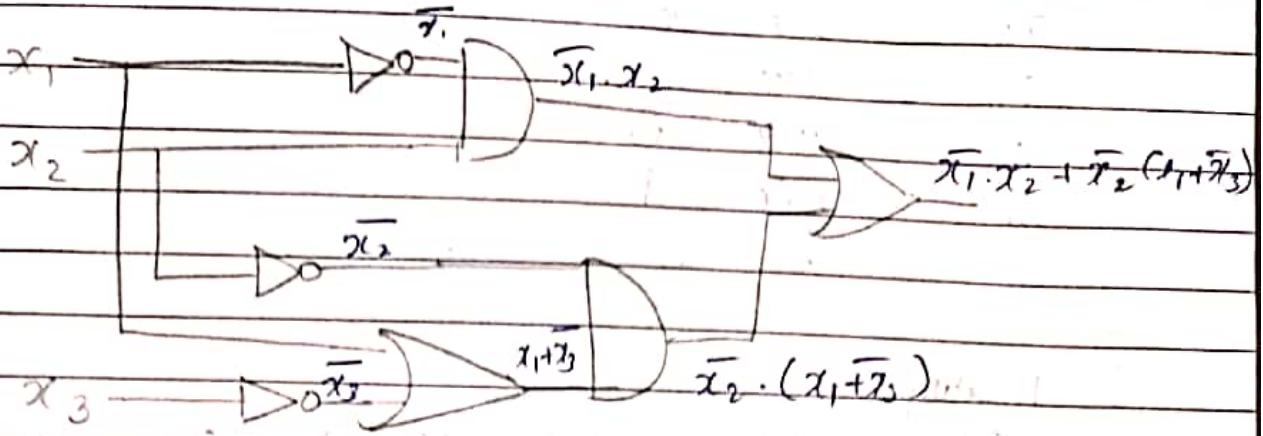


OR, AND and NOT gates are known as basic gates

Q1. Implement $x_1 \cdot (x_2 + x_3) + \bar{x}_1 \cdot \bar{x}_3$



Q2. If $f(x_1, x_2, x_3) = \bar{x}_1 \cdot x_2 + \bar{x}_2 \cdot (x_1 + \bar{x}_3)$



I NOTES

① A network of gates is called a logic ~~gate~~ network.

② Logic Network : duality

George Boole gave a scheme for logical thought and reasoning which was further refined and developed into Boolean algebra.

► AXIOMS OF BOOLEAN ALGEBRA

a. 1a $0 \cdot 0 = 0$

b. 1b $1 + 1 = 1$

c. 2a $1 \cdot 1 = 1$

d. 2b $0 + 0 = 0$

e. 3a $0 \cdot 1 = 1 \cdot 0 = 0$

f. 3b $0 + 1 = 1 + 0 = 1$

g. 4a If $x=0$ then $\bar{x}=1$

h. 4b If $x=1$ then $\bar{x}=0$

► SINGLE VARIABLE THEOREMS

a. 5a $x \cdot 0 = 0$

b. 5b $x + 1 = 1$

c. 6a $x \cdot 1 = x$

d. 6b $x + 0 = x$

$$7a \quad x \cdot 1 = x$$

$$7b \quad x + x = x$$

$$8a \quad x \cdot \bar{x} = 0$$

$$8b \quad x \cdot \bar{x} = 1$$

$$9 \quad \overline{\bar{x}} = x$$

QDP

③ Duality

The axioms above are in pair. Given a logic expression, its dual is obtained by replacing all plus operator with dot operators and vice versa and replacing all zeroes by ones and vice versa.

The dual of any true statement in Boolean algebra is also true. This reflects the principle of duality. Duality implies that at least 2 different ways exist to express any given logic expression.

2 AND 3 VARIABLE PROPERTIES

$$a \quad 10a \quad x \cdot y = y \cdot x \quad \left. \begin{array}{l} \text{Commutative Property} \\ x + y = y + x \end{array} \right\}$$

$$b \quad 10b \quad x + y = y + x \quad \left. \begin{array}{l} \text{Associative Property} \\ x + (y + z) = (x + y) + z \end{array} \right\}$$

$$c \quad 11a \quad x \cdot (y \cdot z) = (x \cdot y) \cdot z \quad \left. \begin{array}{l} \text{Associative Property} \\ x + (y + z) = (x + y) + z \end{array} \right\}$$

$$d \quad 11b \quad x + (y + z) = (x + y) + z \quad \left. \begin{array}{l} \text{Distributive Property} \\ x \cdot (y + z) = x \cdot y + x \cdot z \end{array} \right\}$$

$$e \quad 12a \quad x \cdot (y + z) = x \cdot y + x \cdot z \quad \left. \begin{array}{l} \text{Distributive Property} \\ x + (y \cdot z) = (x + y) \cdot (x + z) \end{array} \right\}$$

$$f \quad 12b \quad x + (y \cdot z) = (x + y) \cdot (x + z) \quad \left. \begin{array}{l} \text{Distributive Property} \\ x \cdot (y + z) = x \cdot y + x \cdot z \end{array} \right\}$$

$$g \quad 13a \quad x + x \cdot y = x \quad \left. \begin{array}{l} \text{Absorption} \\ x \cdot (x + y) = x \end{array} \right\}$$

$$h \quad 13b \quad x \cdot y + x \cdot \bar{y} = x \quad \left. \begin{array}{l} \text{Combining} \\ (x + y) \cdot (x + \bar{y}) = x \end{array} \right\}$$

$$i \quad 14a \quad \bar{x} \cdot \bar{y} = \bar{x} + \bar{y} \quad \left. \begin{array}{l} \text{De Morgan's Theorem} \\ \bar{x} + y = \bar{x} \cdot \bar{y} \end{array} \right\}$$

$$j \quad 14b \quad \bar{x} + y = \bar{x} \cdot \bar{y} \quad \left. \begin{array}{l} \text{De Morgan's Theorem} \\ \bar{x} + y = \bar{x} \cdot \bar{y} \end{array} \right\}$$

$$k \quad 15a \quad x \cdot \bar{x} = 0$$

$$l \quad 15b \quad \bar{x} + \bar{x} = 1$$

$$m \quad 16a \quad x + \bar{x}y = x + y$$

H 16b $x \cdot (x+y) = x, y$

O 17a $xy + yz + zx = xy + zx$

P 17b $(x \cdot y) \cdot (y+z) \cdot (x+z) : (x+y) \cdot (x+z)$

Commutative
Theorem

i) Proof of Theorem by Perfect Induction

> 15a $\overline{x \cdot y} = \overline{x} + \overline{y}$

x	y	$\overline{x \cdot y}$	$\overline{x} + \overline{y}$
0	0	1	1
0	1	1	1
1	0	1	1
1	1	0	0

ii) 17a $x \cdot y = y \cdot x \quad xy + yz + zx = xy + zx$

x	y	z	LHS	RHS
0	0	0	0	0
0	0	1	0	1
0	1	0	0	0
0	1	1	1	1
1	0	0	0	0
1	0	1	0	0
1	1	0	1	1
1	1	1	1	1

iii) 14 b $(x+y) \cdot (x+\bar{y}) = x$

LHS: $(x+y)(x+\bar{y}) = x \cdot x + y \cdot x + x \cdot \bar{y} + y \cdot \bar{y}$
 $= x + x \cdot y + x \cdot \bar{y} + 0$
 $= x(1+y+\bar{y})$
 $= x(1) = x = \text{RHS}$

(Distributive)

$$Q3. x_1\bar{x}_2 + x_2x_3 + \bar{x}_2x_3 = (x_1 + \bar{x}_2 + x_3)(x_1 + x_2 + \bar{x}_3) \quad (\bar{x}_1 + \bar{x}_2 + \bar{x}_3)$$

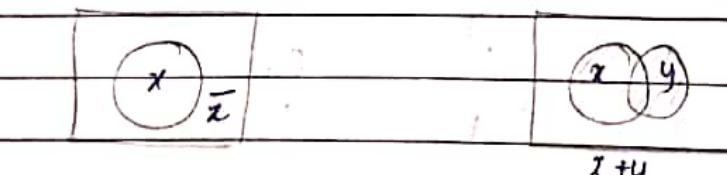
RHS: $(x_1 + \bar{x}_2 + x_3)(x_1 + \bar{x}_3)$ (Combining theorem)
 $= x_1x_1 + x_1\bar{x}_3 + \bar{x}_2x_1 + \bar{x}_2\bar{x}_3 + x_2x_3 + x_2\bar{x}_3$
 $= x_1x_1 + x_1\bar{x}_3 + \bar{x}_2x_1 + x_2x_3 \quad (x_1x_1 = 0)$
 $= x_1\bar{x}_3 + x_1\bar{x}_3 + x_2x_3 \quad (\text{Commutative Theorem})$
 $= x_1\bar{x}_3 + x_2x_3 + \bar{x}_2x_3 \quad (\text{Commutative Theorem})$

$$Q4. \text{Prove that } (x_1 + x_2)(\bar{x}_1 + \bar{x}_3) = x_1\bar{x}_3 + \bar{x}_1x_3$$

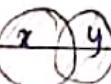
LHS $(x_1 + x_2) \cdot (\bar{x}_1 + \bar{x}_3)$
 $= x_1\bar{x}_1 + x_1\bar{x}_3 + x_2\bar{x}_1 + x_2\bar{x}_3 \quad (\text{Distributive Theorem})$
 $= 0 + x_1\bar{x}_3 + x_2\bar{x}_1 + 0 \quad (x_1\bar{x}_1 = 0)$
 $= x_1\bar{x}_3 + \bar{x}_2x_1 \quad (\text{Commutative Method})$
 $= \text{RHS}$

III NOTES

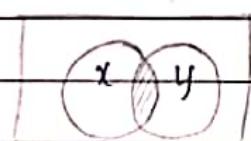
① Venn Diagrams



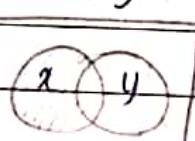
$$x \cdot \bar{z}$$



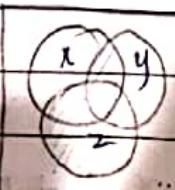
$$\bar{x} + y$$



$$x \cdot \bar{y}$$



$$x \cdot \bar{y}$$



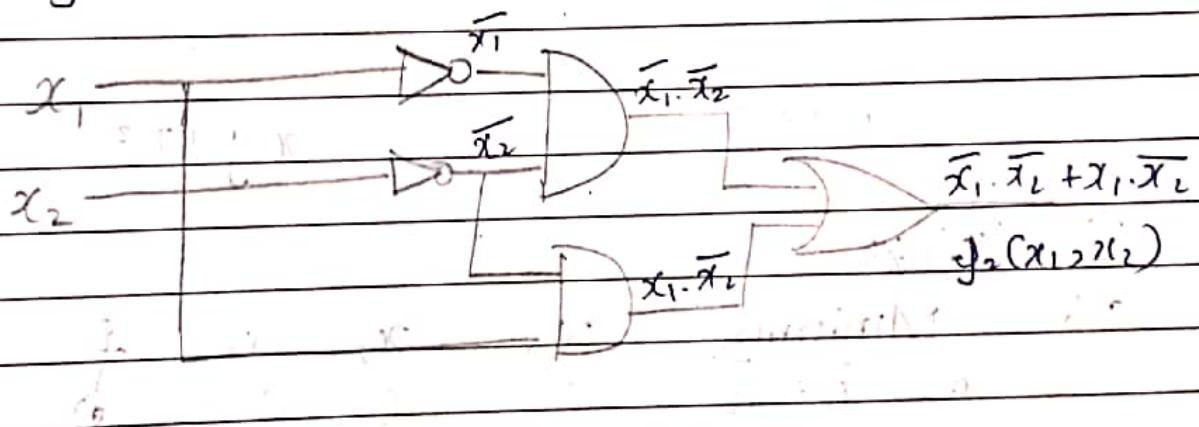
$$x \cdot \bar{y} + z$$

②

Synthesis using AND, OR and NOT
for designing a logic circuit that
implements a truth table whose item
is stated that has a value of 1 for each
evaluation for which the output function
equals a 1. Then the logical sum is
taken.

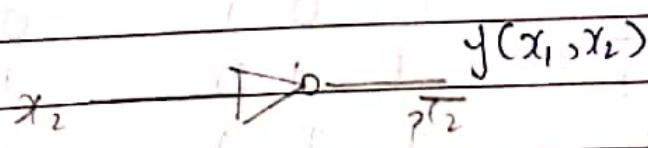
eg:-	x_1	x_2	f_1	f_2
	0	0	0	1
	0	1	0	0
	1	0	0	1
	1	1	1	0

$$f_2(x_1, x_2) = \bar{x}_1 \cdot \bar{x}_2 + x_1 \cdot \bar{x}_2$$



Canonical form of products

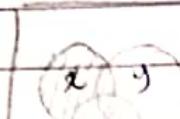
$$\begin{aligned} f_2(x_1, x_2) &= \bar{x}_2 (\bar{x}_1 + x_1) \\ &= \bar{x}_2 (1) \\ &= \bar{x}_2 \end{aligned}$$



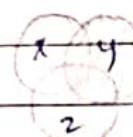
Q5 Verification of the distributive properties
 $x(y+z) = xy + xz$

LHS

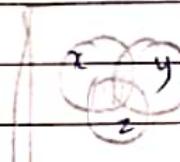
RHS



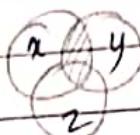
x



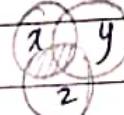
y+z



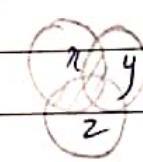
$x \cdot (y+z)$



$x \cdot y$



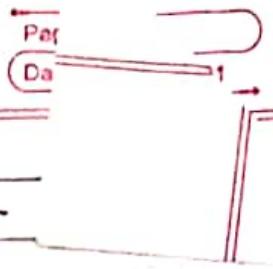
$x \cdot z$



$x \cdot y + x \cdot z$

~~Q5~~ ~~Answers~~

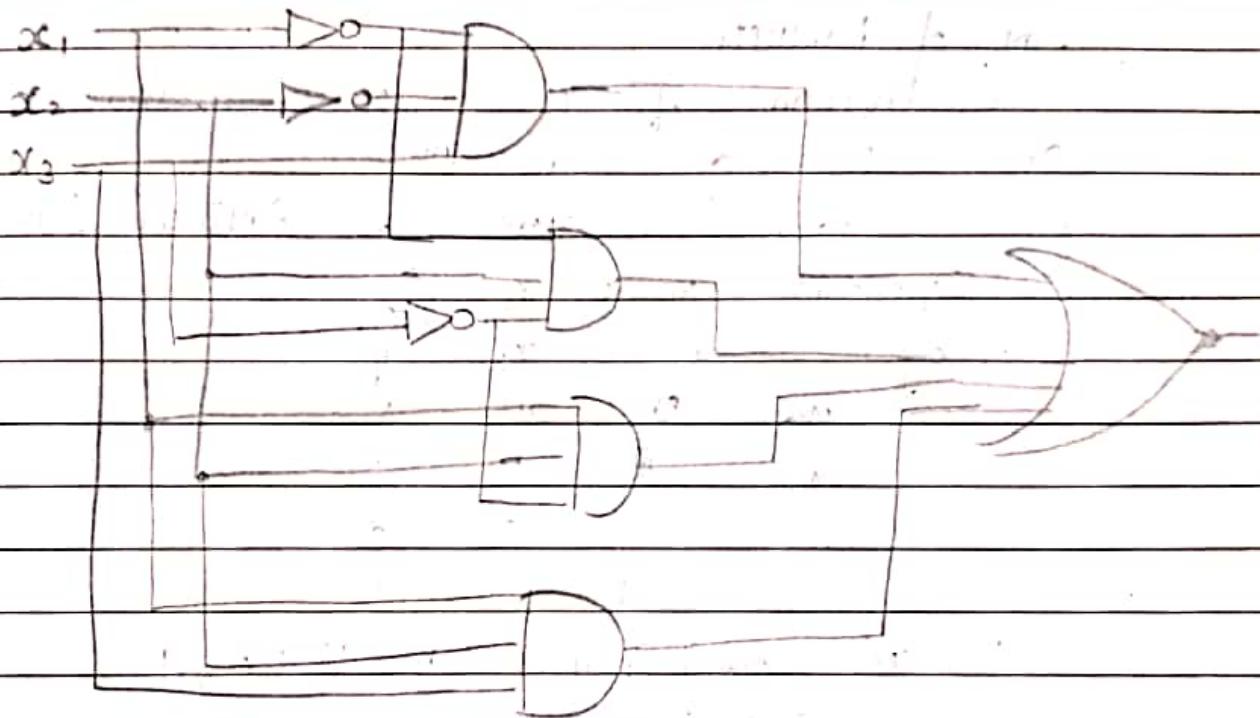
Q6	Minterms	x_1	x_2	x_3	f
m_0	$\bar{x}_1 \bar{x}_2 \bar{x}_3$	0	0	0	0
m_1	$\bar{x}_1 \bar{x}_2 x_3$	0	0	1	1
m_2	$\bar{x}_1 x_2 \bar{x}_3$	0	1	0	1
m_3	$\bar{x}_1 x_2 x_3$	0	1	1	0
m_4	$x_1 \bar{x}_2 \bar{x}_3$	1	0	0	0
m_5	$x_1 \bar{x}_2 x_3$	1	0	1	0
m_6	$x_1 x_2 \bar{x}_3$	1	1	0	1
m_7	$x_1 x_2 x_3$	1	1	1	1



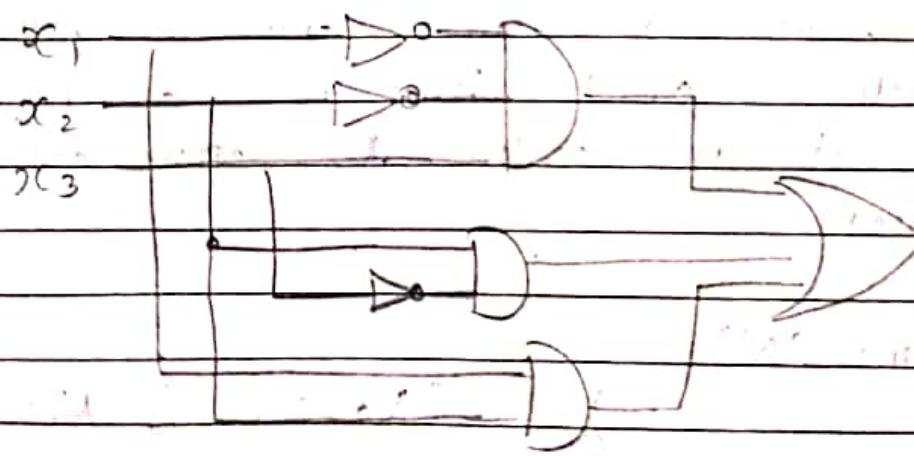
$$f(x_1, x_2, x_3) = \overline{x}_1 \cdot \overline{x}_2 \cdot x_3 + x_1 \cdot \overline{x}_2 \cdot \overline{x}_3$$

$$= \overline{x}_1 \cdot \overline{x}_2 \cdot x_3 + (\overline{x}_1 + x_1)x_2 \overline{x}_3 + x_1 x_2 (x_3 + \overline{x}_3)$$

$$f(x_1, x_2, x_3) = \overline{x}_1 \cdot \overline{x}_2 \cdot x_3 + x_1 \cdot \overline{x}_3 + x_1 x_2 \text{ and}$$



Canonical Sum of Products



Sum of Products

IV NOTES

① Minterms (m_i) :-

For a function of n variables a product term in which each of the variables appear once (in complemented or uncomplemented form) is called a minterm.

② Sum of Products

A function of can be represented by an expression i.e. the sum of minterms where each minterm is ANDed with the corresponding value of f .

Eg:-	x_1	x_2	f
m_0	0	0	1
m_1	0	1	1
m_2	1	0	0
m_3	1	1	1

$$\begin{aligned} f(x_1, x_2) &= m_0 \cdot 1 + m_1 \cdot 1 + m_2 \cdot 0 + m_3 \cdot 1 \\ &= m_0 + m_1 + m_3 \end{aligned}$$

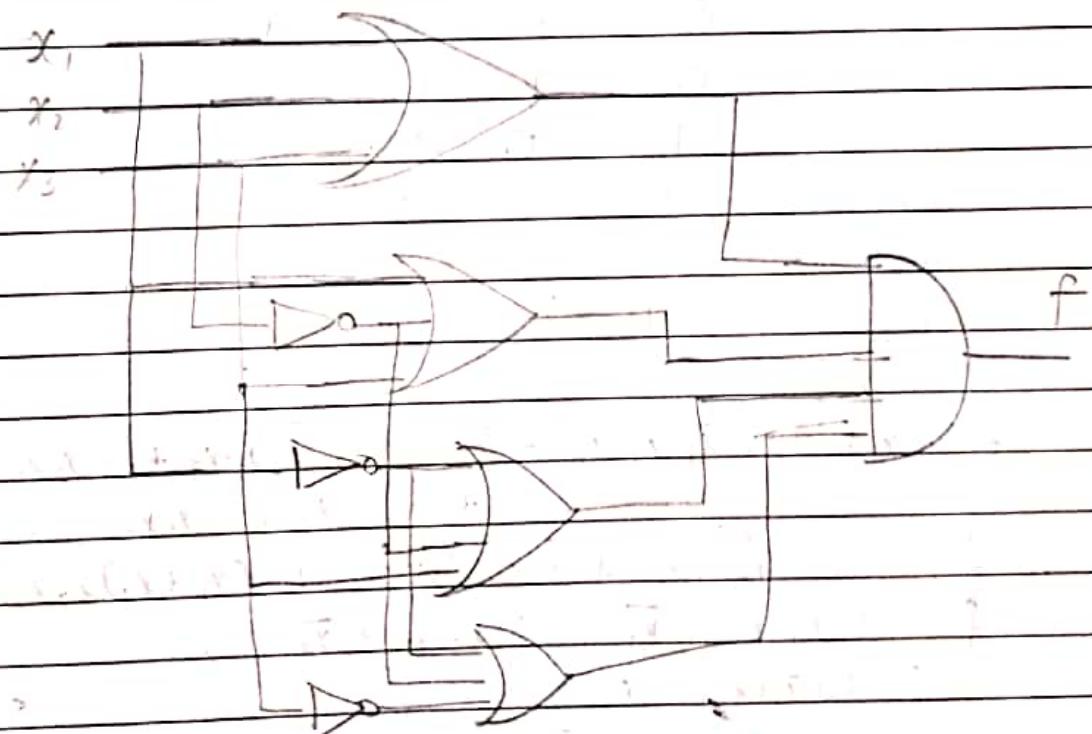
A logic expression that consist of product terms (AND terms) that are summed (OR) is said to be sum of products (SOP). If each product term is a minterm then it is called the canonical sum of products.

③ Maxterms (M_i) :-

$$\text{Eg:- } f(x_1, x_2)_{(S)} = \prod(M_0, M_2, M_6, M_7)$$

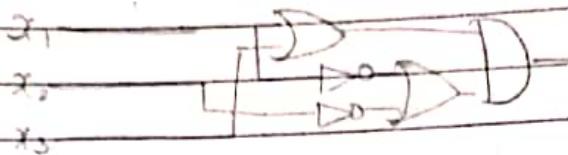
	Maurlems	x_1	x_2	x_3	f
M_0	$x_1 + x_2 + x_3$	0	0	0	0
M_1	$\bar{x}_1 + x_2 + x_3$	0	0	1	1
M_2	$x_1 + \bar{x}_2 + x_3$	0	1	0	0
M_3	$x_1 + \bar{x}_2 + \bar{x}_3$	0	1	1	1
M_4	$\bar{x}_1 + x_2 + \bar{x}_3$	1	0	0	1
M_5	$\bar{x}_1 + \bar{x}_2 + x_3$	1	0	1	1
M_6	$\bar{x}_1 + \bar{x}_2 + \bar{x}_3$	1	1	0	0
M_7	$\bar{x}_1 + \bar{x}_2 + \bar{x}_3$	1	1	1	0

$$f(x_1, x_2, x_3) = (x_1 + x_2 + x_3)(\bar{x}_1 + \bar{x}_2 + \bar{x}_3) (\bar{x}_1 + \bar{x}_2 + x_3) \\ (\bar{x}_1 + x_2 + \bar{x}_3)$$



Canonical forms

$$f(x_1, x_2, x_3) = (x_1 + x_3)(\bar{x}_1 + \bar{x}_2)$$



Product of sums

$$Q7. f(x_1, x_2, x_3) = \sum_m (1, 2, 3, 4, 5, 6, 7)$$

S T ~~$f(x_1, x_2, x_3) \equiv x_1 + x_2 + x_3$~~

	x_1	\bar{x}_2	\bar{x}_3	f
m_0	0	0	0	0
m_1	0	0	1	1
m_2	0	1	0	1
m_3	0	1	1	1
m_4	1	0	0	1
m_5	1	0	1	1
m_6	1	1	0	1
m_7	1	1	1	1

$$f(x_1, x_2, x_3) = \bar{x}_1 \bar{x}_2 x_3 + \bar{x}_1 x_2 \bar{x}_3 + \bar{x}_1 x_2 x_3 + x_1 \bar{x}_2 \bar{x}_3 + x_1 \bar{x}_2 x_3$$

$$+ x_1 x_2 \bar{x}_3 + x_1 x_2 x_3$$

$$f = (\bar{x}_1 + x_1) \bar{x}_2 x_3 + (\bar{x}_1 + x_1) x_2 \bar{x}_3 + (\bar{x}_1 + x_1) x_2 x_3 + x_1 \bar{x}_3 (\bar{x}_2 + x_2)$$

$$= \bar{x}_2 x_3 + x_2 \bar{x}_3 + x_2 x_3 + x_1 \bar{x}_3$$

$$= \bar{x}_3 (\bar{x}_2 + x_2) + x_2 (\bar{x}_3 + x_3) + x_1 \bar{x}_3$$

$$= x_3 + x_1 + x_1 \bar{x}_3$$

$$= x_2 + (x_3 + x_1 \bar{x}_3)$$

$$= x_2 + [(x_1 + x_1) (x_3 + \bar{x}_3)]$$

$$= x_2 + x_3 + x_1$$

$a+a \cdot a$
 $a+a' = 1$
 $(a+b)c = (a+c)(b+c)$

—

Q8. If C_1, x_1, x_2, x_3 : $\Pi M \{0, 1, 2, 3, 4, 5, 6\}$
 S.T. $f(x_1, x_2, x_3) = x_1 \cdot x_2 \cdot x_3$

M	x_1	x_2	x_3	f
M_0	0	0	0	0
M_1	0	0	1	0
M_2	0	1	0	0
M_3	0	1	1	0
M_4	1	0	0	0
M_5	1	0	1	0
M_6	1	1	0	0
M_7	1	1	1	1

$$f = (x_1 + x_2 + x_3)(\overline{x_1} + \overline{x_2} + \overline{x_3})(x_1 + \overline{x_2} + x_3)(x_1 + \overline{x_2} + \overline{x_3}) \\ (\overline{x_1} + x_2 + x_3)(\overline{x_1} + \overline{x_2} + x_3)(\overline{x_1} + \overline{x_2} + \overline{x_3})$$

$$= (x_1 + x_2)(x_3 + \overline{x_3})(x_1 + \overline{x_2})(x_3 + x_3)(\overline{x_1} + x_2) \\ (x_3 + x_3)(\overline{x_2} + x_3)(x_1 + x_1) \\ = (x_1 + x_2)(x_1 + \overline{x_2})(\overline{x_1} + x_2)(\overline{x_2} + x_3)$$

$$= (x_1)(x_2 + \overline{x_2})(x_2)(x_1 + \overline{x_1})(\overline{x_2} + x_3)$$

$$= x_1 \cdot \cancel{x_2} \cdot \cancel{x_2} \cdot (\overline{x_2} + x_3)$$

$$= x_1 \cdot \cancel{x_2} \cdot \cancel{x_2} + x_1 \cdot \cancel{x_2} \cdot x_3$$

$$= x_1 x_2 \cdot x_3$$

1 NOTES

① Universal Gates

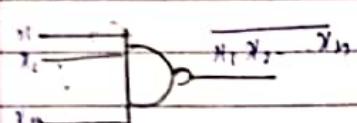
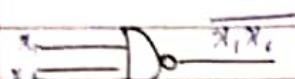
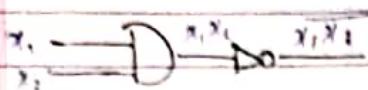
~~NAND~~ ~~NOR~~

▷ NAND & NOR LOGIC GATES

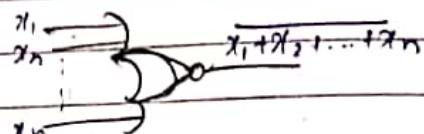
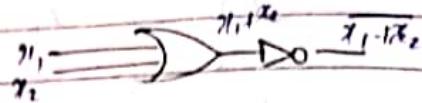
NAND & NOR gates are obtained by complementing the output generated by

AND & OR gate respectively

NAND



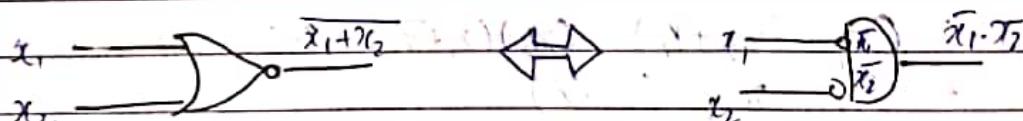
NOR



x_1	x_2	$x_1 \cdot x_2$	$\bar{x}_1 \cdot \bar{x}_2$	$x_1 + x_2$	$\bar{x}_1 + \bar{x}_2$
0	0	0	1	1	1
0	1	0	1	0	0
1	0	0	1	0	1
1	1	1	0	0	0

De Morgan's Rule

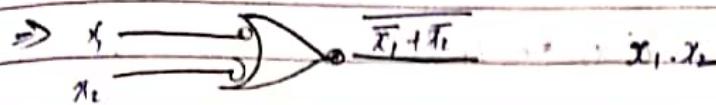
$$\Rightarrow \overline{x_1 + x_2} = \bar{x}_1 \cdot \bar{x}_2$$



$$\Rightarrow \bar{x}_1 \cdot \bar{x}_2 = \bar{x}_1 + \bar{x}_2$$



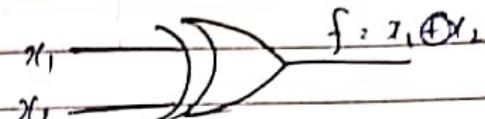
$$\Rightarrow x_1 \cdot \bar{x}_2 = (\bar{x}_1)' + (\bar{x}_2)' = x_1 + x_2$$



1) EXCLUSIVE GATES

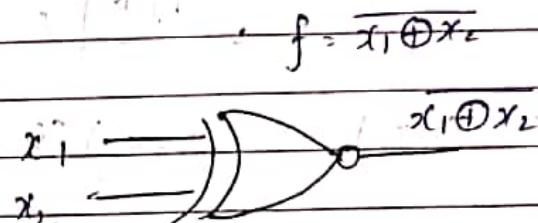
a) Exclusive OR

x_1	x_2	f
0	0	0
0	1	1
1	0	1
1	1	0



b) Exclusive NOR

x_1	x_2	f
0	0	1
0	1	0
1	0	0
1	1	1



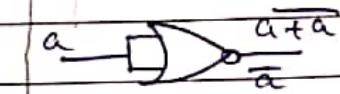
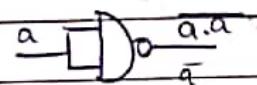
③-

Gate

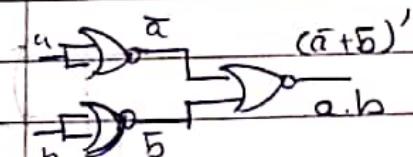
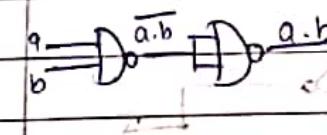
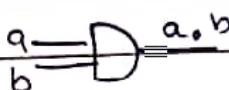
NAND.

NOR

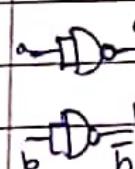
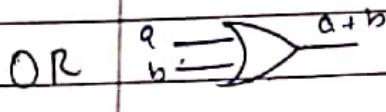
NOT



AND

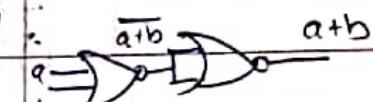
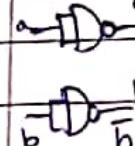
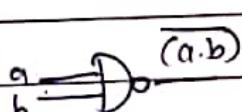


OR

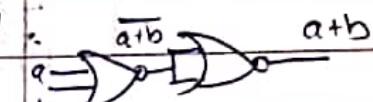
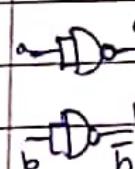


$$a+b$$

NAND

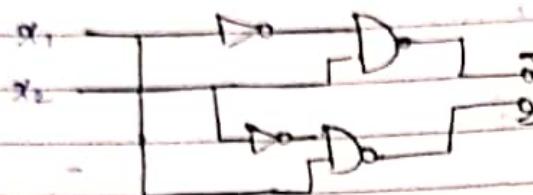


NOR

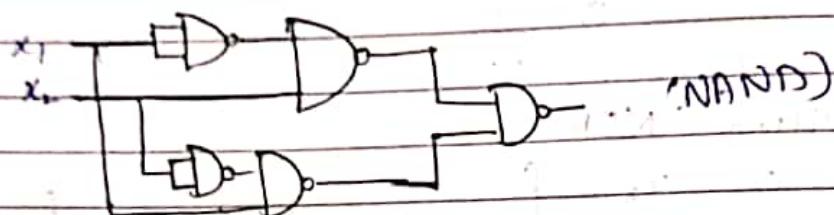


Q9

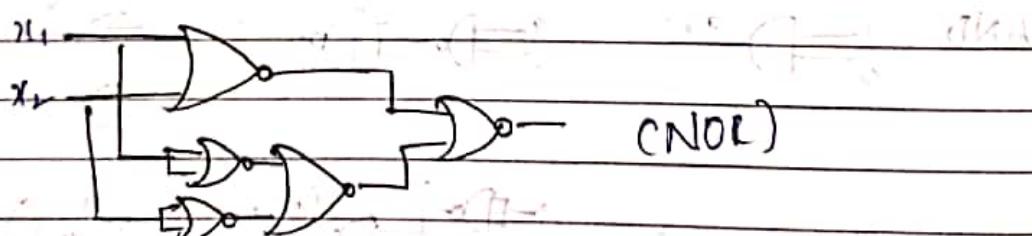
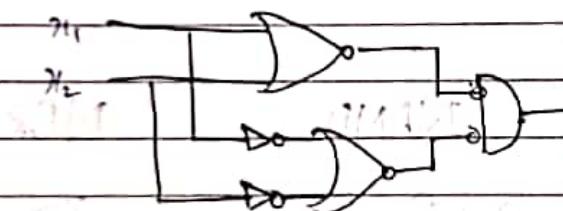
$$f(x_1, x_2)_{pos} = \bar{x}_1 x_2 + x_1 \bar{x}_2$$



Q11



$$Q10 \quad f(x_1, x_2)_{pos} = (x_1 + x_2)(\bar{x}_1 + \bar{x}_2)$$



Q11 $f(x_1, x_2, x_3) = \Sigma(0, 1, 6, 7)$

x_1	x_2	x_3	f
-------	-------	-------	-----

0	0	0	1
---	---	---	---

0	0	1	1
---	---	---	---

0	1	0	0
---	---	---	---

0	1	1	0
---	---	---	---

1	0	0	0
---	---	---	---

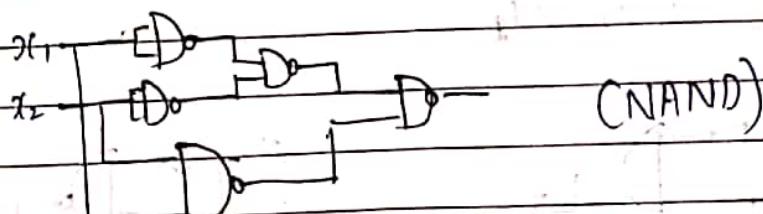
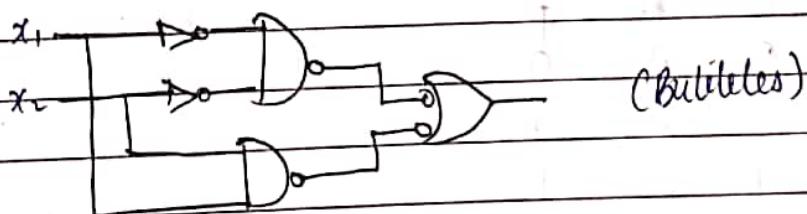
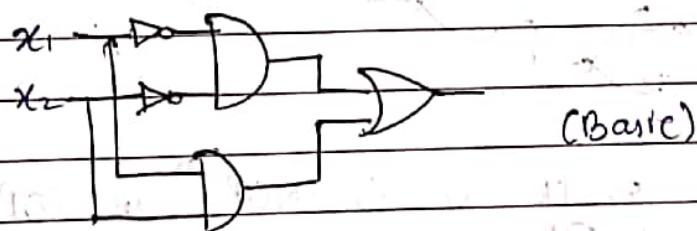
1	0	1	0
---	---	---	---

1	1	0	1
---	---	---	---

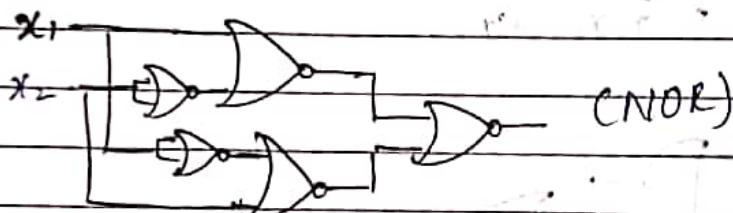
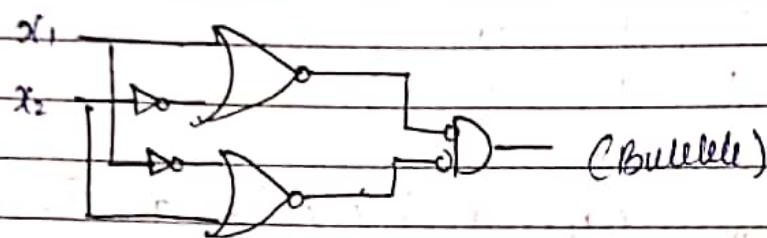
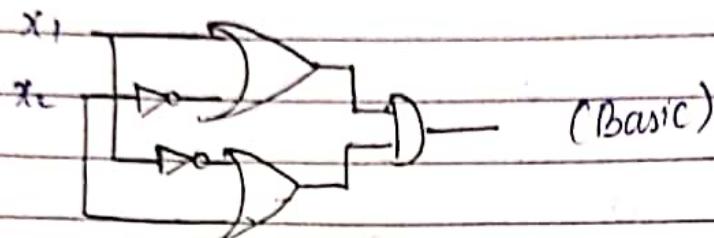
1	1	1	1
---	---	---	---

$$f(x_1, x_2, x_3)_{SOP} = \bar{x}_1 \bar{x}_2 \bar{x}_3 + \bar{x}_1 \bar{x}_2 x_3 + x_1 x_2 \bar{x}_3 + x_1 x_2 x_3$$

$$= \bar{x}_1 \bar{x}_2 + x_1 x_2$$



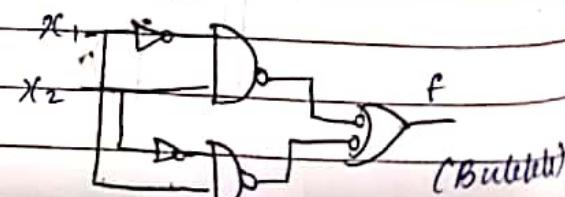
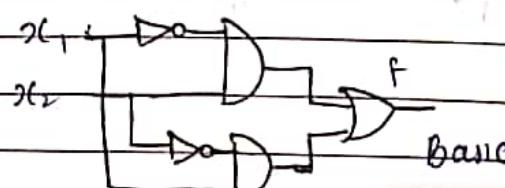
$$f(x_1, x_2, x_3)_{POS} = (x_1 + \bar{x}_2 + x_3)(x_1 + \bar{x}_2 + \bar{x}_3)(\bar{x}_1 + x_2 + x_3)(\bar{x}_1 + x_2 + \bar{x}_3) \\ = (x_1 + \bar{x}_2)(x_1 + x_3)$$

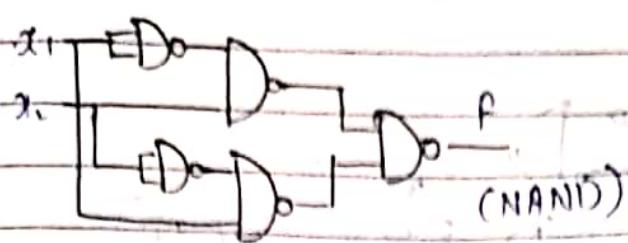


Q12 Implement Ex-OR or Ex-NOR in SOP & POS
Exclusive OR

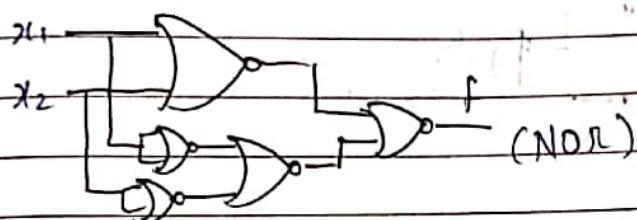
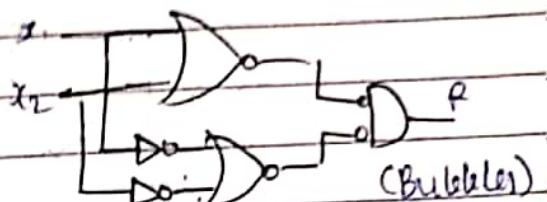
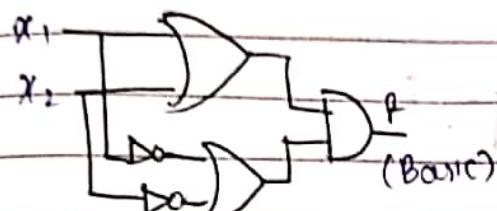
x_1	x_2	f
0	0	0
0	1	1
1	0	1
1	1	0

$$f(x_1, x_2)_{SOP} = \bar{x}_1 \cdot x_2 + x_1 \cdot \bar{x}_2$$





$$f(x_1, x_2)_{\text{POS}} = (x_1 + x_2)(\bar{x}_1 \bar{x}_2)$$



Exclusive NOR

$$x_1 \quad x_2 \quad f$$

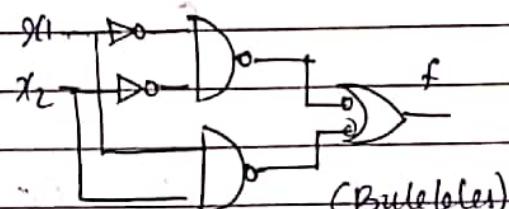
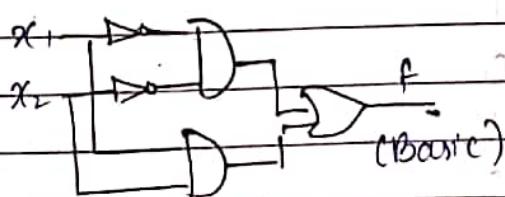
$$0 \quad 0 \quad 1$$

$$0 \quad 1 \quad 0$$

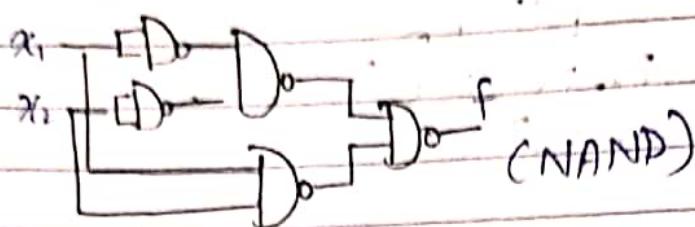
$$1 \quad 0 \quad 0$$

$$1 \quad 1 \quad 1$$

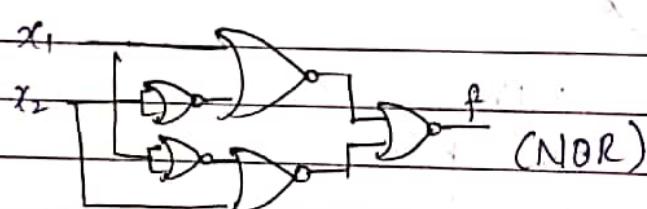
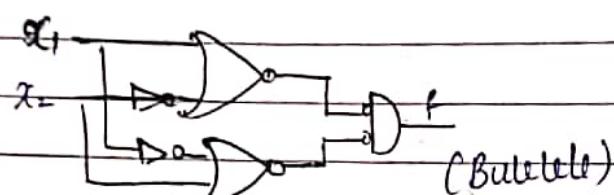
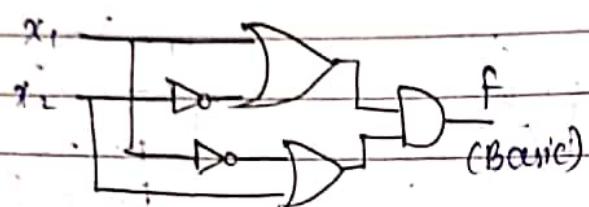
$$f(x_1, x_2)_{\text{exp}} = \bar{x}_1 \bar{x}_2 + x_1 x_2$$



$$f(x_1, x_2)$$



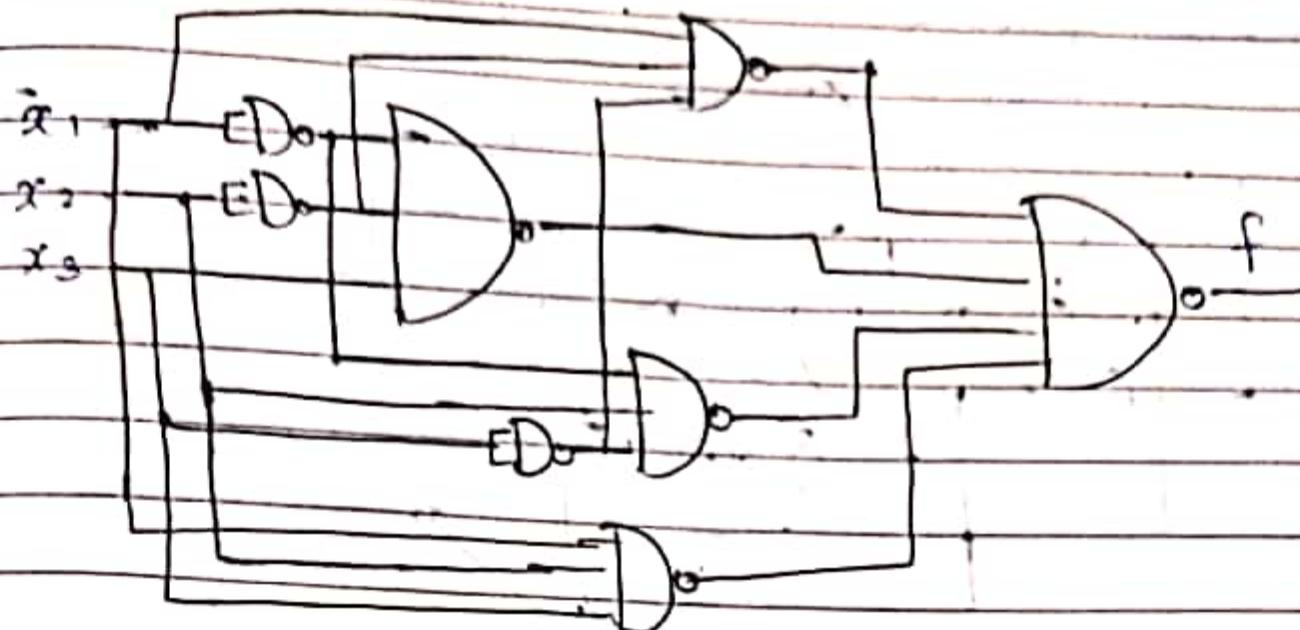
$$f(x_1, x_2) = \overline{(x_1 + x_2)}(\overline{x_1} \cdot \overline{x_2})$$



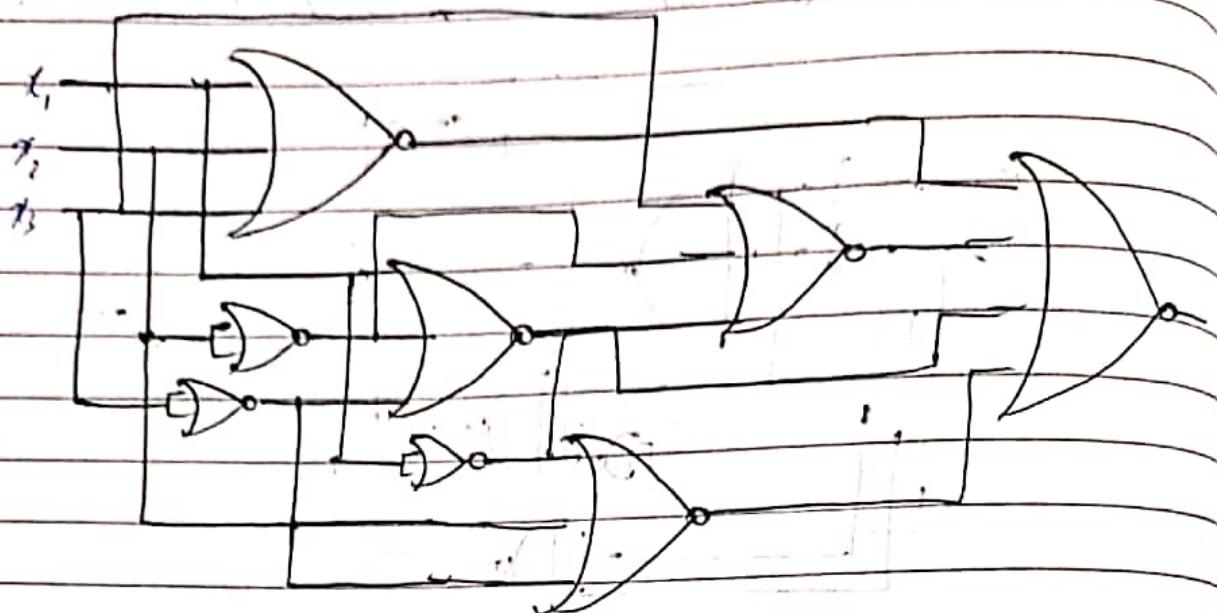
Q13 Design Examples

x_1	x_2	x_3	f
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

$$f(x_1, x_2, x_3)_{\text{cop}} = \bar{x}_1 \bar{x}_2 x_3 + x_1 \bar{x}_2 \bar{x}_3 + x_1 x_2 \bar{x}_3 + x_1 x_2 x_3$$



$$f(x_1, x_2, x_3)_{\text{pos}} = (x_1 + x_2 + x_3)(x_1 + \bar{x}_2 + \bar{x}_3)(\bar{x}_1 + x_2 + x_3) \\ (\bar{x}_1 + \bar{x}_2 + x_3)$$



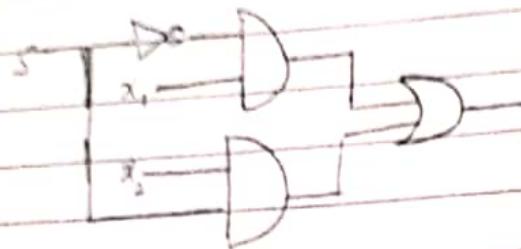
Multiplexer Circuit

This circuit enables us to choose data from exactly one of a number of possible sources.

Eg:- Television, radio.

S	x_1	x_2	f	V/P	O/P
0	0	0	0	d_1	$-f$
0	0	1	0	d_2	$-f$
0	1	0	1	d_3	$s \rightarrow 0$
0	1	1	1	d_4	$s \rightarrow 1$
1	0	0	0	$s \rightarrow 0$	$x_1 \rightarrow f$
1	0	1	1	$s \rightarrow 1$	$x_2 \rightarrow f$
1	1	0	0		
1	1	1	1		

$$\begin{aligned}
 f(s, x_1, x_2) &= \bar{s}x_1\bar{x}_2 + \bar{s}x_1x_2 + \\
 &\quad s\bar{x}_1\bar{x}_2 + sx_1x_2, \\
 &= \bar{s}x_1 + sx_2
 \end{aligned}$$



SYMBOL OF MULTIPLEXER SYMBOL



s	f
0	x_1
1	x_2

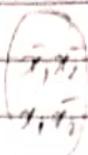
KARNAUGH'S MAP

- The method provides a systematic way of optimising the number of terms needed in the expression. It is made of squares with each square representing a minterm of a function that is to be minimised.
- The simplified expression obtained from the map are always in the

one of the Standard forms - SOP or POS

③ 2 Variables K-Map

$x_1 \backslash x_2$	0	1
0	$\bar{x}_1 \bar{x}_2$	$\bar{x}_1 x_2$
1	$x_1 \bar{x}_2$	$x_1 x_2$



$$\begin{aligned} f(x_1, x_2) &= \Sigma(1, 3) \\ &\rightarrow \bar{x}_2 \end{aligned}$$

$x_1 \backslash x_2$	0	1
0	0 ₀	1 ₁
1	0 ₂	1 ₃

$$f(x_1, x_2) = x_2$$

No. of combinations
 $\Rightarrow 2^n$ n = no. of variables

④ 3 Variables K-Map

$x_1 \backslash x_2 \backslash x_3$	000	001	011	111	110
0	m_0	m_1	m_3	m_2	
1	$\bar{x}_1 \bar{x}_2 \bar{x}_3$	$\bar{x}_1 \bar{x}_2 x_3$	$\bar{x}_1 x_2 \bar{x}_3$	$\bar{x}_1 x_2 x_3$	
1	$x_1 \bar{x}_2 \bar{x}_3$	$x_1 \bar{x}_2 x_3$	$x_1 x_2 \bar{x}_3$	$x_1 x_2 x_3$	

[We need to arrange so only 1 change takes place b/w adjacent boxes]

011
110

211

(★ You can combine boxes only in power of 2 like $2^0=1, 2^1=2, 2^2=4$)

(★ 3 V Kmap has 3 adj. boxes
 $\boxed{1\ 3\ 1\ 2\ 0}$]

Q5 $y(x_1, x_2, x_3) = \Sigma(1, 2, 3, 5)$
using K-map

$x_1 \backslash x_3$	00	01	11	10	$\bar{x}_1 x_2$
x_2	0	1	1	1	$\bar{x}_1 x_2$
0	0	1	1	3	2
1	4	5	7	6	
					$\bar{x}_2 x_3$

$$y(x_1, x_2, x_3) = \bar{x}_1 x_2 + \bar{x}_2 x_3$$

⑤ 4 Variable K-Map

$x_3 \backslash x_2$	00	01	11	10	
x_1	M_0	M_1	M_3	M_2	
00	$\bar{x}_1 \bar{x}_2 \bar{x}_3 \bar{x}_4$	$\bar{x}_1 \bar{x}_2 \bar{x}_3 x_4$	$\bar{x}_1 x_2 \bar{x}_3 \bar{x}_4$	$\bar{x}_1 x_2 x_3 \bar{x}_4$	
01	$\bar{x}_1 \bar{x}_2 \bar{x}_3 x_4$	$\bar{x}_1 x_2 \bar{x}_3 \bar{x}_4$	$\bar{x}_1 x_2 x_3 \bar{x}_4$	$x_1 \bar{x}_2 x_3 \bar{x}_4$	
11	$x_1 \bar{x}_2 \bar{x}_3 \bar{x}_4$	$x_1 \bar{x}_2 \bar{x}_3 x_4$	$x_1 x_2 \bar{x}_3 \bar{x}_4$	$x_1 x_2 x_3 \bar{x}_4$	
10	$x_1 \bar{x}_2 \bar{x}_3 x_4$	$x_1 \bar{x}_2 x_3 \bar{x}_4$	$x_1 x_2 \bar{x}_3 x_4$	$x_1 x_2 x_3 \bar{x}_4$	

Q6 A four variable logic function that is equal to one if any 3 or all 4 of its variables $wxyz$ are equal to 1.
1. Design a minimum cost SOP circuit that implements this majority function.

w	x	y	z	f
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	0	0	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

W X Y Z 00 01 10 11

00	0 ₀	0 ₁	0 ₂	0 ₃
01	0 ₄	0 ₅	0 ₆	0 ₇
11	0 ₈	0 ₉	0 ₁₀	0 ₁₁
10	0 ₁₂	0 ₁₃	0 ₁₄	0 ₁₅

$$\begin{aligned}
 f(w,x,y,z) &= \Sigma(7, 11, 13, 14, 15) \\
 &= xyz + wyz + wxy + wxz
 \end{aligned}$$

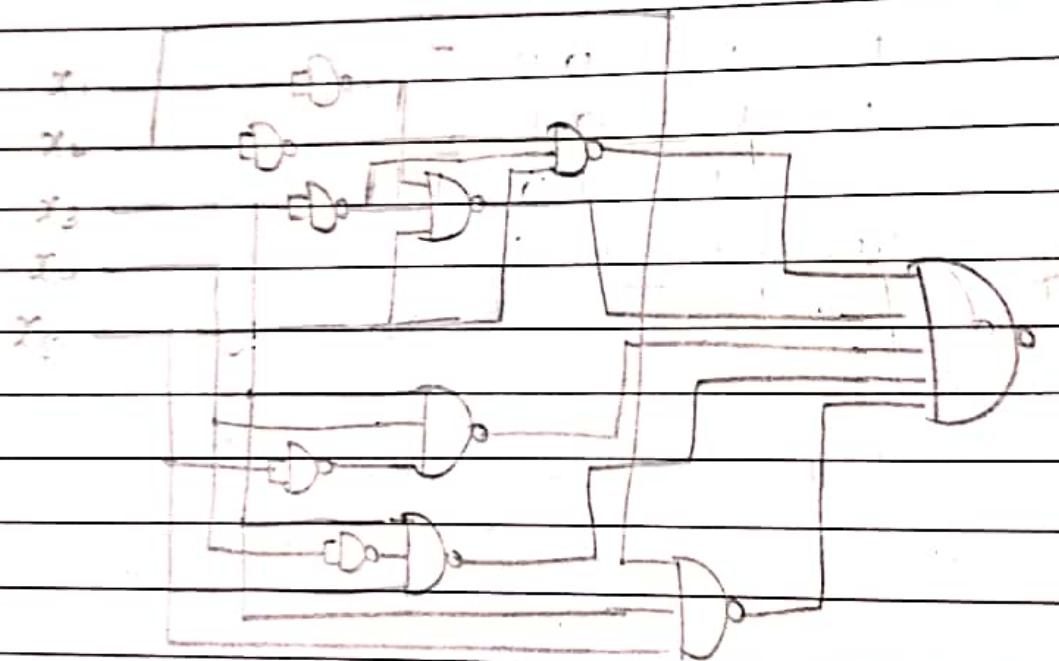
Q7 Simplify $f(A, B, C, D) = \Sigma(2, 3, 6, 7, 12, 13, 14)$

$\bar{A}B$	\bar{B}	\bar{C}	\bar{D}	A	B	C	D	
00	00	01	11	10				$\rightarrow \bar{A}C$
01	01	00	10	11	10	11	10	$\rightarrow \bar{B}CD$
11	11	10	11	01	11	11	11	
10	01	00	01	01	00	01	10	

$$f(A, B, C, D) = AC + BCD + ABC\bar{C}$$

Q19 H.W

NAND Gate



② Prime Implicant

In choosing adjacent squares in a map,

it must be ensured that

- ▷ all the minterms in the function are covered when we combine the squares
- ▷ the no of terms in the expression is minimized
- ▷ there are no redundant items (ie minterms that are already covered by other terms).

$$Q. 18 f(A, B, C, D) = \Sigma(0, 2, 3, 5, 7, 8, 9, 10, 11, 13, 15)$$

AB	C\ D	00	01	11	10	
00	1	0	1	1	2	$\rightarrow A\bar{B}\bar{D}$
01	0	4	5	7	6	$\rightarrow CD$
11	0	13	13	15	14	
10	1	8	9	10	10	$\rightarrow A\bar{B}$

$$f(A, B, C, D) = \bar{A}\bar{B}\bar{D} + CD + BD + A\bar{B}$$

of $f = \pi(2, 3, 4)$ such that $f \rightarrow$ takes 0s or 2, 3, 4.

$$\text{U}_{(15)} = \mathcal{E}(3, 6, 9)$$

Take Os

Take Up

⇒ A prime implicant is a product term obtained by combining the maximum possible number of adjacent squares in the map. If a minterm in a square is covered by only one prime implicant, that prime implicant is known as an essential prime implicant.

⑦ 5 Variable K-Map

~~Approved after 4-~~

	000	001	011	010	110	111	101	100
00	M ₀	M ₁	M ₃	M ₂	M ₅	M ₇	M ₅	M ₄
01	M ₈	M ₉	M ₇	M ₁₀	M ₁₄	M ₁₅	M ₉	M ₁₂
11	M ₂₄	M ₂₅	M ₂₇	M ₂₆	M ₃₀	M ₃₁	M ₂₉	M ₂₈
0	M ₁₆	M ₁₇	M ₉	M ₁₈	M ₂₂	M ₂₃	M ₂₁	M ₂₀

$$Q(9) \text{ if } (x_1, x_2, x_3, x_4, x_5) = \{1, 3, 5, 6, 9, 11, 13, 14, 15, 17, \\ 19, 21, 22, 24, 30, 31\}$$

we can combine boxes again
ONLY if an uncombined term
can be paired up w/ it

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$f(\text{prop})(x_1, x_2, x_3, x_4, x_5)$ = $x_1 \bar{x}_2 x_3 + x_2 \bar{x}_3 x_4$ is only in terms of
 $x_3, x_4, \bar{x}_1 + x_2 \bar{x}_4 x_5$

x_1, x_2, x_5

Conclusion 2.10

\rightarrow 2^o - 1 or 2 is reduce
2^o - 2 only 1 is reduce
2^o - 11 only 2 is reduce

$f(\text{prop})(x_1, x_2, x_3, x_4, x_5) = \overbrace{(x_1, x_2, x_3, x_4, x_5)}^{2^o - 1} + \overbrace{(x_1, x_2, x_3, x_4, x_5)}^{2^o - 2} + \overbrace{(x_1, x_2, x_3, x_4, x_5)}^{2^o - 11}$

$$f_{\text{prop}}(x_1, x_2, x_3, x_4, x_5) = (x_1 + \bar{x}_1)(x_2 + \bar{x}_2 + \bar{x}_4 + \bar{x}_5)(x_3 + \bar{x}_3 + \bar{x}_4 + \bar{x}_5)$$

$$(x_2 + \bar{x}_4 + \bar{x}_5)$$

Don't care conditions

functions that have unspecified o/p for some
if p combinations are called as incompletely
specified functions and the unspecified
minterms of a function are called Don't care
conditions

$$\text{Q. 20. } f(x_1, x_2, x_3) = \Sigma_m(1, 4, 7) + \Delta(2, 5) / \Sigma_d(3, 5)$$

	$x_1 \bar{x}_2 x_3$	$\bar{x}_1 \bar{x}_2 x_3$	$x_1 x_2 \bar{x}_3$	$\bar{x}_1 x_2 \bar{x}_3$	$x_1 x_2 x_3$	$\bar{x}_1 \bar{x}_2 \bar{x}_3$
0	0 0	0 1	1 1	1 0	X 2	X 3
1	1 1	X 5	1 1	0 0	0 0	0 0

Action's can do

combine IF USEFUL

Otherwise ignored

$$f(x_1, x_2, x_3) = x_1 \bar{x}_2 + x_1 x_2 + \bar{x}_2 x_3$$

so final result is
if condition matches then
else ignore

if condition matches then
else ignore

Q 21. $f(G, x_1, x_2, x_3, x_4) = \sum(0, 2, 8, 9, 10, 15) + D(1, 3, 6, 7)$

m	00	01	11	10
00	1	X	X	1
01	1	0	X	X
10	0	0	1	0
11	1	0	0	1

$$f(G, x_1, x_2, x_3, x_4) = \overline{x_2} \overline{x_3} + x_2 x_3 x_4 + \overline{x_1} x_3 x_4 \\ + \overline{x_1} \overline{x_4}$$

$$f(G, x_1, x_2, x_3, x_4) = (\overline{x_2} + x_3)(x_2 + \overline{x_3} + \overline{x_1})(\overline{x_1} + x_3 + x_4)$$

QUINE-MCCLUSKEY METHOD (QM METHOD)

The QM method can also be adopted for minimizing Boolean functions for smaller number of input variables.

① Procedure

- ▷ Arrange the given minterms in ascending order and make groups based on the number of ones in their binary representation
- ▷ Compare the minterms present in successive groups and form pairs of two minterms differ in a single bit position
- ▷ Repeat step 1) with the newly formed items.
- IV) Formulate the prime implicant chart.
- ▷ Find minimal prime implicants. If a minterm is covered only by one prime implicant, then that is an minimal prime implicant.

v) Reduce the prime implicant chart by
removing the rows of each essential
prime implicant and the columns correspond-
ing to the ~~nonessential~~ ~~non-~~ minterms
covered in essential prime implicants. Repeat
step v) for the reduced prime implicant
table.

$$Q.22 \quad f(x_1, x_2, x_3, x_4, x_5) = \Sigma (0, 2, 4, 5, 6, 7, 8, 10, 11, 17, \\ 18, 21, 29, 31) + \Sigma (11, 20, 22)$$

x_1, x_2, x_3, x_4, x_5			
00000	✓0 -	00000	(0,2) 0 00-0
00010	✓2 -	00010	(0,4) 0 0-00
00100	✓4 -	00100	(0,8) 0 000
00101	✓8 -	01000	(3,6) 00-10
00110	✓5	00101	(2,10) 0 010
00111	✓6	00110	(4,5) 0010-
01000	✓10	01010	(4,6) 001-0
01010	✓19	10001	(4,20) 0100
01110	✓20	10100	(8,10) 010-0
10001		X0010	(5,7) 001 1
10010		✓1	00111 (5,21) 0 101
10101		✓14	01110 (6,7) 0 011-
11101		✓11	01011 (6,14) 0-110
11111		✓2	10101 (6,22) 0110
	✓22	10110	(10,11) 0 101-
2 5 2-1	✓21	11101	(10,14) 01-10
2 1-1	✓31	11111	(17,21) 10-01
00101			(18,22) 10-10
			(20,21) 1010
			(20,22) 101 0

(Likes some are rejected)

- (21, 29) 1 1 0 1 (0, 2, 11, 6) 0 0 - 0 → $\bar{x}_1 \bar{x}_2 \bar{x}_5$
(29, 31) 1 1 1 - 1 (0, 2, 8, 10) 0 - 0 - 0 → $\bar{x}_1 \bar{x}_3 \bar{x}_5$
(0, 2, 1, 2, 16) 0 0 - - 0
(0, 8, 2, 10) 0 - 0 - 0
(2, 6, 10, 11) 0 - - 1 0 → $\bar{x}_2 \bar{x}_4 \bar{x}_5$
(2, 6, 18, 22) - 0 - 1 0 → $\bar{x}_2 x_4 x_5$
(2, 10, 6, 14) 0 - - 0 0
(2, 18, 6, 22) - 0 - 1 0
(4, 5, 6, 7) 0 0 1 → $\bar{x}_1 \bar{x}_2 \bar{x}_3$
(4, 5, 20, 21) - 0 1 0 → $\bar{x}_2 x_3 \bar{x}_4$
(4, 6, 5, 7) 0 0 1
(4, 6, 20, 22) - 0 1 - 0 → $\bar{x}_2 x_3 x_5$
(4, 20, 5, 21) - 0 1 0
(4, 20, 6, 21) - 0 1 - 0

$$(10, 11) = \bar{x}_1 x_2 \bar{x}_3 x_4$$

$$(17, 8) = x_1 \bar{x}_2 \bar{x}_3 x_4$$

$$(0, 29) = x_1 x_2 \bar{x}_4 x_5$$

$$(29, 31) = x_1 x_2 x_3 x_5$$

0, 2, 4, 5, 6, 7, 8, 10, 14, 17, 18, 21, 29, 31

$$\bar{x}_1 x_2 \bar{x}_3 x_4$$

$$x_1 \bar{x}_2 \bar{x}_3 x_4$$

$$x_1 x_2 \bar{x}_3 \bar{x}_4 x_5$$

$$\checkmark x_1 x_2 x_3 x_5$$

$$\bar{x}_1 \bar{x}_2 x_3$$

$$\bar{x}_1 \bar{x}_3 x_2$$

$$\bar{x}_1 x_4 \bar{x}_5$$

$$\bar{x}_2 x_4 \bar{x}_5$$

$$\bar{x}_1 \bar{x}_2 x_3$$

$$\bar{x}_2 x_3 \bar{x}_4$$

$$x_2 x_3 \bar{x}_5$$

✓ ✓

✓ ✓

✓ ✓

✓

✓

X

* If all minimum
are w/o 10 or 6, just pick!

6 10

✓

Ridiculous

$$\checkmark x_1 x_2 \bar{x}_3 x_4$$

$$x_1 x_2 \bar{x}_5 x_3$$

$$\bar{x}_1 \bar{x}_2 \bar{x}_3$$

$$\bar{x}_1 x_2 \bar{x}_4$$

$$\bar{x}_2 x_3 \bar{x}_5$$

also mean another
small table.

↑
do 6 as its only one
left, then we will
take 1 from the 2
 $\bar{x}_1 \bar{x}_2 \bar{x}_3$ OR $\bar{x}_1 x_2 \bar{x}_5$

$$y(x_1, x_2, x_3, x_4, x_5) = x_1 \bar{x}_2 \bar{x}_3 x_4 + x_1 x_2 x_3 x_5 + \bar{x}_1 \bar{x}_3 \bar{x}_5 \\ + \bar{x}_1 x_4 \bar{x}_5 + x_2 x_4 \bar{x}_5 + \bar{x}_1 \bar{x}_2 x_3$$