



TRANSCRIPT

SL.No.: **TS020794**

CONSOLIDATE MARKS MEMO

Serial No **219040700463**

Month & Year of Final Exam **April, 2019**

Name **SUHAS MADDALI**

Class Awarded **First Class with Distinction**

Hall Ticket No : **15071A04F1**

Year of Admission **2015-2016**

Bachelor Of Technology

Branch: **ELECTRONICS AND COMMUNICATION ENGINEERING**

Sl.No	Subject Title	Grade	Grade Points	Credits	Sl.No	Subject Title	Grade	Grade Points	Credits
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I SEMESTER					I YEAR					II SEMESTER*				
1	ADVANCED CALCULUS	A+	9.0	3.00	1	ADVANCED ENGINEERING PHYSICS	A+	9.0	3.00					
2	COMPUTER PROGRAMMING	A	8.0	4.00	2	CIRCUIT THEORY	B	7.0	4.00					
3	ENGINEERING DRAWING	A+	9.0	4.00	3	COMPUTATIONAL METHODS	A+	9.0	3.00					
4	ENGINEERING PHYSICS	A+	9.0	3.00	4	DATA STRUCTURES	A	8.0	4.00					
5	ENGLISH	A	8.0	3.00	5	ENGINEERING CHEMISTRY	O	10.0	3.00					
6	ENVIRONMENTAL STUDIES	A	8.0	3.00	6	ORDINARY DIFFERENTIAL EQUATIONS AND LAPLACE	A	8.0	3.00					
7	COMPUTER PROGRAMMING LABORATORY	O	10.0	2.00	7	DATA STRUCTURES LABORATORY	O	10.0	2.00					
8	ENGLISH LANGUAGE COMMUNICATIONS SKILLS LABORATORY	A+	9.0	2.00	8	ENGINEERING PHYSICS AND ENGINEERING CHEMISTRY	A+	9.0	2.00					
9	IT AND ENGINEERING WORKSHOP	A+	9.0	2.00										

I SEMESTER					II YEAR					II SEMESTER				
1	ELECTRONIC DEVICES AND CIRCUITS	A+	9.0	4.00	1	ANALOG COMMUNICATIONS	C*	6.0	3.00					
2	FOURIER AND COMPLEX ANALYSIS	O	10.0	3.00	2	CONTROL SYSTEMS	O	10.0	4.00					
3	PRINCIPLES OF ELECTRICAL ENGINEERING	A	8.0	3.00	3	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	A	8.0	3.00					
4	PROBABILITY THEORY AND STOCHASTIC PROCESSES	A+	9.0	4.00	4	ELECTRONIC CIRCUIT ANALYSIS	O	10.0	3.00					
5	SIGNALS AND SYSTEMS	O	10.0	3.00	5	PULSE AND DIGITAL CIRCUITS	A	8.0	3.00					
6	BASIC SIMULATION LABORATORY	A+	9.0	2.00	6	SWITCHING THEORY AND LOGIC DESIGN	A+	9.0	3.00					
7	ELECTRICAL ENGINEERING LABORATORY	O	10.0	2.00	7	ANALOG COMMUNICATIONS LABORATORY	A+	9.0	2.00					
8	ELECTRONIC DEVICES AND CIRCUITS LABORATORY	A	8.0	2.00	8	ELECTRONIC AND PULSE CIRCUITS LABORATORY	O	10.0	2.00					
9	GENDER SENSITIZATION (VALUE ADDED COURSE-2 CREDITS)	--	--	--										

I SEMESTER					III YEAR					II SEMESTER				
1	ANTENNAS AND WAVE PROPAGATION	D*	5.0	4.00	1	BUSINESS ECONOMICS AND FINANCIAL ANALYSIS	A+	9.0	3.00					
2	COMPUTER ORGANIZATION	A	8.0	3.00	2	DIGITAL SIGNAL PROCESSING	A+	9.0	4.00					
3	DIGITAL COMMUNICATIONS	B	7.0	3.00	3	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	O	10.0	3.00					
4	LINEAR AND DIGITAL IC APPLICATIONS	A	8.0	3.00	4	ENTREPRENEURSHIP	A	8.0	3.00					
5	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	A+	9.0	3.00	5	MICROPROCESSORS AND MICROCONTROLLERS	A	8.0	3.00					
6	ADVANCED ENGLISH COMMUNICATION SKILLS LABORATORY	A	8.0	2.00	6	MICROWAVE ENGINEERING	A	8.0	3.00					
7	DIGITAL COMMUNICATIONS LABORATORY	A	8.0	2.00	7	DIGITAL SIGNAL PROCESSING LABORATORY	O	10.0	2.00					
8	LINEAR AND DIGITAL IC APPLICATION LABORATORY	O	10.0	2.00	8	MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	A+	9.0	2.00					

I SEMESTER					IV YEAR					II SEMESTER				
1	ARTIFICIAL NEURAL NETWORKS AND FUZZY LOGIC	A	8.0	3.00	1	ADHOC WIRELESS NETWORKS	A+	9.0	3.00					
2	CELLULAR AND MOBILE COMMUNICATIONS	B	7.0	3.00	2	EMBEDDED REAL TIME OPERATING SYSTEMS	A	8.0	3.00					
3	COMPUTER NETWORKS	A	8.0	3.00	3	SATELLITE COMMUNICATIONS	A+	9.0	3.00					
4	INTERNET OF THINGS	B	7.0	3.00	4	COMPREHENSIVE VIVA - VOCE	B	7.0	2.00					
5	MANAGEMENT SCIENCE	A	8.0	3.00	5	PROJECT WORK	O	10.0	10.00					
6	VLSI DESIGN	C	6.0	3.00	6	TECHNICAL SEMINAR	O	10.0	2.00					
7	ECAD AND VLSI LABORATORY	A	8.0	2.00										
8	INDUSTRY ORIENTED MINI PROJECT	A+	9.0	2.00										
9	MICROWAVE ENGINEERING LABORATORY	O	10.0	2.00										

Number of Credits registered for **188** Aggregate Marks Secured for the best **181**

CGPA **8.70**

Date of Issue **07-06-2019**



(See overleaf for Rules concerned to award of class)

*(Courses registered but not counted for calculation of aggregate)

Controller of Examinations

Principal

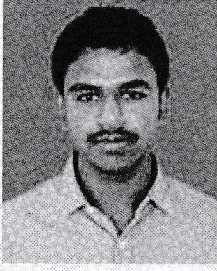


VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING & TECHNOLOGY
AN AUTONOMOUS INSTITUTE, AFFILIATED TO JNTUH, HYDERABAD
VIGNANA JYOTHI NAGAR, PRAGATHI NAGAR, NIZAMPET (S.O.), HYDERABAD - 500 090, TELANGANA STATE, INDIA.



TRANSCRIPT

SL.No.: TS020798



PROVISIONAL CERTIFICATE

Hall Ticket No. **15071A04F1**

PC No. 319040700463

This is to certify that Mr. SUHAS MADDALI, son of ANJANAVARAPRASAD MADDALI and SHARADA MADDALI, is qualified for the award of BACHELOR OF TECHNOLOGY in ELECTRONICS AND COMMUNICATION ENGINEERING, under regular mode, for having passed the prescribed examination held during April, 2019 in First Class with Distinction. He has satisfied all the requirements for the award of B.Tech degree by the Jawaharlal Nehru Technological University, Hyderabad.



CMM No. : 219040700463

Date : 07-06-2019

Controller of Examinations

Principal