

Top level
Direct conversion SDR prototype

Sheet: /
File: Akashi-07.sch

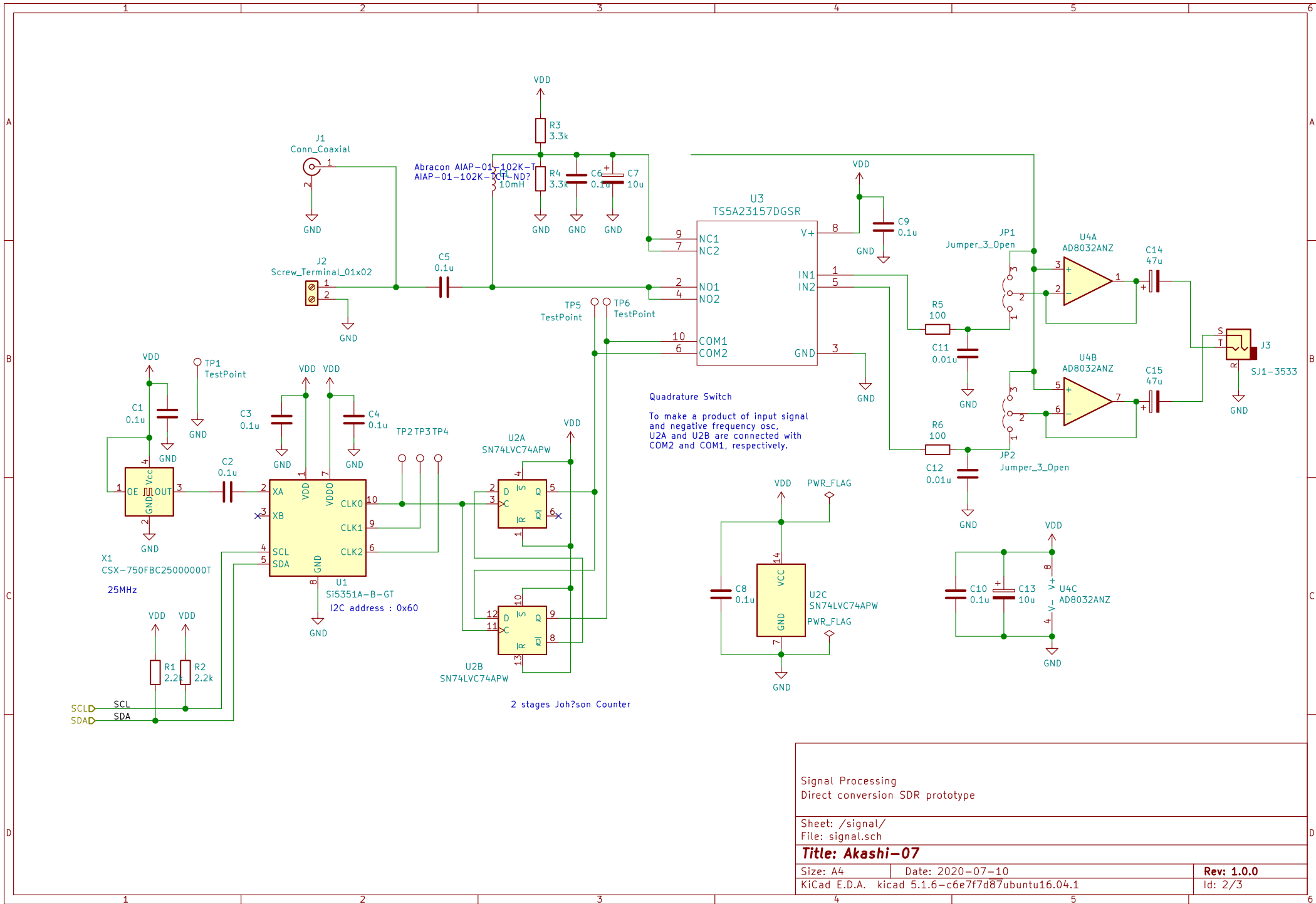
Title: Akashi-07

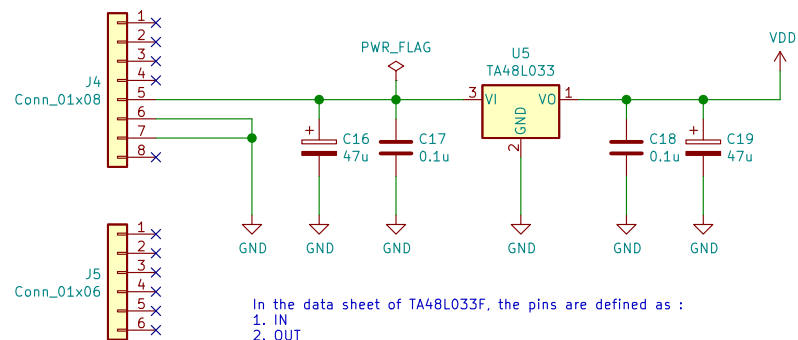
Size: A4 Date: 2020-07-10

Rev: 1.0.0

KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu16.04.1

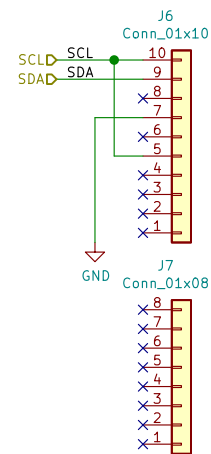
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In the data sheet of TA48L033F, the pins are defined as :
 1. IN
 2. OUT
 3. GND
 Tab. GND

But this definition doesn't fit to the SOT89-3 footprint of KiCAD. Thus, the pin assignment of the above symbol follow the KiCAD SOT89-3.



J6 pin 5 is "rescue" pin for I2C SCL.
 In case of some slave I2C SDA is stick to "L", toggling SCL by this rescue pin can release the slave.

This rescue pin have to be configured as open drain, and have to be kept as "H" during normal operation.

J6 pin 5 is PA6 of Nucleo G431RB and F722ZE

Connectors
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Sheet: /connector/
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