INTRODUCTION TO MURASAKI

An STM32 Peripheral Class Library

STAY HOME



CONTENTS





INTRODUCTION

WHO AM I?





WHAT IS MURASAKI?

Photo by Daniele Levis Pelusi on Unsplash

IN THIS SECTION

- What is Murasaki?
- What is the target processor?
- What is the development environment?
- What is the overview of the classes?

OVERVIEW

- A collection of the peripheral wrapper class
 - UART, SPI, I2C, ADC, Encoder, SAI, I2S, EXTI, GPIO
- RTOS aware classes are available
 - Task, Synchronizer, CriticalSection
- IO APIs are :
 - Wrapped by Class.
 - Synchronized : IO call returns when IO function is complete.
 - Blocking : If IO is occupied, wait until it becomes vacant.
- Hosted by GitHub
 - <u>https://github.com/suikan4github/murasaki</u>

TARGET

- STM32 microcomputer series.
- Core pressor is not matter.
- Providing one unified API through the STM32 variants

- Tested Target
 - STM32F0 : CORTEX-M0
 - STM32G0 : CORTEX-M0+
 - STM32L1 : CORTEX-M3
 - STM32F4 : CORTEX-M4
 - STM32G4 : CORTEX-M4
 - STM32F7 : CORTEX-M7
 - STM32H7 : CORTEX-M7

REQUIRED ENVIRONMENT

- Linux or Windows
 - Developed on Ubuntu 16.04 LTS
 - Windows 10 + WSL is confirmed
 - MacOS is not tested
- CubeIDE 1.3
 - Device Configuration Tool is essential
 - Makefile build must be acceptable. But not tested.
- Other tools
 - Doxygen
 - pdflatex
 - Terminal emanator

MOTIVATION

- A class library which doesn't bother the development.
 - Strict name space
 - Strict parameter typing
 - Synchronous and blocking IO API
 - Multi-task aware
 - Enough speed to support audio DMA data
- Strict parameter checking
- Context free printf()

OUT OF SCOPE

- Very quick response processing
 - Motor control
 - Power supply control
- Very low power operation
 - Mouse
 - TV remote controller
- Very small memory footprint
- High reliability application

PLATFORM AS ABSTRACT LAYER

- Murasaki provides dedicated class for each peripheral
- Application will use the peripheral through these class variable.
- Programmer can create these class variable as he/she wants.



CUBE IDE AND MURASAKI



PRINTF() DEBUG SUPPORT

- A dedicated Printf() function is available.
- Thread safe
- Bi-context
 - Can use from both task and interrupt context
- Buffered
 - As soon as text is stored in the buffer, Print() returns.



UART

- UART is packed in the Uart class.
- Transmit() / Receive () member functions are :
 - Synchronous
 - Blocking

155⊖ v	oid Test101Master(void) {
56	// Start UART simple test.
57	murasaki::platform.test_state = cmd_101;
58	murasaki::platform.test_success = false;
59	
60	<pre>murasaki::platform.sync command->Release(); // tell slave ta</pre>
61	
62	murasaki::platform.uart->Transmit(
63	tx data uart.
64	sizeof(tx data uart)
65):
66	<i>//</i>
67	murasaki::platform_sync_ack=>Wait():
00	muracaki i debuggar Nrietf(

I2C

- I2C is packed in the I2cMaster and I2cSlave class.
- Transmit () / Receive() member functions are :
 - Synchronous
 - Blocking

Т	murasaki::platform.i2c master->Transmit(
L	i2c device,
	tx_data_i2c,
	<pre>sizeof(tx_data_i2c),</pre>
L	&transfered count):

SPI

- SPI is packed in the SpiMaster and SpiSlave class.
- TransmitAndReceive() member function is :
 - Synchronous
 - Blocking

<pre>murasaki::platform.sync_command->Release();</pre>	// tell slave task next test
murasaki::platform.spi_master->TransmitAndRe	ceive(murasaki::platform.slave_adapter, tx_data_spi_master, rx_data_spi_master, sizeof(tx_data_spi_master));
1.0	

BY THE WAY, SPI IS CONSIDERED HARMFUL

- The clock polarity and the clock phase is up to the slave.
 - For each time to access different slave, the master have to be re-configured.
- Muarsaki uses the SpiSlaveAdapter class to specify these configuration.



AUDIO

- Audio is packed in the DuplexAudio class.
- TransmitAndReceive() member functions is :
 - Synchronous
- DuplexAudio is not blocking IO.
 - This IO doesn't assume multiple task access the IO randomly
- Both I2S and SAI port are supported

220	wh	ile (true) // Talk Through
222		<pre>// Wait the end of current audio transmission & receive. // Then, copy the tx buffer to tx DMA buffer.</pre>
224 225		// And then copy the <u>rx DMA buffer to rx buffer</u>
226		tx_left,
228		rx_left,
229 230		// Copy RX to TX : talk through
231 232		<pre>for (int i = 0; i < AUDIO_CHANNEL_LEN; i++) { tx left[i] = rx left[i];</pre>
233		<pre>tx_right[i] = rx_right[i]; }</pre>
235		
236		murasaki::platform.led_st0->Toggle();
238 239	}	murasaki::platform.led_st1->Toggle();
240	}	

GPIO

- GPIO class
 - GpIn
 - GpOut
- Simple bit operations



OTHER PERIPHERALS

- ADC is packed in the Adc class.
- Convert member function is :
 - Synchronous
 - Blocking

- EXTI is packed in the Exti class.
- Wait member function is :
 Synchronous
- Exti is not blocking.
 - Only one task wait for specific interrupt.

MULTI-TASKING

- Task is easy to create.
- Syncronizer class for
 - Wait
 - Release
- CriticalSection class for
 - Inter-task exclusive resource access.





AUTOMATIC INTERRUPT HANDLING

- Peripheral interrupts are handled internally.
- Programmer doesn't need to care.



IMPORT AND RUN

Photo by Daniele Andy Li on Unsplash

IN THIS SECTION

- We fetch the sample programs from github, and import
- Run the program
- Walkthrough the program
- See the debug functionality

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1.OPNE A NEW WORKSPACE

Create a new workspace

8 STM32CubeIDE Launcher				
Select a directory as workspace				
STM32CubeIDE uses the workspace directory to store its preferences and development artifacts.				
Workspace: ⁹ /home/takemasa/STM32CubeIDE/workspace_foo				
Recent Workspaces				
Copy Settings				
? Cancel Launch				

2.OPEN THE GIT PERSPECTIVE

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3.COPY AN URL OF THE REPOSITORY

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4.CLONE THE REPOSITORY

ce_foo - STM32CubeIDE
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Select one of the following to add a repository to this view:
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Create a new local Git repository

😣 🗊 Clone Git Repository				
Source Git Repository				
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Repository path: /suika	n4github/murasaki_samples.git			
Connection Protocol: https Port:				
Authentication				
User:	suikan4github			
Password:				
Store in Secure Store				
	Next, Next and then Finish			
?	< Back Next > Cancel Finish			

5.UPDATE SUBMODULES



6.START TO IMPORT



😣 🗇 Import
Select
Import one or more projects from a Git repository.
Select an import wizard:
type filter text
 ▷ General ▷ C/C++ ▼ Git
🕮 Projects from Git
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7.SPECIFY THE REPOSITORY

Import Projects from Git Select Repository Source Select a location of Git Repositories	Tł	nis is the one we cloned at step 4
type filter text Existing local repository Clone URI	Select a Git Repository	GIT
(?) < Back Next > ()	You can also add other local repositories to the list type filter text i murasaki_sample [master] - /home/takemasa/git,	 Import Projects from Git Select a wizard to use for importing projects Depending on the wizard, you may select a directory to determine the wizard's scope Wizard for project import Import existing Eclipse projects Import using the New Project wizard Import as general project
		? < Back Next > Cancel Finish

8.SPECIFY A PROJECT TO IMPORT

- 1. Uncheck "Search for nested project"
- 2. Click "Deselect All"
- 3. Check desired project.
- 4. Click "Finish"

😣 💿 Import Projects from Git	
Import Projects	GIT
Import projects from a Git repository	
<u>P</u> rojects:	
type filter text to filter unselected projects	Select All
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9.TRIAL BUIDL

- Now, we have a sample project in the workspace
- Ctrl-B to build

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10.START THE DEBUGGER

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▶ 🚑 murasaki/Src ▶ 👝 Debug ▶ 🔄 Inc	 Toggle Breakpoint Toggle Line Breakpoint Toggle Watchpoint 	
▼	Toggle Method Breakpoint Skin All Breakpoints	

Make sure the Nucleo is connected through USB
11.READY TO RUN

 Make sure a terminal emulator is waiting the serial communication



12.RUN THE PROGRAM



🕽 🗐 🔲 takemasa@vm: ~/git/shared_foo/script takemasa@vm:~/git\$ cd shared_foo/ takemasa@vm:~/git/shared_foo\$ ls book-introducing-python halide study kica jupyter-notebook mana cpp-opencv takemasa@vm:~/git/shared foo\$ cd script/ takemasa@vm:~/git/shared_foo/script\$ ls configure-github-global configure-github-local v2 terminal takemasa@vm:~/git/shared_foo/script\$./terminal C-Kermit 9.0.302 OPEN SOURCE:, 20 Aug 2011, for Copyright (C) 1985, 2011, Trustees of Columbia University in the City o Type ? or HELP for help. (/home/takemasa/git/shared_foo/script/) C-Kermi Connecting to /dev/stlink_console, speed 115200 Escape character: Ctrl-\ (ASCII 28, FS): enabl Type the escape character followed by C to get or followed by ? to see other options. WW!!! Push blue button to start the demo

13.PUSH THE BLUE BUTTON



😕 亘 🗉 takemasa@vm: ~/git/shared_foo/script

(/home/takemasa/git/shared_foo/script/) C-Kermit>c Connecting to /dev/stlink_console, speed 115200 Escape character: Ctrl-\ (ASCII 28, FS): enabled Type the escape character followed by C to get back, or followed by ? to see other options.

W!!!! Push blue button to start the demo

Probing I2C devices

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TWO STRUCTURES OF PROJECT

- The CubeIDE introduce
 "Advanced Project
 structure"
- In the "Advanced" structure, the Src directory is under the Core directory.
- Let's open the murasaki_platform.cpp file



Old structure

Advanced structure

INSIDE MURASAKI_PLATFORM.CPP

- InitPlatform()
 - Initialization of Murasaki
 - Programmer must edit this function to initialize his/her platform
- ExecPlatform()
 - Execution body of application.



INSIDE EXECPLATFORM()

- 1. Start a new task
- 2. Print a message
- 3. Then, wait for the blue button
 - 1. This task halt here and wait for the interrupt from blue button

Type the escape character followed by C to get or followed by ? to see other options.

WW!!! Push blue button to start the demo



AFTER BUTTON PUSHED

• An utility function I2cSerch() is called.

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102© void ExecPlatform() 103 { 104 // counter for the demonstration

104	// counter for the demonstration.
105	int count = 0;
106	
107	// Start LED blink
108	<pre>murasaki::platform.task1->Start();</pre>
109	
110	// waiting for the Button push.
111	<pre>murasaki::debugger->Printf("!!! Push blue button to start the demo ¥n")</pre>
112	<pre>murasaki::platform.b1->Wait();</pre>
113	
114	<pre>// List up connected I2C device to the console.</pre>
115	I2cSearch(murasaki::platform.i2c_master);
116	
117	// Loop forever
118	while (true) {
119	
120	<pre>// print a message with counter value to the console.</pre>
121	murasaki::debugger->Printf("Hello %d ¥n", count);
122	
123	// update the counter value.
124	count++;
125	
126	// wait for a while
127	murasaki::Sleep(500);
128	}
129	}
130	

INSIDE STARTED TASK

 The started task just blinks LED.





POWER OF NAME SPACE

- Alt-/ shows the candidate of the keywords/identifiers.
- This makes programming easy
- Strict namespace of Murasaki narrowing down the candidate by minimum timing.



DEBUG : PRINTF

Let's add Murasaki::debugger->Print() and run



Escape character: Ctrl-\ (ASCII 28, FS): enab Type the escape character followed by C to get or followed by ? to see other options.

HIL Push blue button to start the demo Hello, 2020

DEBUG : ASSERTION

Let's add Murasaki::debugger->Print() and run



DEBUG : SYSLOG

Let's add Murasaki::SetSyslogFacilityMask() and SetSyslogSeverityThreshold()

20 vo	id ExecPlatform()	
	<pre>// counter for the demonstration. int count = 0;</pre>	
	// Start LED blink murasaki::platform.task1->Sta <u>r</u> t();	
	murasaki::SetSyslogFacilityMask(murasaki:: <i>kfaExti</i>); murasaki::SetSyslogSererityThreshold(murasaki:: <i>kseDebug</i>);	
	<pre>// waiting for the Button push. murasaki::debugger->Printf("!!! Push blue button to start t murasaki::platform.b1->Wait();</pre>	the der
	<pre>// List up connected I2C device to the console. I2cSearch(murasaki::platform.i2c master);</pre>	

!!! Push blue button to start the demo 66164, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 199, Wait(): Enter
557825001, 0, kfaExti, kseDebug: exti.cpp, line 256, isReady(): Enter
557837878, 0, kfaExti, kseDebug: exti.cpp, line 264, isReady(): Exit with true
557851576, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 238, Match(): Enter
557864315, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 241, Match(): Matched. Exit with true
557878601, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 218, Release(): Enter
557891519, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 238, Match(): Enter
557904255, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 241, Match(): Matched. Exit with true
557918800, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 221, Release(): Matched and release
557933484, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 225, Release(): Exit with true.
557951601, 0x200023e8, kfaExti, kseDebug: exti.cpp, line 211, Wait(): Exit with 0

DEBUG: TASK STACK HEADROOM

Let's add member functions of TaskStrategy class



takemasa@vm: ~/git/shared_foo/script !!! Push blue button to start the demo Stack headroom is -1/256



WE NEED ADDITIONAL SETTINGS

Pinout & Configur	ation	Clock Configurat	tion	Project Manager
	Additio	onal Software	\sim	Pinout
Q v :		FREERTOS Mod	e and Conf	iguration
Categories A->Z		Confi	guration	
System Core >	Reset	t Configuration		
Analog >	© 0	Mutexes Tasks and Queues	SreeR1	FOS Heap Usage here and Semaphores
Timers >	•	Advanced settings Config parameters		User Constants Include parameters
Connectivity >		h (Crtl+F)		0
Multimedia >		USE_MALLOC_FAILED_I	ноок [Disabled
Computing >		USE_DAEMON_TASK_S CHECK_FOR_STACK_OV	TARTU [Disabled Option1 V
Middleware 🗸	✓ Kun t	GENERATE_RUN_TIME	STATS	Option1 Option2
•		USE STATS FORMATTI	NG FU [Disabled
FREERTOS	∨ Co-ro	outine related definitions		
LIBJPEG		USE_CO_ROUTINES	[Disabled
MBEDILS	_	MAX CO ROLITINE PRI	IORITIES (2

۹	\sim	FREERTOS Mode and Configuration	
Categories A->	≻Z	Configuration	
System Core 🗦	>	Reset Configuration	
Analog >	>	Mutexes Solution Mutexes Solution So	
Timers >	>	 Advanced settings Config parameters Include parameters 	
Connectivity >	>	Configure the below parameters :	
Multimedia >	>	VI ask Delay Enamed xTask Get Scheduler State Enabled	
Computing	>	xTaskResumeFromISR Enabled xQueueGetMutexHalder Disabled	
Middleware 🚿	_	xSemaphoreGetMutex Disabled	
\$		uxTaskGetStackHighW Disabled	7
FATFS FREERTOS LIBJPEG	•	xTaskGetCurrentTaskH., Disabled eTaskGetState Enabled	
MBEDTLS PDM2PCM		xEventGroupSetBitFro Disabled 1 xTimerPendFunctionCall Disabled	

RESULT OF TASK HEADROOM

- We can see the "rest of stack" for each task.
- The unit is byte.
- Check FreeRTOS
 Configuration for details :
 - configCHECK_FOR_STACK_OVE RFLOW



DEBUG : HARD FAULT

1029	void ExecPlatform()
103 104	{ // counter for the demonstration.
105	int count = 0;
107	// Start LED blink murasaki::platform.task1->Start();
110 111 112 113	// waiting for the Button push. murasaki::debugger->Printf("!!! Push blue button to sta murasaki::platform.b1->Wait();
114 115	typedef void (*FUNCTYPE)(void);
116 117 118	<pre>FUNCTYPE funcPtr = reinterpret_cast<functype>(0); funcPtr();</functype></pre>
119 120 121	<pre>// List up connected I2C device to the console. I2cSearch(murasaki::platform.i2c_master);</pre>
122	// Loop forever

	takema	asa@vm: ~/git/shared_foo/script	
!!! Push	blue	button to start the demo	
Sourious	excer	ption or bardfault occured	
Stackod			
Stacked		0x0000000	
Stacked	KT :		
Stacked	RZ :	0X00000007	
Stacked	R3 :	0×00000000	
Stacked	R12 :	0xA5A5A5A5	
Stacked	LR :	0x080087F7	
Stacked	PC :	0×00000000	
Stacked	PSR :	0x40000000	
С	FSR :	0x00020000	
н	FSR :	0x40000000	
D	FSR :	0×0000000	
Δ	FSR :	0×00000000	
Â	MAR ·	0xE000ED34	
P			
		oid the stacking by C compiles use of	1-
(Note :		old the stacking by C compiler, use r	ete
Duce P	0110	AUTTON TO FFIET THO domo	



DOCUMENTATION

Photo by Debby Hudson on Unsplash

IN THIS SECTION

- Location of PDF document
- Making Doxygen document
- Tour of Doxygen document
- WIKI

LOCATION OF PDF DOCUMENT

- murasaki/doc
- PDF document is same contents with HTML document
- Just a matter of format



MAKING DOXYGEN DOCUMENT

Install Eclox, to create the document

🖻 🕶 🔚 🕼 🥸 🕶 🐔 🕶 📾 📸 🕶 🗳 🕶 🗳 🕶 🚱	@ •	F 🕶
Project Explorer 🛛 📄 🔄 🔽 🖳 🗖	🛯 🖻 muras	aki_
• 📭 > nucleo-f446-64 [murasaki_sample master]	91 92	11
▶ ﷺ Binaries	93	11
▶ 🔊 Includes	94	mur
▶ 🖓 > Drivers	95	MUF
Middlewares	97	mur
	98	MUF
▶ 📴 > 2LC	99	
🕨 🚑 Startup	100 }	



HTML DOCUMENTATION

- Classes and Functions are grouped by "Module"
- "Murasaki Class Collection" module is a list of the peripheral classes.
 - Usually, application programmer uses classes in this module.

Murasaki Class Library 3.0.0

A peripheral controll class collections for the STM32 microprocessor fa

Main Page	Related Pages	Modules	Namespaces	Classes
Modules				
Here is a list o	f all modules:			
Murasaki AF	Pl reference		Murasa	ki API referer
Murasaki	Class Collection		STM32	Class library
Third par	ty classes		Classes	for the third
Definition	ns and Configuration	n	Definition	ons and cont
Application	on Specific Platform	1	Variable	es to control
Abstract	Classes		Generic	classes as t
Synchron	ization and Exclusiv	e access	Sync be	tween the ta
Helper cl	asses		Classes	to support t
Utility fur	octions		Collecti	on of the us
CMSIS				

MURASAKI CLASS COLLECTION

- Peripheral like UART, SPI, I2C, ADC, GPIO, SAI, I2S are controlled by these classes.
- Algorithm class like SimpleTask or DuplexAudio are also listed here.

Main P	age	Relat	ed Pages	Modules	Namespaces	Classes	Files
Mura ^{Aurasaki}	API refere	Class	Collec	ction			
атмз2	Class I	ibrary. N	lore				
Collabo	oration	diagram	for Muras	saki Class Colle	ction:		
				M	urasaki API reference	e 🚽 🛛 Mu	rasaki Clas
Class	ses						
class	muras STM3	aki::Ado 2 dedic	ated ADC	class. More			
class class	muras STM3 muras Gener	aki::Add 2 dedic aki::Bitl	ated ADC n ose bit inp	class. More out. More			
class class class	muras STM3 muras Gener muras Gener	aki::Add 2 dedic aki::Bit al purpo aki::Bit al purpo	ated ADC n pse bit inp Dut pse bit out	class. More put. More tput. More			
class class class class	muras STM3 muras Gener muras Gener muras Debu	aki::Add 2 dedic aki::Biti al purpo aki::Biti al purpo aki::Del g class.	c ated ADC n ose bit inp Out ose bit our bugger Provides p	class. More put. More tput. More printf() style out	put for both task a	nd ISR context	. More
class class class class class	muras STM3 muras Gener muras Debug muras Stereo	aki::Add 2 dedic aki::Bitl al purp aki::Bitl al purp aki::De g class. aki::Du o Audio	ated ADC n Dose bit inp Dut bugger Provides p plexAudio is served l	class. More but. More tput. More printf() style out	put for both task an	nd ISR context	:. More
class class class class class class	muras STM3 muras Gener muras Gener muras Stereo EXTLN	aki::Add 2 dedic aki::Bitl al purp aki::Bitl aki::De g class. I aki::Du o Audio aki::Ext vrapper	c ated ADC n ose bit inp Dut ose bit out bugger Provides p plexAudio is served l i class. Mo	class. More but. More tput. More printf() style out by this class. Mo re	put for both task an	nd ISR context	. More

DEFINITIONS AND CONFIGURATION

- These macros configures Murasaki
 - Buffer size
 - Use of Debug
- To change the configuration, define these macros in the platform_config.hpp

Definitions and Configuration Murasaki API reference					
Definitions and configuration collection of murasaki platform. More					
Collaboration diagram for Definitions and Configuration:					
Murasaki API	reference				
#define PLATFORM_CONFIG_DEBUG_LINE_SIZE 25 Size of one line[byte] in the debug printf. Mor	6 e				
#define PLATFORM_CONFIG_DEBUG_BUFFER_SIZE Size[byte] of the circular buffer to be transmit	PLATFORM_CONFIG_DEBUG_BUFFER_SIZE 4096 Size[byte] of the circular buffer to be transmitted through the serial port. More				
#define PLATFORM_CONFIG_DEBUG_SERIAL_TIMEO Timeout of the serial port to transmit the strin	PLATFORM_CONFIG_DEBUG_SERIAL_TIMEOUT (murasaki::kwmsIndefinitely Timeout of the serial port to transmit the string through the Debug class. More				
#define PLATFORM_CONFIG_DEBUG_TASK_STACK_S Size[Byte] of the task inside Debug class. Mor	PLATFORM_CONFIG_DEBUG_TASK_STACK_SIZE 256 Size[Byte] of the task inside Debug class. More				
#define PLATFORM_CONFIG_DEBUG_TASK_PRIORIT The task proiority of the debug task. More	PLATFORM_CONFIG_DEBUG_TASK_PRIORITY murasaki::ktpHigh The task proiority of the debug task. More				
#define MURASAKI_CONFIG_NODEBUG false Suppress MURASAKI_ASSERT macro. More					
#define MURASAKI_CONFIG_NOCYCCNT false Doesn't run the CYCCNT counter. More					
#define NUM_OF_CALLBACK_OBJECTS 8 The number of the interrupt callback handling	NUM_OF_CALLBACK_OBJECTS 8 The number of the interrupt callback handling objects. More				
#define NUM_OF_EXTI_OBJECTS 8 The number of the EXTI callback handling ob	jects. More				

SYNCHRONIZATION AND EXCLUSIVE ACCESS

- Inter-task synchronization
- Interrupt vs. task Synchronization
- Inter-task exclusive acsses.

Synchronization and Exclusive access

Murasaki API reference

Sync between the task and interrupt. Make the resources thread safe. More...

Collaboration diagram for Synchronization and Exclusive access:

Murasaki API reference

Classes

class	murasaki::CriticalSection A critical section for task context. More
class	murasaki::Synchronizer Synchronization class between a task and interrupt. This class provid

UTILITY FUNCTIONS

- Cycle counter control
- I2C device search
- Other functionality may added



USAGE GUIDES

- Beside of APIs, Murasaki has different aspect of documents.
 - Usage Introduction
 - Program flow explanation
 - Porting Guide

Main Page Related Pages Module **Related Pages** Here is a list of all related documentation pa Preface Simplified IO Preemptive multi-task Synchronous IO Thread-safe IO Versatile printf() logger Guard by assertion System Logging Configurable **Target and Environment** Usage Introduction Message output Serial communication Debugging with Murasaki. Tasking Other peripherals I2C Master **I2C Slave** SPI Master SPI Slave GPIO Duplex Audio Program flow

Program flow

Application flow HAL Assertion flow Spurious Interrupt flow Assertion flow General Interrupt flow EXTI flow Porting guide Directory Structure CubeIDE setting

> Heap Size Stack Size Task stack size of the default task UART peripheral SPI Master peripheral I2C peripheral I2C peripheral EXTI Configuration Task Priority and Stack Size Heap memory consideration Platform variable Routing interrupts Error handling Summary of the porting

CONFIGURATION OF PERIPHERAL

- Also, each peripheral class describes :
 - How to configure the device
 - Interrupt handling
 - IO operations

Detailed Description

The I2cMaster class is the wrapper of the I2C controller.

Configuration

To configure the I2C peripheral as master, chose I2C peripheral in the Device (NVIC interrupt.



Also, pay attention to the I2C Maximum Output Speed. The default setting by C with an oscilloscope.



WIKI

- Murasaki project has its own wiki.
- Supplemental information will be placed here







HOW TO CREATE YOUR OWN APPLICATION

Photo by Sneaky Elbow on Unsplash

IN THIS SECTION

- Create a new project to your Nucleo
- Configure the device by CubeIDE Device Configuration Tool
- Clone Murasaki into the project
- Install Murasaki
- Set up the project to use Murasaki
- Build
- Adjust to the target Nucleo

CREATE AN C++ PROJECT FOR YOUR NUCLEO

- First of all, create a new STM32 Project into your work space.
- File -> New -> STM32
 Project



SPECIFY THE CORRECT NUCLEO NAME

- Follow the procedure :
 - 1. Select board selector
 - 2. Type the name of the Nucleo to the Number search
 - 3. Select correct Nucleo board
 - 4. Then, click "Next"

😣 🗉 STM32 Project	
Target Selection	
Select STM32 target	
MCU/MPU Selector Board Selector Cross Se	lector
Board Filters	
Part Number Search 🗸 🗸	*
Q 446 VINCLEO-F446RE NUCLEO-F446ZE Ven STM 32446E-EVAL	
Type >	

SPECIFY THE PROJECT NAME

- Follow the procedure :
 - 1. Specify the name in to "Project Name"
 - 2. Make sure to select the "C++" as the Target Language
 - 3. Then click "Finish"
 - 4. Regarding the default pin state and Eclipse perspective, click OK for a while
 - 5. Now, the Device Configuration Tool appears

Setup STM32 project						
Project Project Name: 🗹 Use default	my-nucleo-446					
Location: /home/takemasa/STM32CubeIDE/workspace_foo Browse Options Targeted Language O C O C++						
Targeted Binary Type Executable Static Library Targeted Project Type STM32Cube Empty						
?	< Back Next > Cancel Finish					

CONFIGURE THE FREERTOS

- Choose CMSIS_V1 as FreeRTOS interface.
- Set the Minimal_stack_size
 as 256
 - This will increase the stack size
 of the default task



CONFIGURE THE HEAP SIZE

- Set TOTAL_HEAP_SIZE 12kB or more.
- Murasaki uses FreeRTOS heap for all activity.
- At least 12kB is require.

Pinout & Configuration		Clock Configuration		
			Additional Software	
Q	~ Ø	FREERTOS Mode and Configuration		
Categories A->2	Z	Mode		
System Core	>	Interface CMSIS_V1	~	
Analog	>			
Timers	>	Cor	nfiguration	
Connectivity	>	Reset Configuration	Mutexes 📔 😔 FreeRTOS Heap Usage	
Multimedia	>	 Oser Constants Config parameters Inclu 	│	
Computing	>	Configure the below parameters :	A	
Middleware	~	USE_TICKLESS_IDLE	Disabled	
\$			D Enabled	
FATES	_	RECORD_STACK_HIGH_	Disabled	
LIBJPEG		Memory Allocation	Dvnamic / Static	
MBEDTLS		TOTAL_HEAP_SIZE	15360	
USB DEVICE		Memory Management sc	heap_4	
USB_HOST		imes Hook function related definitio	ns	

CONFIGURE THE UART

- Nucleo Uses one UART as USB serial port.
- The port is up to the Nucleo
 - UART
 - USART
 - LPUSART
- By default, appropriate port is asynchronous, by CubeIDE
- Make sure the parameters are:
 - 115200bps
 - 8bit
 - No parity
 - 1 stop bit


CONFIGURE THE UART NVIC

Check the Global interrupt



CONFIGURE THE UART DMA

- Follow procedure :
 - 1. Select the DMA settings
 - 2. Add a DMA
 - 3. Configure it as TX
 - 4. Add one more DMA
 - 5. Configure it as RX
 - 6. Leave the Mode and Width as default
 - Normal
 - Byte

USART2 Mode and Configuration					
		Mode			
Mode Asynchronous				\sim	
Hardware Flow Control	(RS232) Disable			\sim	
	Con	figuration	_		
	Con	Ingulation			
Reset Configuration					
🥺 Parameter Settings 🛛 (🥝 User Constants 🛛 📀 🛛	NVIC Settin <mark>gs 🛛 🥝</mark> DMA Se	ttings 🧧 🧕	GPIO Settings	
DMA Request	Stream	Direction		Priority	
USART2_RX	DMA1 Stream 5	Peripheral To Memory	Low		
USART2_TX	DMA1 Stream 6	Memory To Peripheral	Low		
Add Delete					
CDMA Request Settings—					
		Peri	pheral	Memory	
Mode Normal	\sim	Increment Address			
Use Fifo 🔲 Threshol	Id 🗸 🗸	Data Width Byte	\sim	Byte ∨	

CHANGE THE TIME BASE

- QubeHAL uses its own time base
 - By default, this time base is SysTick
 - Also FreeRTOS uses it.
- Follow the procedure :
 - 1. Select "System Core"
 - 2. Select "SYS"
 - 3. Choose any Timebase Source except SysTick



CONFIGURE THE CLOCK (F722 ONLY)

- CubeIDE has bug of Nucleo F722 clock configuration
- Follow the procedure to fix :
 - 1. Select "Clock Configuration"
 - 2. Change the "Input Frequency" to 8 MHz.
 - By default, it is 25MHz



CHECK THE NAME OF LED PIN

- We use LED pin in the Skelton code
- Check the name of LED pin
 - It is board dependent



GENERATE A CODE

- Now, time to generate a code.
- Once generated, type Ctrl-B to build.
- Build must be OK.



OPEN THE PROJECT LOCATION

- This is most tricky part.
- If you have installed EasyShell, execute "Open with default Application"
 - On Linux, project location is opened by Nautilus file browser
 - On Windows, project location is opened by command prompt window
- If you have not installed EasyShell, go to the project by yourself



OPEN THE SHELL WINDOW

- Linux only
- From the context menu open the shell window

workspace_foo	my-nucleo-446 →	۹		
Core	Debug	Drivers		
	C	C		
Middlewares	STM32F446RETX_ FLASH.ld	STM32F446RETX_ RAM.ld		
<i>r</i> -nucleo-446.ioc	新しいフォル 新しいドキュ	·ダー(F) メント(D)	>	
	消去したファイルを復元 端末で開く(E)			
	貼り付け(P)			

SHELL WINDOW IS OPEN

- Shell window is located at project
- Make sure the project contents exist
- Now, we are ready to clone the Murasaki repository

COPY THE REPOSITORY URL



CLONE THE REPOSITORY

Lakemasa@vm: ~/STM32CubeIDE/workspace_foo/my-nucleo-446 takemasa@vm:~/STM32CubeIDE/workspace_foo/my-nucleo-446\$ ls Core Drivers STM32F446RETX FLASH.ld my-nucleo-446.ioc Debug Middlewares STM32F446RETX_RAM.ld takemasa@vm:~/STM32CubeIDE/workspace_foo/my-nucleo-446\$ git clone https://github .com/suikan4github/murasaki.git Cloning into 'murasaki'... remote: Enumerating objects: 197, done. remote: Counting objects: 100% (197/197), done. remote: Compressing objects: 100% (179/179), done. remote: Total 1898 (delta 119), reused 91 (delta 15), pack-reused Receiving objects: 100% (1898/1898), 12.06 MiB | 5.31 MiB/s, done Resolving deltas: 100% (1341/1341), done. Checking connectivity... done. takemasa@vm:~/STM32CubeIDE/workspace_foo/my-nucleo-446S

git clone https://github.com/suikan4github/murasaki.git

INSTALL MURASKI TO PROJECT



WHY DO WE NEED INSTALLATION?

- There are several point which CubeIDE Skelton calls Murasaki.
 - InitPlatform()
 - ExecPlatform()
 - HAL's assertion failure hook
 - Spurious Interrupt
 - Hard fault
- These call must be coded by programmer
 - No weak binding routines
- The installer script is the best way to avoid the routine works

REFRESH THE PROJECT

- While we installed on the shell command, Eclipse doesn't know that
- Follow the procedure :
 - 1. Select project
 - 2. Open the context menu
 - 3. Execute "Refresh"
 - 4. Now, you can see "Murasaki" in the project



OPEN THE PROPERTY

- Now, we open the project to set :
 - Include directory
 - Source directory



ADD INCLUDE PATH

- Follow the procedure :
 - 1. Open "C/C++ General"
 - 2. Select "Paths and Symbols"
 - 3. Select "Includes tab"
 - 4. Select "GNU C++"
 - 5. Click "Add"
 - 6. Write "murasaki/Inc"
 - 7. Click "OK"



murasaki/Inc

ADD SOURCE LOCATION

- Follow the procedure :
 - 1. Select "Source Location"
 - 2. Click "Add Folder…"
 - 3. Select "murasaki/Src"
 - 4. Click "OK"



BUILD MAY FAIL

- The result of build is up to Nucleo board
 - F722, F746, H743 : No error
 - Nucleo 64 :
 - UART port may be different
 - LD name may be different
- Fix depends on the target.

🛐 Problems 🧔 Tasks 💷 Console 🛛 🔲 Properties	Ŷ	û 🕏		6
CDT Build Console [my-nucleo-446]				
arm-none-eabi-gct "/Core/Src/murasaki_platform.cpp" -mcpu=cortex-m4 -std=gnl arm-none-eabi-gcc "/Core/Src/stm32f4xx_hal_msp.c" -mcpu=cortex-m4 -std=gnul arm-none-eabi-gcc "/Core/Src/stm32f4xx_hal_timebase_tim.c" -mcpu=cortex-m4 arm-none-eabi-gcc "/Core/Src/stm32f4xx_it.c" -mcpu=cortex-m4 -std=gnull -g3 arm-none-eabi-gcc "/Core/Src/syscalls.c" -mcpu=cortex-m4 -std=gnull -g3 -DU arm-none-eabi-gcc "/Core/Src/system_stm32f4xx.c" -mcpu=cortex-m4 -std=gnull -g3 -DUSE arm-none-eabi-gcc "/Core/Src/system_stm32f4xx.c" -mcpu=cortex-m4 -std=gnull -g3 -DUSE arm-none-eabi-gcc "/Core/Src/system_stm32f4xx.c" -mcpu=cortex-m4 -std=gnull -g3 -DUSE arm-none-eabi-gt - 0 "my-nucleo-446.elf" @"objects.list" -mcpu=cortex-m4 -T Core/Src/murasaki platform.o: In function InitPlatform :	u++ 1 -9 -st -DI SE_I _HAI -9 "/h	14 -93 - g3 -DUSE d=gnu11 USE_HAL_ HAL_DRIV L_DRIVEF 3 -DUSE_ ome/take	-DUSE_H _HAL_I _g3 -I DRIVEF /ER -DS R -DSTI _HAL_DF emasa/S	HAL DRI DUSI R -1 STM: STM: RIVI STM:
/home/takemasa/STM32CubeIDE/workspace_foo/my-nucleo-446/Debug//Core/Src/mur	asal	ki_plat†	form.c	pp:
collect2: error: ld returned 1 exit status				
"make -j4 all" terminated with exit code 2. Build might be incomplete.				
22:44:48 Build Failed. 2 errors, 0 warnings. (took 3s.600ms)				

FIX THE UART PORT IDENTIFIER

- The correct UART port is defined in main.c
- Apply this identifier to the murasaki_platform.cpp

🖻 main.c ន

44

45

46

49

43 /* USER CODE END PM */



ADJUST THE LED IDENTIFIER

- The LED name is generated by the Device Configuration Tool
- Port name and Pin name have to be adjusted
 - LDx_GPIO_Port
 - LDx_Pin





FINALLY WE CAN RUN

- Build the target
- Debug the target
- And then, resume

😕 🗢 💿 takemasa@vm: ~/git/shared_foo/script
Escape character: Ctrl-\ (ASCII 28, FS): enabled Type the escape character followed by C to get back, or followed by ? to see other options.
'⊕Hello 0 Hello 1 Hello 2 Hello 3 Hello 4 Hello 5 Hello 6

FURTHER PROGRAMING WITH MURASAKI

- Platform variable is defined in Inc/platform_def.hpp
 - Programmer must modify the Murasaki::Platform type
 - And must configure the device by Device Configuration Tool
 - And then Initialize them
- There is no Global Interrupt Mask
 - Murasaki assumes the peripheral IO control is in task context
 - Thus, inter-task exclusive access is enough
- If you need to control the IO which is not covered by Murasaki
 - You can control them through the HAL in a Murasaki task
 - You can use its HAL callback as you want
 - Murasaki doesn't interfere such the IO



SUMMARY

Photo by Aaron Burden on Unsplash

MURASAKI

- A class library for STM32 series
 - Multi-task native
 - Synchronous and blocking IO
 - Strict name space and IDE's name completion helps coding
 - Automatic interrupt handling
 - Rich debugging method
- Supporting multiple STM32 MCU series
- Hosted and managed by GitHub repository / tools
- Documentation by Doxygen



THANKS & STAY HOME