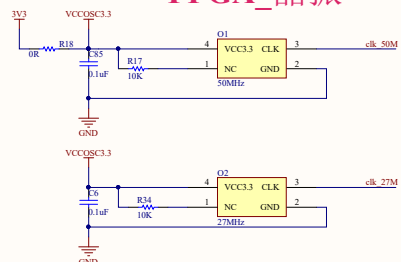
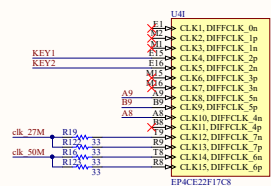
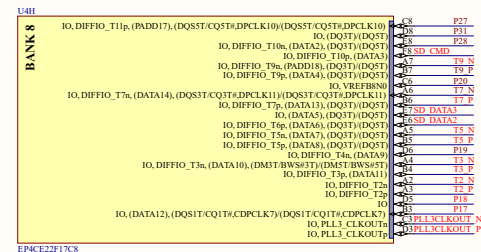
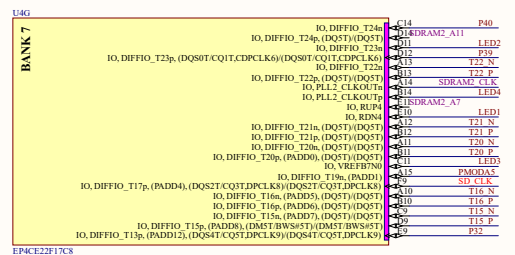
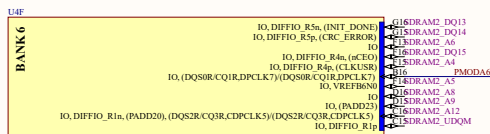
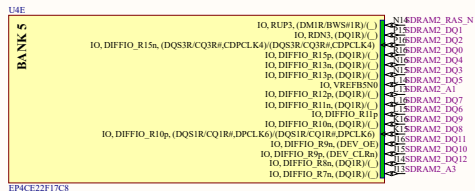
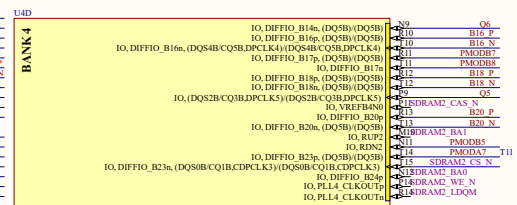
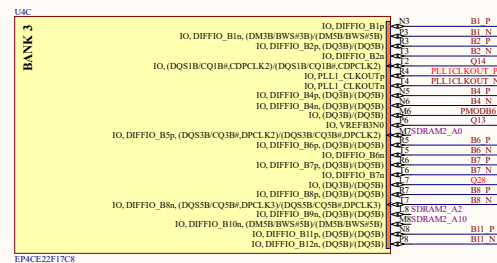
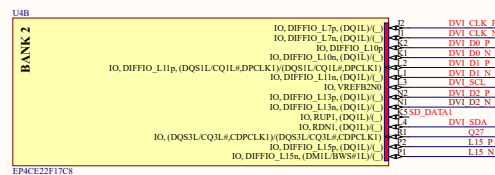
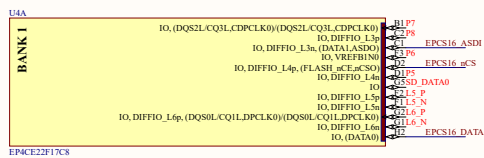


Title		
Size	Number	Revision
A3		
Date:	2021-2-19	Sheet of
File:	E:\FILE\...EXTconnect.SchDoc	Drawn By:

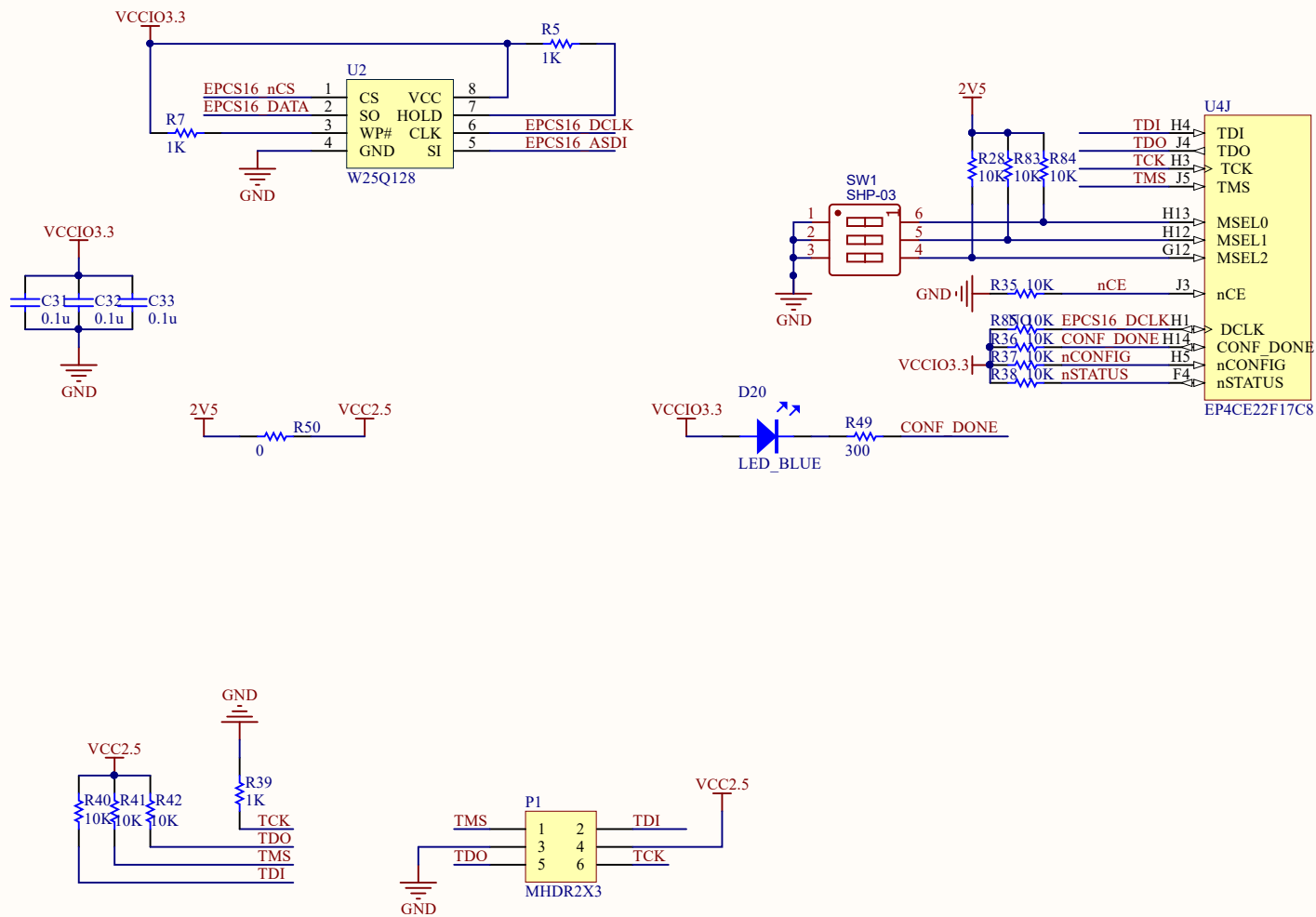


1

2

3

4



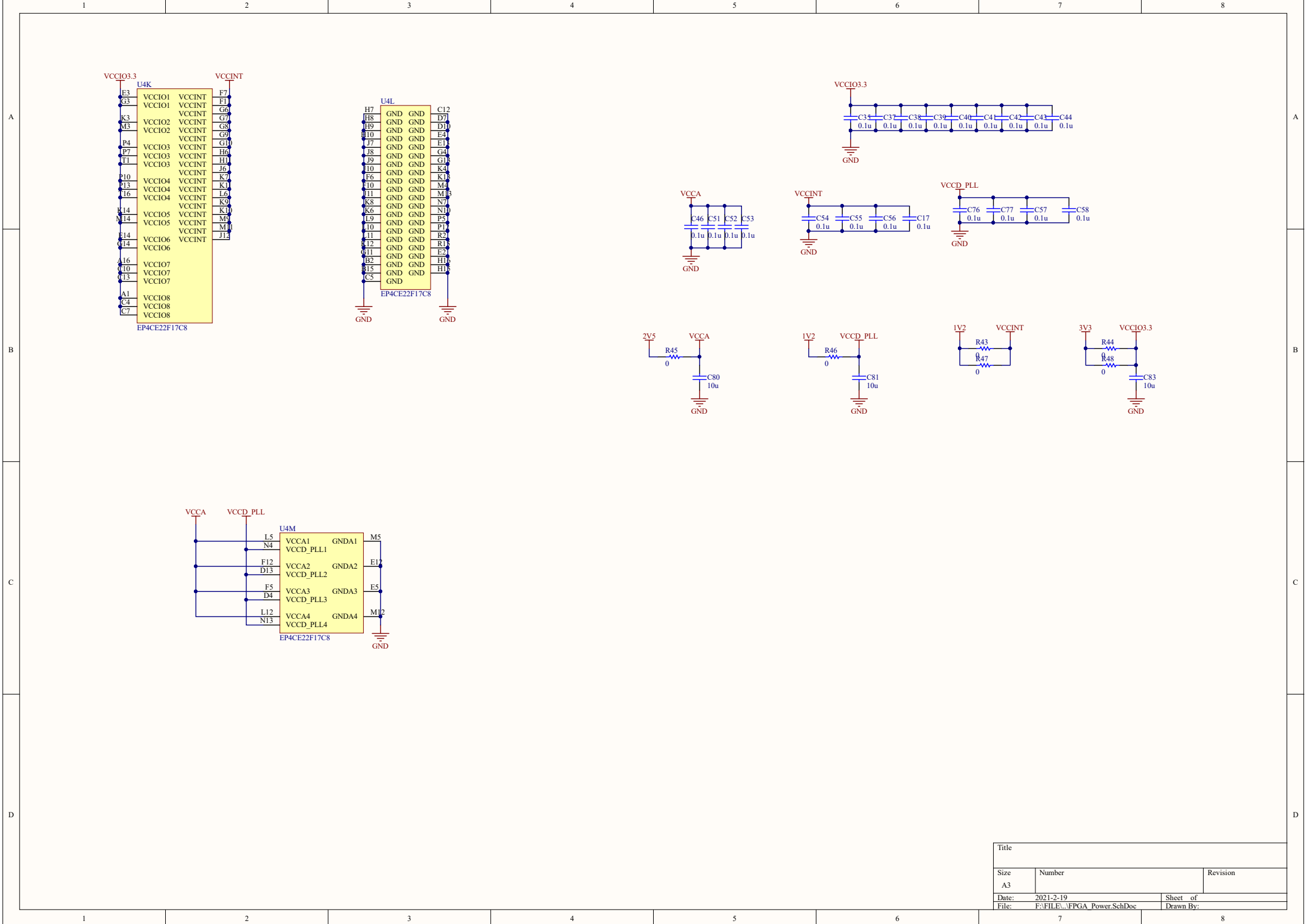
Title		
Size	Number	Revision
A4		
Date:	2021-2-19	Sheet of
File:	F:\FILE\...\FPGA_JTAG.SchDoc	Drawn By:

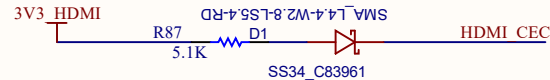
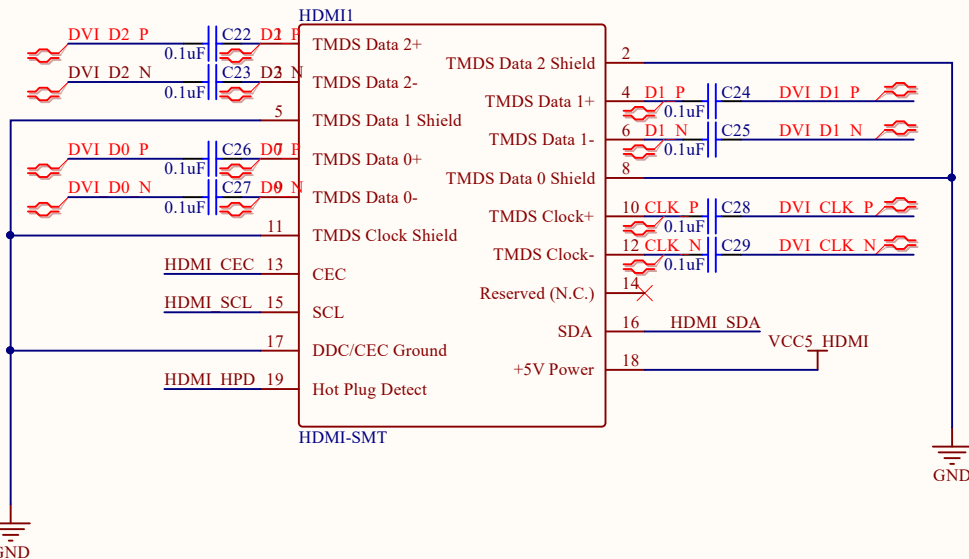
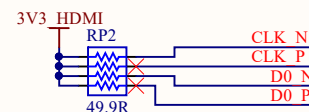
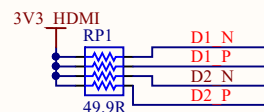
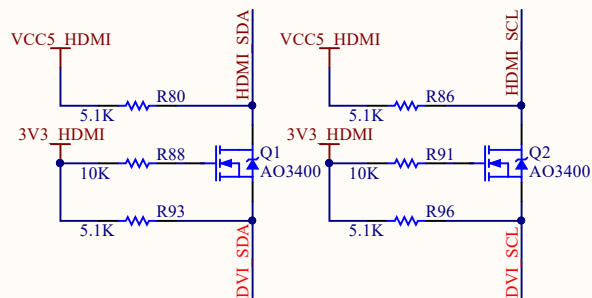
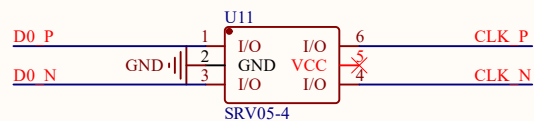
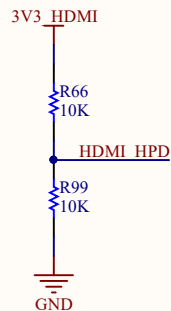
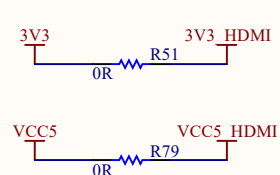
1

2

3

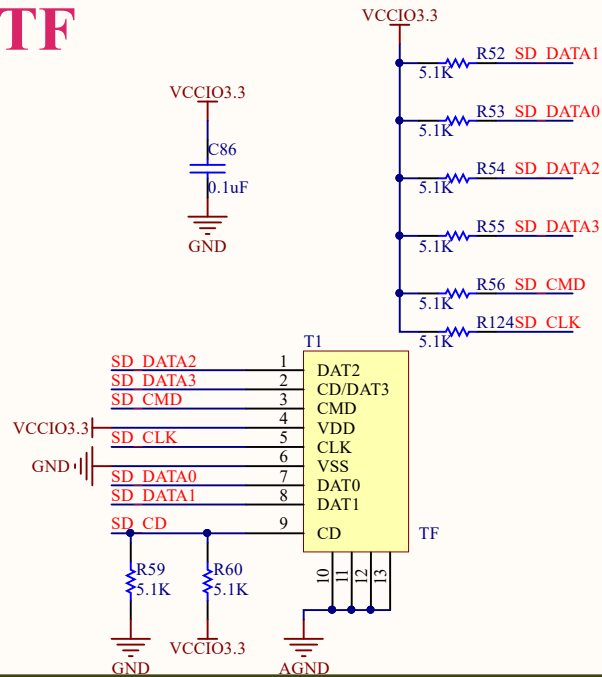
4



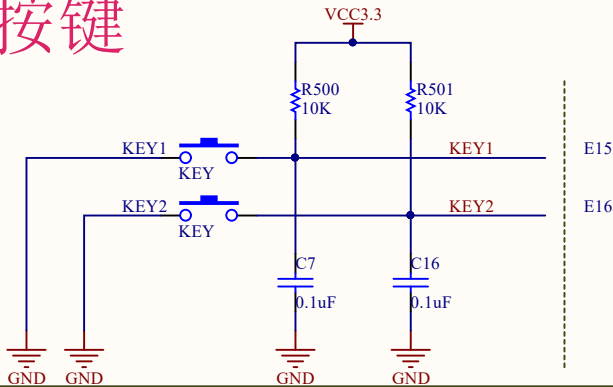


Title		
Size	Number	Revision
A4		
Date:	2021-2-19	Sheet of
File:	F:\FILE\HDMI.SchDoc	Drawn By:

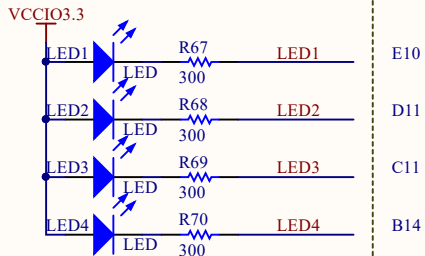
TF



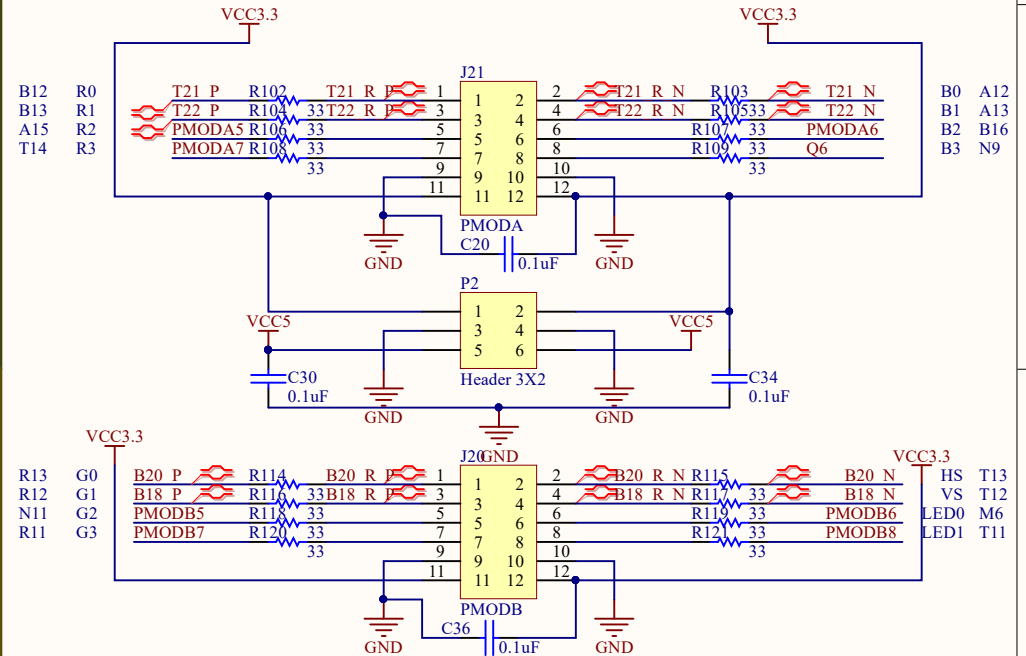
按键



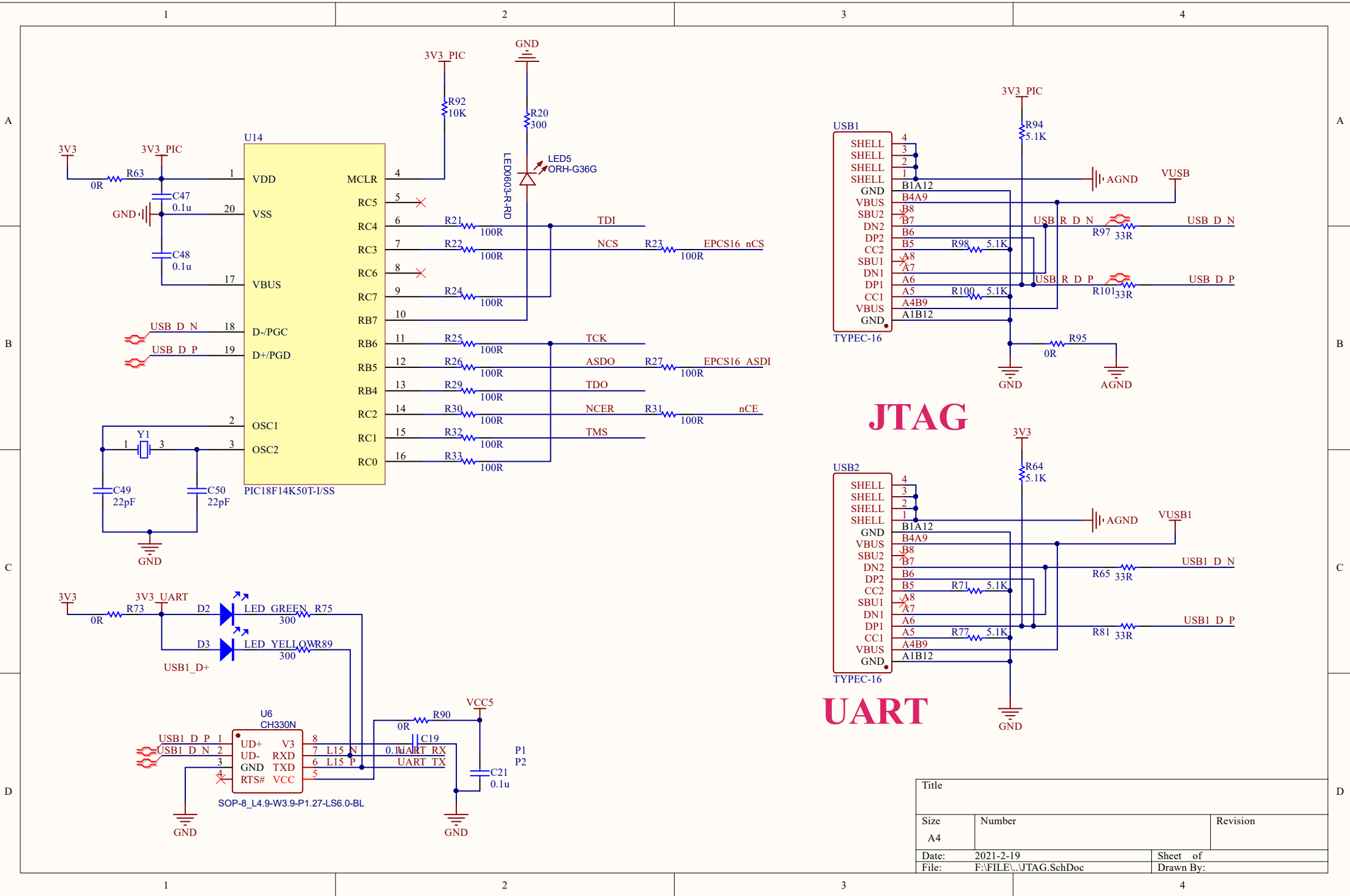
LED



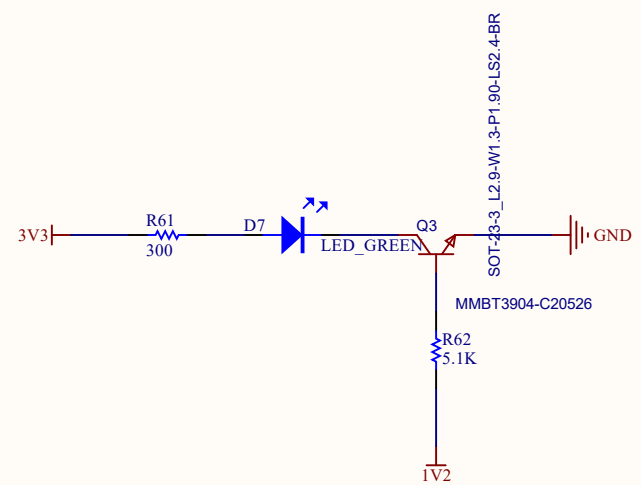
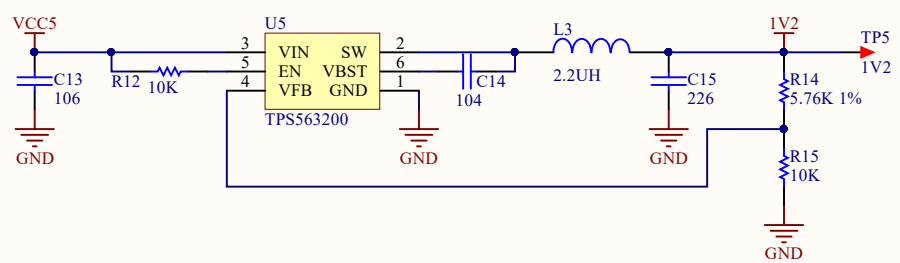
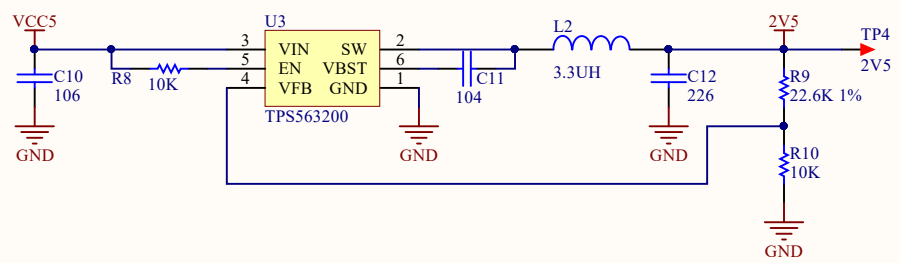
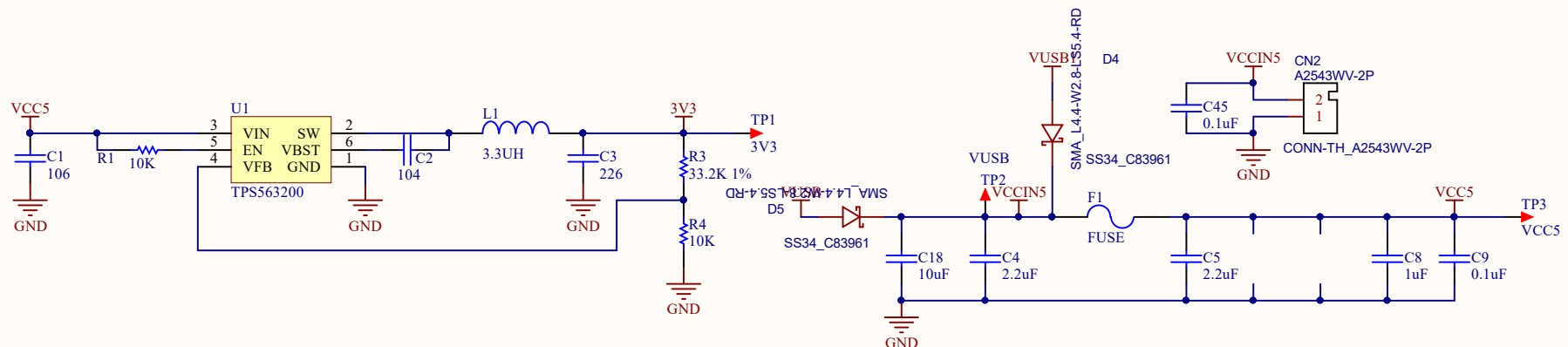
PMOD



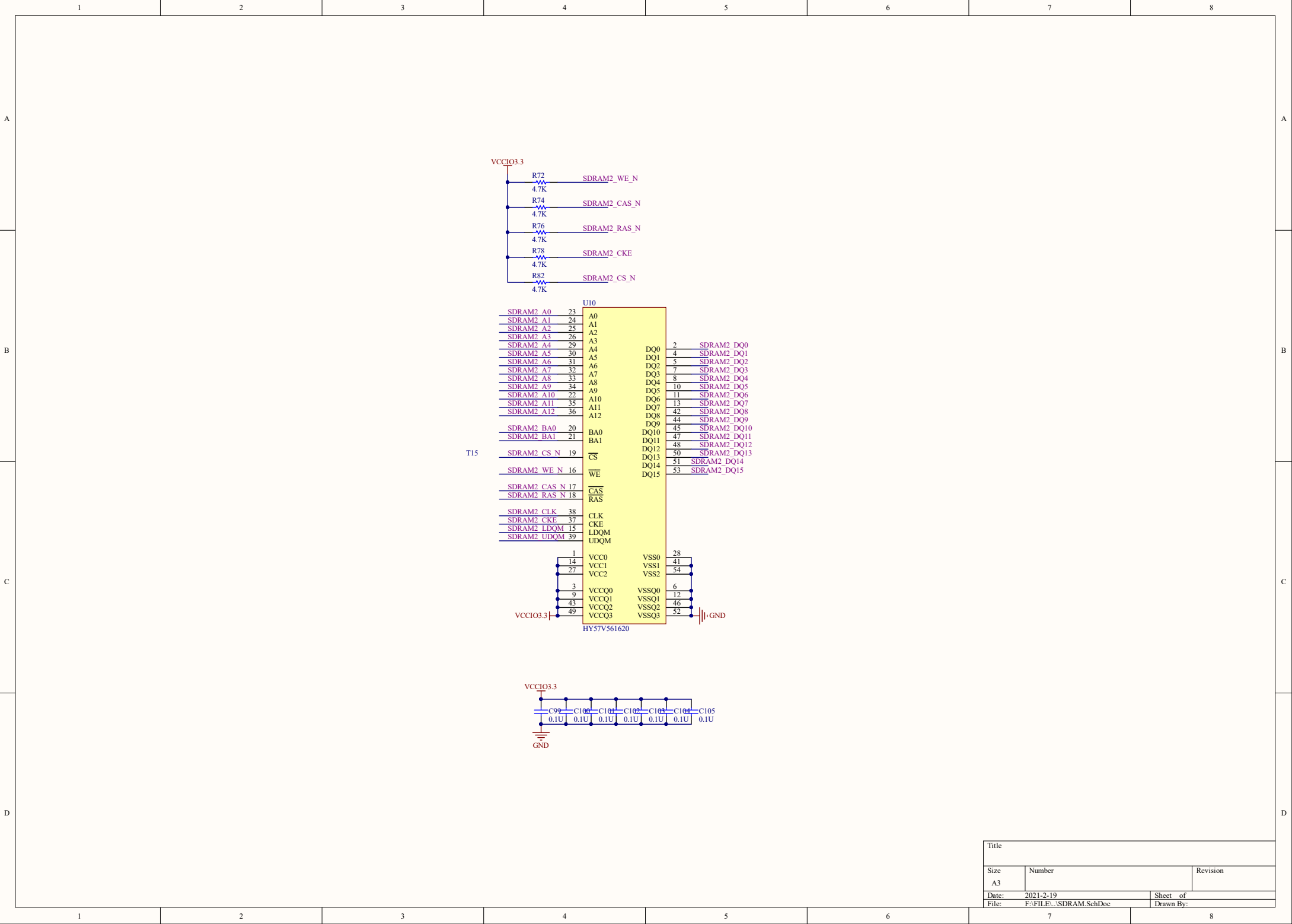
Title		
Size	Number	Revision
A4		
Date:	2021-2-19	Sheet of
File:	F:\FILE\INTERFACE.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2021-2-19	Sheet of
File:	F:\FILE\JTAG.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2021-2-19	Sheet of
File:	F:\FILE\POWER.SchDoc	Drawn By:



Title		
Size	Number	Revision
A3		
Date:	2021-2-19	Sheet of
File:	F:\FILE\...\SDRAM.SchDoc	Drawn By:

