Sample Program (X)

Source card reference	Relative address	Sample Program (X)		
1	0	X	START	
2			ENTRY	$x1,x2$
3			EXTRN	$y1,Y$
4	20	$x1$	===	
5	30	$x2$	===	
6	40		DC	$A(x1)$
7	44		DC	$A(x2+15)$
8	48		DC	$A(x2-x1-3)$
9	52		DC	$A(Y)$
10	56		DC	$A(y1+Y-x1+4)$
11			END	

Sample Program (Y)

Source card reference	Relative address	Sample Program (Y)		
12	0	Y	START	
13			ENTRY	$y1$
14			EXTRN	$x1,x2$
15	16	$y1$	===	
16	24		DC	$A(x1)$
17	28		DC	$A(x2+15)$
18	32		DC	$A(x2-x1-3)$
19			END	

$size(X)=60$

$size(Y)=34$

ESD Cards

source reference	Name	Type	ID	Relative address	Length
1	X	SD	01	0	60
2	x1	LD	—	20	—
2	x2	LD	—	30	—
3	Y	ER	02	—	—
3	y1	ER	03	—	—

TXT Cards (selective)

Source reference	Relative addr.	Contents	Comments
6	40-43	20	addr. of x1
7	44-47	45	addr. of x2+15
8	48-51	7	30-20-3
9	52-55	0	unknown to X
10	56-59	-16	-20+4

RLD Cards

Source Reference	ESD ID	Length(byte)	+ or −	Relative address
6	01	4	+	40
7	01	4	+	44
9	02	4	+	52
10	03	4	+	56
10	02	4	+	56
10	01	4	−	56

Similar cards are created by assembler in program Y.

Main storage after loading programs X and Y

Address	Value
104	
⋮	
144	124
148	149
152	7
156	168
160	232
164	
168	
⋮	⋮
192	124
196	149
200	7