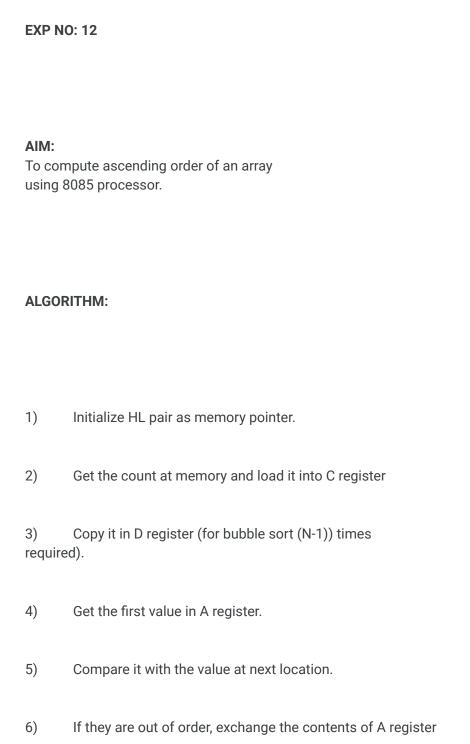
ASCENDING ORDER



and memory.	
7)	Decrement D register content by 1
8)	Repeat step 5 and 7 till the value in D register become zero.
9)	Decrement the C register content by 1.
10) Repeat steps 3 to 9 till the value in C register becomes zero.	
PROGRAM:	
LOOP: LXI H,3500	
MVI D,00	
MVI C,05	
LOOP1: MOV A,M	
INX H	
CMP M	
JC LOOP2	

MOV B,M

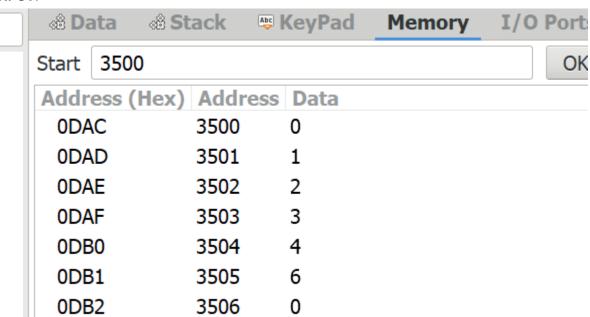
MOV M,A DCX H MOV M,B INX H MVI D,01 JNZ LOOP1 MOV A,D RRC

LOOP2: DCR C

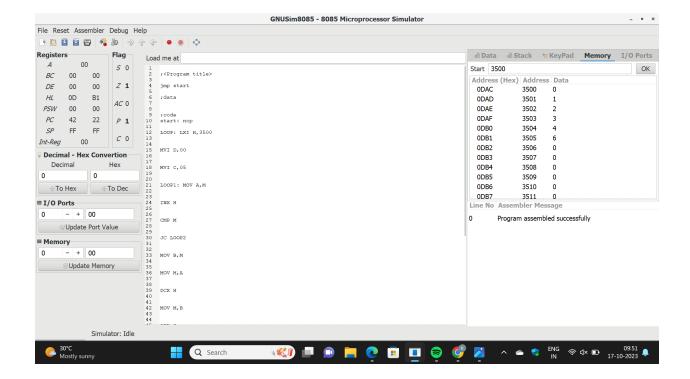
JC LOOP

HLT

INPUT:



OUTPUT:



RESULT: Thus

the program was executed successfully using 8085 processor simulator.