

Customer's Name

TO.

DESIGN	CHECK	APPROVAL
Kang	LV	

2022-12-14

R&D DEPT

Approval Sheet

PART PLB-N1PRGB-ATW-AI

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告知信息：

此产品最终解释权单位为"深圳雷克维尔电子有限公司",
因优化稳定性而产生的创新/改变/修正都是被允许的,
我们无法接受因为这些必须优化的因素而承担任何金钱或/和法律上的责任！

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1. 一般特性 General Characteristics

- 1.1 额定值(Rating Value): DC28V 0.4A.
1.2 工作温度(Work Temperature Range): $-25^{\circ}\text{C} \sim 70^{\circ}\text{C}$
1.3 贮存温度(Store Temperature Range): $-25^{\circ}\text{C} \sim 80^{\circ}\text{C}$
1.4 正常测试条件(未有特殊说明量测在以下条件进行):

General test condition (Tests and measurements shall be made under the following standard conditions unless otherwise specified):

正常温度: $5^{\circ}\text{C} \sim 35^{\circ}\text{C}$ 相对湿度: 45%~85% RH 气压: 8,600~10,600 帕
Temperature: $5^{\circ}\text{C} \sim 35^{\circ}\text{C}$ Relative humidity: 45%~85% Air pressure: 8,600~10,600 pa

2. 产品外观及尺寸要求 Appearance & Dimension Requirement

- 2.1 产品外形结构紧凑, 无配合不良.
The structure of product is compact, and assembly of parts has no badness.
- 2.2 产品塑胶部件无严重缩水、披锋、欠注、斑点、破损或变形现象.
The plastic parts of product have no serious defects such as very serious shrink, scarcity, fleck, disrepair, transmutation, etc.
- 2.3 产品引脚和外壳无严重氧化、脏污、变形、毛刺或电镀不良.
Lead feet and shell have no serious defects such as oxidation, smudge, disrepair, burr, defects on plating.
- 2.4 开关操作顺畅, 节奏感强, 无明显卡塞现象, (自锁开关锁芯锁住后, 允许导芯倾斜正负 2°)
Operating switch is unhindered, rhythmized, and there is not palpable clag. (After the keystroke is locked, it is normal that the keystroke tilt to one side plus or minus 2°)
- 2.5 产品结构及尺寸参见产品规格图纸.
Construction and dimensions: Refer to individual product drawing.

3. 电气特性 Electronic Characteristics

No.	项 目 Item	测 试 方 法 Test Method	测试设备 Equipment	特 性 要 求 Requirements
3.1	接触电阻 Contact Resistance	在低电流 ($\leq 100\text{mA}$) 条件下测试. Measured at low current (100mA or less).	低电阻测试仪 Low Resistance Meter	$100\text{m}\Omega$ max
3.2	绝缘阻抗 Insulation Resistance	测试相邻引脚之间, 引脚与外壳之间的绝缘阻抗(DC 500V). Measurement shall be made between adjacent terminals, between terminal and shell(DC 500V).	绝缘测试机 Insulation Resistance Tester	$100\text{M}\Omega$ min
3.3	耐压测试 Dielectric Withstand Voltage	输入一定电压(50~60Hz, 电压值 AC 500V) 1 分钟, 漏电流为 2mA, 测试邻近端子间. Apply certain voltage (50~60Hz, AC 500V) for 1 minute between adjacent contacts of the connector with 2mA leakage sensitivity.	耐压测试机 Puncture Tester	没有绝缘破坏. 电弧等异常. No arcing, break down and damaging insulation.



1. Characteristic Parameter of RGB IC:

Color	Wave Length (nm)	Luminous Intensity (mcd)	Luminous Flux (lm)
Red	620-625	300-500	1.0-1.5
Green	515-530	1000-1500	3.0-4.0
Blue	460-470	200-400	0.5-1.0

2. Electric Parameter (Absolute rating, Ta=25°C, VSS=0V) :

Parameter	Symbol	Range	Unit
Voltage	V_{DD}	+3.5~+5.5	V
Logic Input Voltage	V_i	-0.5~VDD+0.5	V
Operating Temperature	T _{opt}	-40~+85	°C
Storage Temperature	T _{stg}	-50~+150	°C
ESD Withstanding Voltage (Device Mode)	V_{ESD}	200	V
ESD Withstanding Voltage (Human Body Mode)	V_{ESD}	4K	V

3. Electric Parameter (Generally TA=-20~+70°C, VDD=4.5~5.5V, VSS=0V) :

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Power Voltage Inside IC	V_{DD}	---	5.2	---	V	---
R/G/B Port Withstanding Voltage	$V_{DS, MAX}$	---	---	26	V	---
DOUT Drive Capability	ID_{OH}	---	49	---	mA	DOUT grounds, Max Driving Current
	ID_{OL}	---	-50	---	mA	DOUT connects anode, Max current
Rollover Threshold of Input Signal	V_{IH}	3.4	---	---	V	VDD=5.0V
	V_{IL}	---	---	1.6	V	
PWM Frequency	F_{PWM}	---	1.2	---	KHZ	---
Quiescent Dissipation	I_{DD}	---	1	---	mA	---

4. Dynamic Parameter (Ta=25℃) :

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Data Transmission Rate	fDIN	---	800	---	KHZ	Duty Ratio 67% (Data 1)
DOUT Transmission Delay	T _{PLH}	---	---	500	ns	DIN→DOUT
	T _{PHL}	---	---	500	ns	
I _{out} Rise time	T _r	---	100	---	ns	V _{DS} =1.5V I _{OUT} =13mA
	T _f	---	100	---	ns	

5. Data Transmission Time:

Name of Timing Table		Min.	Actual Value	Max.	Unit
T	Element Cycle	1.20	--	--	μs
TOH	0, high level time	0.2	0.32	0.4	μs
TOL	0, low level time	0.8	0.88	1.0	μs
T1H	1, high level time	0.64	0.65	1.0	μs
T1L	1, low level time	0.2	0.55	0.56	μs
Trst	Reset, low level time	>80	--	--	μs

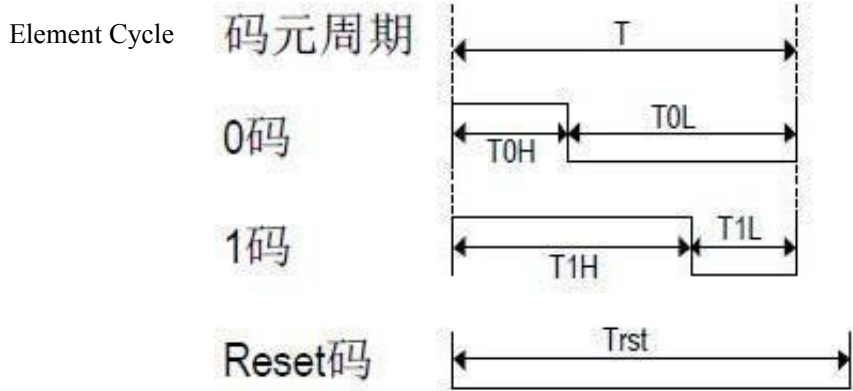
- 5.1 协议采用单极性归零码，每个码元必须有低电平，本协议的每个码元起始为高电平，高电平 时间宽度决定“0”码或“1”码。
- 5.2 书写程序时，码元周期最低要求为 1.2μs。
- 5.3 “0”码、“1”码的高电平时间需按照上表的规定范围，“0”码、“1”码的低电平 时间要求小于 20μs。

5.1 UNI-RZ(Unipolar Return-to-zero) is applied. There must be low level in each code element. In this agreement, each code element starts form high level, whose time width depends 0 code or 1 code.

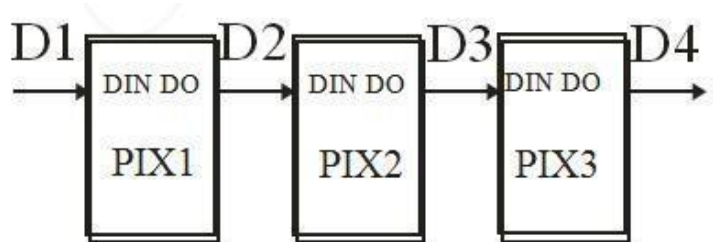
5.2 Min element cycle would be 1.2μs when coding.

5.3 The high level time of 0 code and 1 code should be on the basis of the chart above. The low level time of 0 code and 1 code are required to be less than 20μs.

6. Timing Waveform (Ta=25°C) : Input Code Pattern:

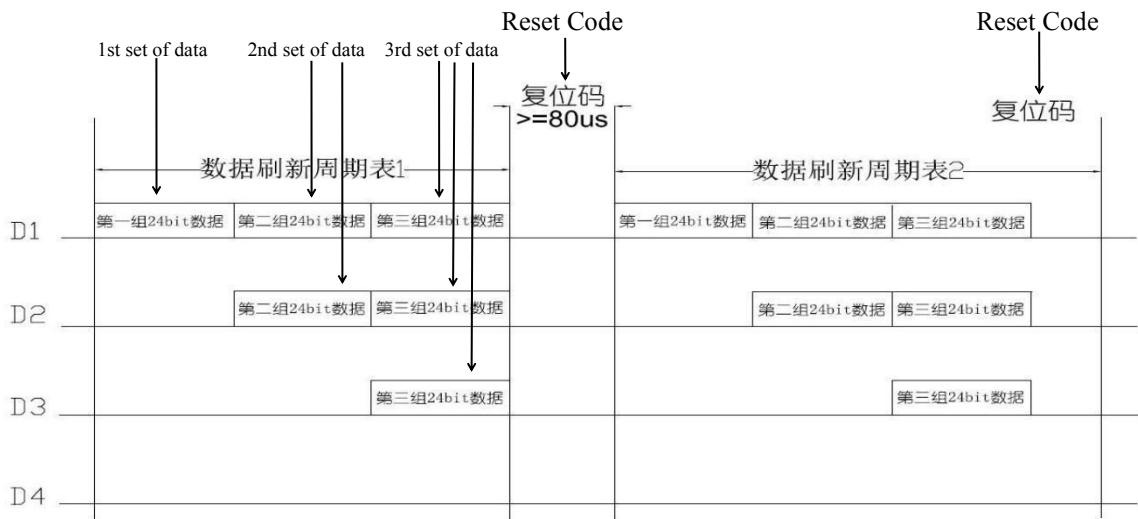


Connection Method 连接方式：



P.S: D1 is the data sent from MCU port, D2, D3, D4 are data automatically shaping forwarded by cascade circuit.

7. Data Transmission Method (Ta=25℃) :



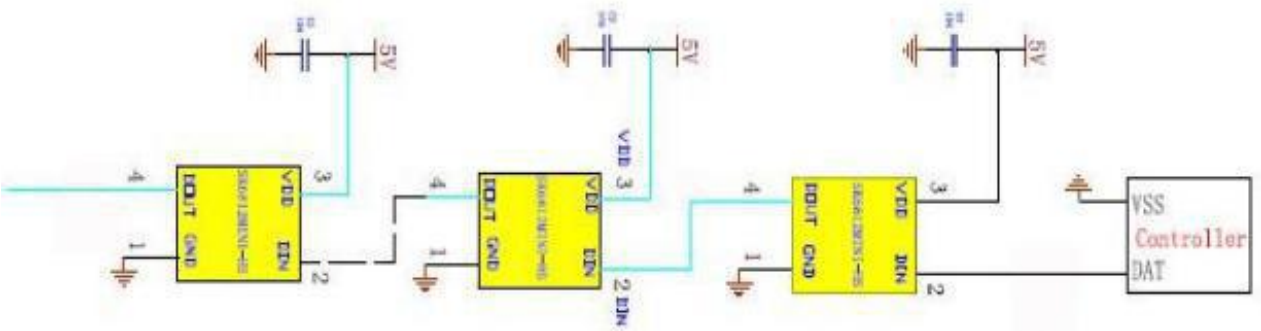
P.S: D1 is the data sent from MCU port, D2, D3, D4 are data automatically shaping forwarded by cascade circuit.

8. 24bit Data Structure (Ta=25℃) :

G7	G6	G5	G4	G3	G2	G1	G0	R7	R6	R5	R4
R3	R2	R1	R0	B7	B6	B5	B4	B3	B2	B1	B0

High level first, sending data in order of G,R,B(G7 → G6 →... B0)

9. Typical Application Circuit:



Each LED connects in parallel with a 0.1uF chip capacitor at its anode and cathode.