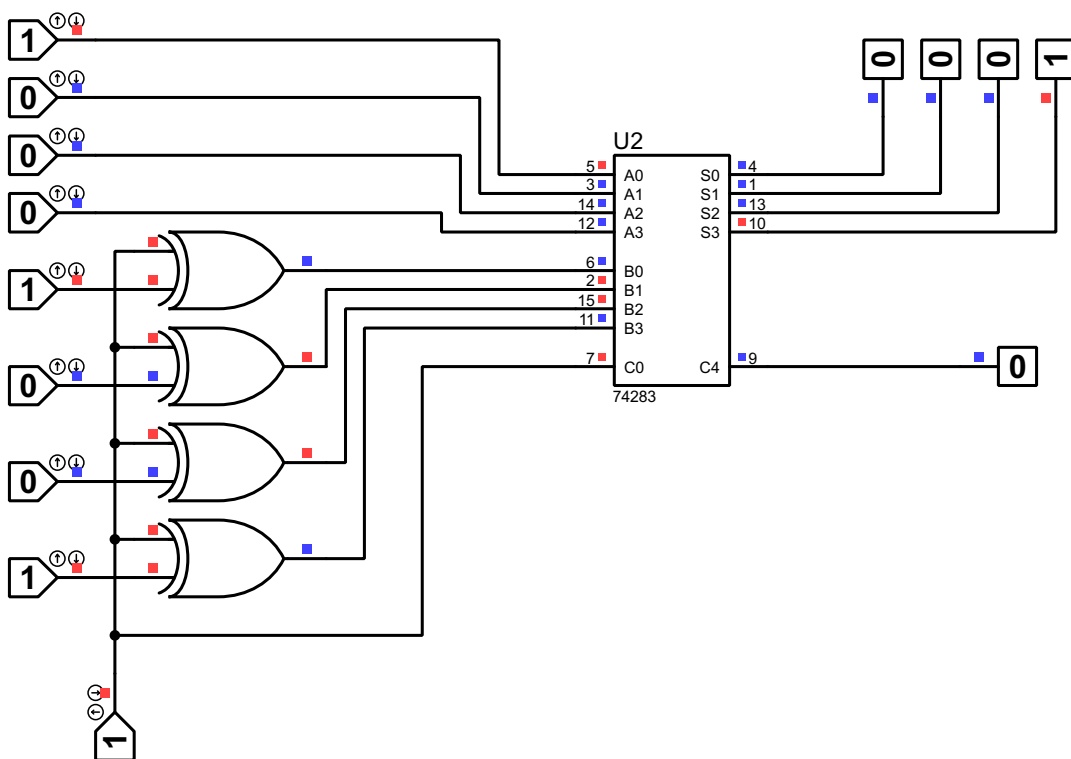
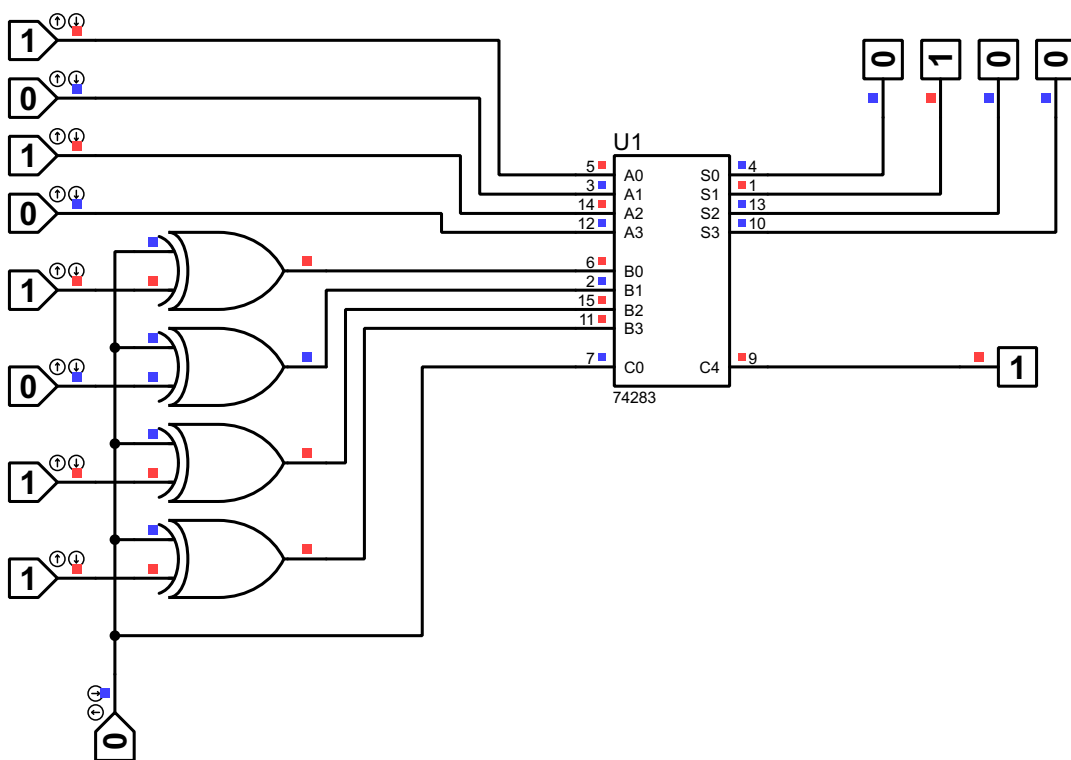


Adder / subtractor circuit using IC



FILE NAME: Lab_6_1.pdsprj

DESIGN TITLE: Lab_6_1.pdsprj

PATH: D:\Lab\DL\Lab_6_1.pdsprj

BY: @AUTHOR

BOARD: @BOARD

REV:@REV

DATE:

03-09-2024

PAGE:

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TIME: 12:02:42