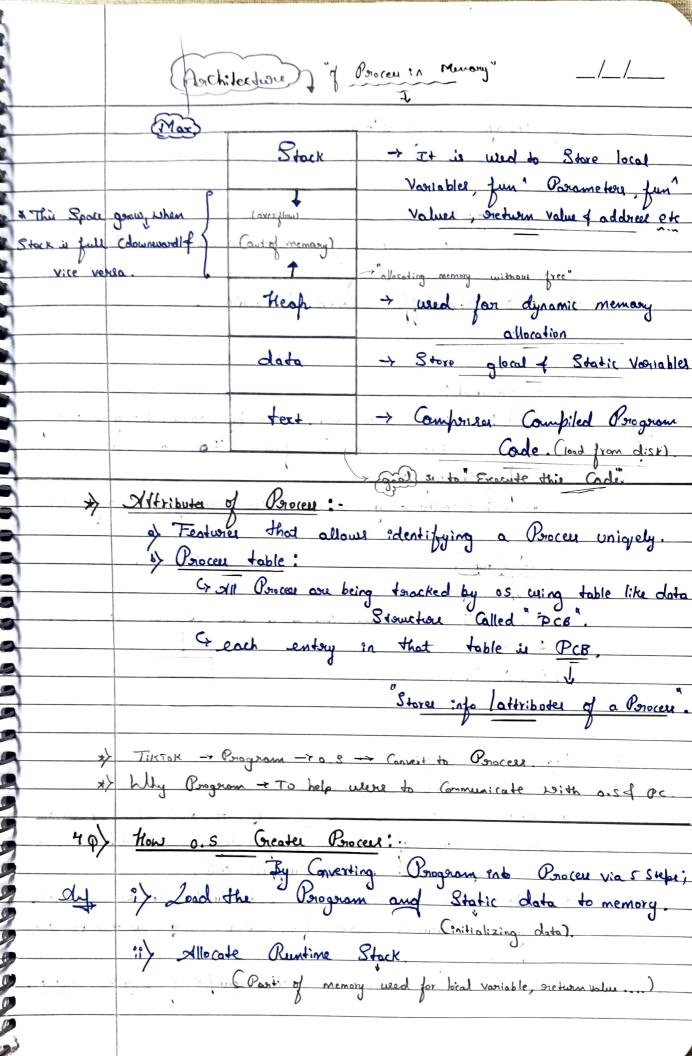
	Intenduction To Perocees"
Tiktok (Prog)	* Program -> Campiled Cade Ready to Execute
O.S. Corios La	10 + Priviles -> Program under Execution
Onocess	Priogram -> Compiled Code Ready to Execute  in + Priocear -> Priogram under Execution.  + Current Status of Prioceas is indicated by Pc (Program Countr)  and CPU registers.
6	and CPU registera
$\rightarrow$	Paraceer Stoucture:
·	
	(file) given to
	(file) givento it compile - Executable - Onogram.  (file) givento it with the state of Soctions.
	Derocere Memory: It is divided into 4 Soctions.
	i) Stack ii) Theat
	<b>/</b>



	_/_/	4
	(Acs)	5
	on the memory allocation: (Allocating Quintime Stack).	
	Co Book of memory used for alynamic Allocation	1
	A STATE OF THE STA	
	in To taske:	
	The ole and ever handlers are alloted.	
(a)	20.8 Shandoffe Control to main(). (0.8 kow only main)	
	Contains 0 - r 0.8 Coline program Executed Quecesfully).	
7 3 7 7 8	with the state of	
<b>©</b>	Stack overlead: - (overland)	
(grove)	Avoid: Stock unwinding Should be done Soling a lmit for aux succession funt. (Base Case Set)	
ST.	aux recureion fun. Bale Cale Set)	
; (•)	Cut of Overnoon;	
	Avoid: deallocating all the unnecessary memory albed.	
	Parocentable	
	CV stocett table	
	1 P. Cach one is a PCB (Process Control Block)	
	2 Pz	
	3 Ps. Pa	
		977
0	Registere in CPU:	7
	* Et is a data Stancture	
	* When a Process is sunning and its time Slice Explores, the	7
	Curvent Value of Pro au Specific Register would be Stored in	
1.5	OCB and would be Smoffed out.	
1.00	* When the Process is Scholdwed to be run, the	,)
,	oregister Value is oread from OCB and written to CPU	, ) " 
	energy steek. Aller and the steeks person	
	Cr This is the main purpose of rugisture in PCB".	
		*

	//
	Process States Process Prieses
	Jacob Diave J Sistem Grant
	Xe Parces Executes it Changes States" - x lite of
	The Process Executes, it Changes States"- + life Cycle.  (Generation to Termination).
E States	The state of the s
	1. New: 03 is about to Pick & Porogram & Convert into
1	Perocess of Perocess in the Stage of being Greated.
	l. Ready: The Process has all the resources it reeds to over
	Waiting to be assigned to the Porocessan
	3. Run: Instructions are being Executed; (Pu in
	allocated and being securted; (PD in
	4 bloiling: The Process is will do a state
	4. Waiting: The Process is waiting for some sto actions
	don Some evente do accur. For ago waiting
N. 1. 1. 1.	Jos Kayboord ile Child-Poroceu to finish, disk
V	5. Terminated: The Process has finished Execution.
	PCB entous our down Process toble
	PCB entony sumoved from Process table
L Pangeam =	Oncord ?
(Dis)-	Now admitted [eq: Time quantum] Exit > Terminated
	Introupt
	Ready Running
	Ilo Event Completion. (Pu dispid) 30   Event Nait.
	Schedular dispatch
34. 00 m	waiting to
1	Windshot The Committee of the Committee
	P. Pi P3 P3
	Ready -
	, P <sub>3</sub>
	, Chailing

	Processes:  3) Job Quene: - (from Dick -+ Otensony).  1) Processes in new State. (Dick to Ready).  11) Processes in 2° memory.  11) Tob Schoolular Clong Tarm Schoolular) (LTS) Picks  Process from the fool and loads them into  memory for Execution (R.Q).  15) Ready Quene: -
\	
*	Onoceer queues:
-	The state of the s
	a) Job Queue: - Gram Diek or Menony).
	:> Processes in new State. (Dox to Roady)
	1) Peresent in 2° memory.
	Job Schoolular Chang Fram Schoolular) (LTS) Picke
	Process from the fool and loads them into
	memory for Execution (R.Q)
14	LTS - The gives off blancits different (yele [ = 5, 5 (2700) 2nd s]
9	
1 -	b) Ready Queue:
	Parasse of Parls (P ) to P
	Procese in Ready State (Ready to Running)
	Con Sold I a Could To Sold I a col
	Picks Proces from the Pro and dispatch it
41.77	to CPU. (defending on Priority).
1	* STS -7 has high frequency -> No Cou idle time -> Hank very fast.
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
	c) Mailing Queue:
	"> Processes in Nait State.
Jarro	Degree of Multi-Programming:  **A Number of Brocesse in the memory.  **LTS Controls Degree of M.P.  Dispat Chen:  **The module of 0.5 that gives Control of CPU to a Process Sclected by STS.
*>	Degree of Multi- Programming:
	* Number of Processes in the memory.  * LTS Controls Degree of M.P.
	+ LTS Control Degace of M.P.
<*	Dispat Chen:
-	CPU to a Process School by STS.
	CPU to a Process Schroted by STS.
	in the second se

				_/_/
	G" many)	<b>∆</b> T.5.	Ready Q	
	Garage		( memoly)	STS
LTS -Y Sa	bell a mixture of Poro		(p <sup>c</sup> )	°v ×
Q	2 Computation, 7/2 navagence	46	(Vone	(Running)
	24		2	
*	MTS -> Medium	1 Learn 5	Chedular:	
	10.50 Least	Swofed out		9
Over Dogs	en der Pr. Pr. Pr. Pr. Pr. Pr. Pr. Pr. Pr. Pr	SwoMed in		Swapping
	Sin morning	Swap .	Space (20 memory)	J
: f	CTO Posset	Memory Ernest	ion) in	
	In Sualling:	Mon Contest in	o a Hall have	may as (PAM)
- Constant	mi bunt elle ini	7 4 9 1 1	i servere tile	The Chart
	* Time Sharing System	n may have in	ITS	
	* Rt Remove Programming.	Dr. 4	2.0	<i>V</i>
	* There removed to	Processes Can	be reintroduced	Porto memory,
	This is Called "	Smoffing.	otioned before to	Fig left off.
	* Suap-out and	Sund-James	one by MTS	
	" Swapping in	a mechanism gr	solucia a P	-core G. La
		and of man	THE MOSO CON	Move 1 to 2
	Storage (disk)	and make the	t memory ova	ilable to other
	the Process	from 2° Sto	rage to main	Memory.
		$\nu$	Q	J

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State of the last

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	Gozi)	**
;	ant Processes Such and	
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,	Ready Purus (CPU) Land	
	Ready Purus Course	
	The saludad to recent which the ETIA 4	
	The working Queue	
	Context - Switching: Orland Contest is with the storing).  * Switching the CPU to another Process requires  Performing a State Save of Current Process of a State  restore of different Process.	
	* Switching the CPU to another Process requires	
	restore of different Process.	
	The work of	
	nous Process School de de de sous	
•		
	Ashile Switching (als Ready of Process Jusen weeks process in due during this) =	
10 10		
7	athe of the single has been been a	
- 3-	Arfhan Process: Whose Porent Process has been	
a	Jerminated and 11th in Still sunning	
- the spain	Leave To Hall Asse (Alfancella Joseph Calante Calantella de la la constant de la	
	Rosew is done by Joak () Command	
	1	

1	(ch:/d)( Run)
	P. (Running) Jank () Pr 0.3
	Exeption - Terminated () (Recorded by init)
46	Zombie Peroceu / Defunct Peroceul: - (Z+).
و المنابع	and the mond in
	* It is a Perocess Debose Execution is Campleted
	t Zombie Process usually accur for Child Processes,
	* Zombie Process wevally accor for Child Processes,
	as the Porent Porcess Still needs to read it child's
120	Cell, the Zombie Process is eliminated from the
9 1 3 3	Cell, the Zombie Porocess is eliminated from the
1. 9	Peroceer dable, This is known as Reafing Z. 0?
	Le principal de la marchine de la companya de la co
	(Smin)  (P) (Walt) () (Posent)  (Papert)
	Papert
	Jank ()
,	(hild) Execute (hild) Execute (for 3 min).
Car min	(cenin)
	* As entary in the Process table Can only be sumoved,
	often the Parent Proces neads the Exit Status of
	Child Process, Hence, the Childforocess remains a zombie
	till it is oremoved from Proceed table.