O P3

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	"Colt Polo 4 Col 10"	
	Camputu Lysten 4 Camponente	
	1) Hardwood - Provide Fraix Computer Rosowers Eg. C	Po, Memary , Flodewic
	ii) 0.5 - Okceause allocation of OManagement.	G
	ii) Application Programe -> cury in which System rusau	view are used
	to Salve & Computing Proble	
	in Meru -> People, Machines of Computers.	m, games.
<b>→</b> ,	Types of 0.8:	
tomitus do e	Jingle Process 0.8: - only 1 Process Executes at a time of brain a squale & No Maximum willization of CPU. (Ready to	xow (Keody greens
0 87	t & MS DOS & Brocere Storvation occus	Execute) to
*		
	Botch Perocessing 0.8: - & No Maximum utilization of	CPU
* (Batch)	(Sorting Jobs) & Prientities Connot be Set	
with Similar	needs (un. 2 Job 2) Johnson Batch & Batch 2 - 65	
"Punch Card"	-> : Wir	
Indeance of the	x offer botch I with Job 1,3,5  * Whe Execute botch & with Joh 2,4,6.	
	* Es ATLAS, MonChester Univ	0
	:ii) Multiprogramming 0.8:-	0
	V. In Caron CPU 12till ration hu Keeping	multiple jobs
· · · · · · · · · · · · · · · · · · ·	in the memory So that CPU always has one to Exc Some job gets busy with Ilo.	earte in Cale
	* Single, CPU. * Ilo ofention -> Copy from USB	0
	* Cordert Switching for Process	
	* Switch happens when Congrent Voices goes to	vait state
	X CPU idle time reduced.  (G. THE, Dijkstra — (G.	intinued -
	Grand Control of the	named ?

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000				_/_/
5	*	The a.S allocates Franchisse is used	the xxxxxxxx among the	Paragrame Such that the
9	<u> </u>	The 0.8 is the		the times on Conjuter.
0			revally Called as 1/2	Ennel .
9	*>	Eg of 0.3:-	CGUT - boxed, Pc).	
9		ii) GNULL iii) macos Warkedo	(Macintell), used for	tions, Those - tier client/serve
9	40	7000	410/4	Smoothhones watch toblet
<del>0</del>		a. g. S. Non-Konnel		
9		Kennel.	System recessory fun -> 34	oxed in handdisk (num when called)
9		<u>Features</u>	0.8	Kennel.
<u> </u>		Definition:	al Software that manager	Care part of 0.3 that handles Communication blue
9		2) wer Interaction.	user directly interacts with	hardware of Roftware.
9		2) Q	like Gurig @ Commandlines	background, with no dissort was interaction.
		Waragement:	o. S. manager shigh level orevaurce allocation &	torke like memory
			Onceres & Afflications	allocation, Process Scheduling and device Management
		·	•	

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		1
	a. S is a Complete Package that includes keenel as a key	
	Component bibile 0,8 allows users to interact noith Computer from	
	Perograms, the Kernel is responsible for low level operations like	
	Managina memary and coternation with findungs	
	Managing memory and interacting with hardwork.	5
	View of 0.8:-  1) Culor View ->  3) Standalane System (ear of use + high Pexformance)  11) Were at different terminals (Maximize Resource utilization).  11) Workstation were (ease of use + Resource availability).  11) Were of handheld System (ease of use & Pexformance)	
**	View of 0.8:-	
	1/culon View ->	
	3) Standalone Bystem (ease of use + high Pexformance)	
	is were at different derminals (Maximize Repaires willization)	
		1
	Westertation and case of all I describe about by	1
	iv) Users of handheld System (ease of use of Performance	5
		T
<u> </u>	m) embeded Systems Charling mlc) - No wer interaction but	J
	Some L'ED's to Show Status of Wark.	7
	Salta Chay	万元元
	I PI N	5
	ii) Lystem View -	
	1) Resource allocator -> CPU time, memory Space,	,
	( Conflict is Solved) Tile - Storage Tools To device, Thought like of	
		<i>;</i> ;
		<i>;</i>
	Thomas of the state of the stat	
		- A
	DCB - Process Control Block. (Process Identification dumber)	-
,	* while Greating a Process -> 0.8 furfarms Several operation	10
	* Lohile Greating a Porocess - 0.8 furfamme Several ofuration of To 9 dentify each Process, it assigns PID to each Process	
	* to Edentify each Process, it assigns PID to each Process  * As o. 8 performs Multiprogramming, it needs to keep brock of all Brocks	
	* Abile Greating a Parocess . & furfarms Several ofurations  * To 9 dentify each Parocess, it assigns PID to each Process  * As a. & furfarms Multiprogramming, it needs to keep tomak of all Brocess	
	* Abile Greating a Parocess . & furfarms Several ofurations  * To 9 dentify each Parocess, it assigns PID to each Process  * As a. & furfarms Multiprogramming, it needs to keep tomak of all Brocess	
	* to Edentify each Process, it assigns PID to each Process  * As o. 8 performs Multiprogramming, it needs to keep brock of all Brocks	
	* To identify each Process, it assigns PID to each Process  * To identify each Process, it assigns PID to each Process  * Ale o. 8 performs Multiprogramming, it needs to keep track of all Brocess  * Thus, OCB is used to tosack the Process's Execution State  * PCB is a data Structure used to manage into door afrocess	5万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万
	* Ashile Greating a Process - o. 8 furfarms Several ofuration to To Edentify each Process, it assigns PID to each Process to the sack of all Brocess the Process of the State of the Process of all Brocess the Process of Execution State of the Process of the Proc	四万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万
	* Ashile Greating a Process - o. 8 ferjamme Several operation to To Edentify each Process, it assigns PID to each Process to the process of all boxess the Thus, PCB is used to track the Process's Execution State in PCB is a data Structure used to manage into about a process in Registers: They are Small Storage areas in Computing CPU that Store and Americal data during instruction Execution.	
	* Ashile Greating a Process - o. 8 ferjamme Several operation to To Edentify each Process, it assigns PID to each Process to the process of all boxess the Thus, PCB is used to track the Process's Execution State in PCB is a data Structure used to manage into about a process in Registers: They are Small Storage areas in Computing CPU that Store and Americal data during instruction Execution.	
	* Lohile Greating a Process - 0.8 fergasms Several operation  * To Adentify each Process, it assigns PID to each Process  * As 0.8 performs Multiprogramming, it needs to keep track of all boxess  * Thus, OCB is used to touck the Process's Execution State  * PCB is a data Structure used to manage into about a process  * Registers: They are Small Storage areas in Computer's CPU that  Store and manipulate data during instruction Execution.  * They are made up of this-flops [Store a Single bit of info].  * They are made up of this-flops [Store a Single bit of info].	四万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万万

$\Rightarrow$	Process ID - unique adentification alumber allated
	Process Status - New, Ready, Running, Wait, Terminate etc
State	Pointers to Parent Child Process
	Program Counter + Address to next antivaction
Cs Can	<- CPU Register -> Decumulator, General Purpose R, Bare R
	\(\lambda\)
Au tile Isa	
. !	Memory Mant Info + Volum of Base & Limit Registery, Page table
Jan Ollin Zisa	Mise Info +> Amount of CPU used, Time (one train)
	in M.
Ţ	iv) Multitarking: \ Time Quantum
11 (C)	* Single CPU + Context Switching and time Sharing week
*67	* In Greated Responsiveness & high Priority Process Possible
CTSS, MII	* Logical Extension of Multiprograting.
	able to sun more than I tack Simultaneously.
	* CPU idle time further reduced.
1.65	
	Wultiprocessing: A Better throughout
	* Content Switching * Time Sharing
* 67	* More than I CPU in a Single Computer ( Care-Procedor)
Dindows	Least Perocee Stanuation + In Creases Reliability.
	Cy z is danged attex wasks)
CONTRACTOR OF THE PARTY OF THE	Listoributed 0.8.:
CEPT CO	FUS * 0.8 Manager Many bunch of Resources, >= 1 CPU, >= 1914,
* E	>= memory etc OIt works over Network.
Locus	* Collection of Independent, networked, Communicating and
,	Thysically Seperate Computational nodes.
	1 Losely Caufled + duteromans,
	VIII RTOS:
1	
* 🚱	& Real time severe free Combutations within Light-line
ATCS.	* Real time revior free, Confutations within tight-time
ATCS.	* Real time severer force, Computations within tight-time boundaries.  * fastest Execution of Jobs. Six Toroffic control System.
	Dindouse  Ceve on the control of the

		(Cove -> CPU)	
	Multi Thouading	_/_/_	6
*/*	Program: Program is an Executive of Conflete	able file which Contains &t of	6
	* It is a Compiler's Code * Ready to be Executed	* Stored in DISK.	
*	MultiThreading.,  Perogram: Perogram is an Execute instructions to Complete  * It is a Compiler's Code  * Ready to be Executed  Perocess: Perogram under Execut  Thread:  * Light-weight Perocess.  * An independent Path of  * Single Sequence Stream  * Used to achieve Paralle  * Light Deske Lehich are independent  * Text editors is the  * Spell Checking, Jore	ion, Resides in Confutor i memory (RAM)	6
*/.	Thought:		
	* Simple Sequence Strange	Execution in a Process	
(Jeg to PN	-> + used to achieve Paralle	lism by dividing a Parocess's	-5
	* Co. Multiple table in a	benouser.  n you one typing in a editor.	6
	Spell Checking, form	encurrently by multiple threads).	5
Ind Interview	. MThreading	Mulli Tal king	
	More CPUX Multiproceuling environment  4) A Proceu is divided into Several  different Sub-tooks Colled Threads,	took Simultaneously is called	6
	which has it own Path of Execution.  It is Called Multithreading.	Multitasking. Time Sharing blu	()= ()=
1	* more than I Process being	in more than I Thread being	0
	Context Switched.	Context Switched  * Chu > = I	0
	A) Bolodian of memory Production Eister  * Process one Scheduled.	* No Toolstion of memory Protection * Threads one Scheduled.	0
-	Recurrer are differently allocated	Recourse ore Gored	000
			0

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3	_/_/
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44	Thread Schelding:
	* Threads are Scheduled for Execution based on their
	Usioxity.
	A Even though thoseads are Executing within the Runtime, all thoseads are assigned Processory time Slice byos
	all threads are assigned Processory time Slice byos
<b>→</b>	Thread Context Switching Process Context Switching
•	
	\$ 0.3 Save Channet State of throads \$ 0.8 Saver Current State of
	Switch to another thread of Process and Switches to another.
	Same Process.  by restoring its State.
	* Fost Switching * Slow Switching
	* CPU's Coche State is Preserved * CPU's Coche State is flushed
	* doesnot in Clude Switching * In Clude Switching of memory
•	" (pc, Stock, sugister one Switched).
	(pr. Stock, sugister one Switches).