



Sree Vidyanikethan Engineering College (Autonomous)
Sree Sainath Nagar, Tirupati -517 102-517102
 (Affiliated to Jawaharlal Nehru Technological University Anantapur, Anantapuramu)

GRADE SHEET

Examination : I B.Tech I Semester (SVEC-16) Regular Examinations, December 2017	Month & Year : December 2017
Branch : Electronics and Communication Engineering	
Name of the Student : BADDALA SUMANTH REDDY	Hall Ticket No. : 17121A0411

S.No.	Subject Code	Subject Title	Internal Marks	External Marks	Total Marks	Grade Point	Grade	Credits
1	16BT1BS02	ENGINEERING PHYSICS	29	61	90	9	S	3
2	16BT1BS03	MATRICES AND NUMERICAL METHODS	30	68	98	10	O	3
3	16BT1BS04	MULTI-VARIABLE CALCULUS AND DIFFERENTIAL EQUATIONS	30	67	97	10	O	3
4	16BT10241	NETWORK ANALYSIS	28	57	85	9	S	4
5	16BT10501	PROGRAMMING IN C	25	50	75	8	A	3
6	16BT1BS32	ENGINEERING PHYSICS LAB	50	48	98	10	O	2
7	16BT10232	ELECTRICAL AND ELECTRONICS WORKSHOP PRACTICE	45	47	92	9	S	2
8	16BT10251	NETWORK ANALYSIS LAB	47	48	95	10	O	2
9	16BT10531	PROGRAMMING IN C LAB	45	48	93	9	S	2
Total :			329	494	823			24

GPA : 9.29



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GRADE SHEET

Examination : I B.Tech II Semester (SVEC-16) Regular Examinations, June 2018	Month & Year : June 2018
Branch : Electronics and Communication Engineering	
Name of the Student : BADDALA SUMANTH REDDY	Hall Ticket No. : 17121A0411

S.No.	Subject Code	Subject Title	Internal Marks	External Marks	Total Marks	Grade Point	Grade	Credits
1	16BT1HS01	TECHNICAL ENGLISH	28	53	81	8	A	3
2	16BT1BS01	ENGINEERING CHEMISTRY	28	61	89	9	S	3
3	16BT2BS01	TRANSFORMATION TECHNIQUES AND PARTIAL DIFFERENTIAL EQUATIONS	26	67	93	9	S	3
4	16BT20401	ELECTRONIC DEVICES AND CIRCUITS	21	53	74	7	B	3
5	16BT20541	FOUNDATIONS OF DATA STRUCTURES	28	53	81	8	A	3
6	16BT1HS31	ENGLISH LANGUAGE LAB	42	47	89	9	S	2
7	16BT1BS31	ENGINEERING CHEMISTRY LAB	49	45	94	9	S	2
8	16BT10331	COMPUTER AIDED ENGINEERING DRAWING	47	46	93	9	S	3
9	16BT20551	FOUNDATIONS OF DATA STRUCTURES LAB	46	49	95	10	O	2
Total :			315	474	789			24

GPA : 8.58



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GRADE SHEET

Examination : II B.Tech I Semester (SVEC-16) Regular Examinations, November 2018	Month & Year : November 2018
Branch : Electronics and Communication Engineering	
Name of the Student : BADDALA SUMANTH REDDY	Hall Ticket No. : 17121A0411

S.No.	Subject Code	Subject Title	Internal Marks	External Marks	Total Marks	Grade Point	Grade	Credits
1	16BT30241	ELECTRICAL TECHNOLOGY	23	48	71	7	B	3
2	16BT3HS01	ENVIRONMENTAL STUDIES	26	52	78	8	A	3
3	16BT3BS02	SPECIAL FUNCTIONS AND COMPLEX ANALYSIS	29	60	89	9	S	3
4	16BT30401	ELECTRONIC CIRCUIT ANALYSIS & DESIGN	24	44	68	7	B	3
5	16BT30402	SIGNALS AND SYSTEMS	21	45	66	7	B	3
6	16BT30403	SWITCHING THEORY AND LOGIC DESIGN	26	46	72	7	B	3
7	16BT30251	ELECTRICAL TECHNOLOGY LAB	45	40	85	9	S	2
8	16BT30431	BASIC ELECTRONICS AND DIGITAL DESIGN LAB	47	47	94	9	S	2
9	16BT30432	SIGNALS AND SYSTEMS LAB	46	43	89	9	S	2
Total :			287	425	712			24

GPA : 7.88



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GRADE SHEET

Examination : II B.Tech II Semester (SVEC-16) Regular Examinations, May 2019	Month & Year : May 2019
Branch : Electronics and Communication Engineering	
Name of the Student : BADDALA SUMANTH REDDY	Hall Ticket No. : 17121A0411

S.No.	Subject Code	Subject Title	Internal Marks	External Marks	Total Marks	Grade Point	Grade	Credits
1	16BT40401	ANALOG COMMUNICATIONS	25	39	64	6	C	3
2	16BT40402	DIGITAL IC APPLICATIONS	27	41	68	7	B	3
3	16BT40403	ELECTROMAGNETIC THEORY AND TRANSMISSION LINES	28	32	60	6	C	3
4	16BT40404	LINEAR IC APPLICATIONS	26	51	77	8	A	3
5	16BT40405	PROBABILITY AND STOCHASTIC PROCESS	24	53	77	8	A	3
6	16BT40406	PULSE AND DIGITAL CIRCUITS	27	44	71	7	B	3
7	16BT40431	ANALOG COMMUNICATIONS LAB	48	45	93	9	S	2
8	16BT40432	ELECTRONIC CIRCUIT ANALYSIS AND DESIGN LAB	45	40	85	9	S	2
9	16BT40433	PULSE AND DIGITAL CIRCUITS LAB	48	42	90	9	S	2
Total :			298	387	685			24

GPA : 7.5



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GRADE SHEET

Examination : III B.Tech I Semester (SVEC-16) Regular Examinations, November 2019	Month & Year : November 2019
Branch : Electronics and Communication Engineering	
Name of the Student : BADDALA SUMANTH REDDY	Hall Ticket No. : 17121A0411

S.No.	Subject Code	Subject Title	Internal Marks	External Marks	Total Marks	Grade Point	Grade	Credits
1	16BT3HS02	MANAGERIAL ECONOMICS AND PRINCIPLES OF ACCOUNTANCY	24	56	80	8	A	3
2	16BT50201	CONTROL SYSTEMS	26	57	83	8	A	3
3	16BT50401	DIGITAL COMMUNICATIONS	24	52	76	8	A	3
4	16BT50402	MICROPROCESSORS AND MICROCONTROLLERS	26	42	68	7	B	3
5	16BT50403	VLSI DESIGN	26	54	80	8	A	3
6	16BT50501	COMPUTER NETWORKS	25	34	59	6	C	3
7	16BT50431	LINEAR AND DIGITAL IC APPLICATIONS LAB	45	45	90	9	S	2
8	16BT50432	MICROPROCESSORS AND MICROCONTROLLERS LAB	47	47	94	9	S	2
9	16BT4HS31	SOFT SKILLS LAB	50	44	94	9	S	2
Total :			293	431	724			24

GPA : 7.88



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GRADE SHEET

Examination : III B.Tech II Semester (SVEC-16) Regular Examinations, December 2020	Month & Year : Decemeber 2020
Branch : Electronics and Communication Engineering	
Name of the Student : BADDALA SUMANTH REDDY	Hall Ticket No. : 17121A0411

S.No.	Subject Code	Subject Title	Internal Marks	External Marks	Total Marks	Grade Point	Grade	Credits
1	16BT5HS01	MANAGEMENT SCIENCE	22	46	68	7	B	3
2	16BT60401	ANTENNAS AND WAVEGUIDES	18	41	59	6	C	3
3	16BT60402	DIGITAL SIGNAL PROCESSING	19	48	67	7	B	3
4	16BT40502	DATABASE MANAGEMENT SYSTEMS	20	48	68	7	B	3
5	16BT60406	TELECOMMUNICATION SWITCHING SYSTEMS	17	32	49	5	D	3
6	16BT60408	INFORMATION THEORY AND CODING	22	40	62	6	C	3
7	16BT6MOOC	MASSIVE OPEN ONLINE COURSE**	-	-	-	-	P	-
8	16BT60431	DIGITAL COMMUNICATIONS LAB	47	46	93	9	S	2
9	16BT60432	DIGITAL SIGNAL PROCESSING LAB	47	48	95	10	O	2
10	16BT60433	SEMINAR	-	89	89	9	S	2
Total :			212	438	650			24

GPA : 7.08