



CXA-0271 (5W DUAL OUTPUTS WITH DIMMING FUNCTION)

DESCRIPTION:

This low profile DC to AC Inverter is developed for dual lamps, low power LCD back-light. Application includes industrial PC and LCD monitor.

Applicable LCD; 8 to 12 inches double lamp type

Lamp Voltage 460Vrms

Lamp Current 6mArms

Lamp Start Up Voltage 1500Vrms (Vin: 12V)



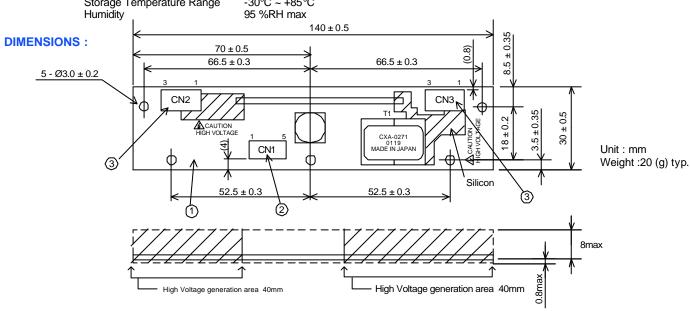
FEATURES:

Alarm signal function PWM dimming type.

Silicon Coating in High Voltage area

TEMPERATURE & HUMDITY:

RE & HUMDITY:
Operating Temperature Range
O°C ~ +60°C
Storage Temperature Range
-30°C ~ +85°C



No.	Part Description	Qty.	Note	
1	PWB	1	UL94V-0	t=1.0mm
2	Connector CN1	1	S5B-PH-SM3-TB	(JST)
3	Connector CN2,CN3	2	SM02(8.0)B-BHS-1	(JST)

CN1: S5B-PH-SM3-TB (JST)	,
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Pin	Symbol	Note				
CN1-1	Vin	10.8 ~ 13.2V				
CN1-2	GND	0 V				
CN1-3	Vbr	0 ~ 2.5V				
CN1-4	Vst ^{*1}	0V / 5V				
CN1-5	Vrmt	0V ~ 0.4V : OFF 2V ~ Vin : ON				

(Japan)

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Tel: +1-847-390-4439 (USA) Tel: +44-118-921-6206 (EU)

CN2: SM02	(8 N)	B-BHS-1	(JST)
OINZ . OIVIOZ	(0.0)		(001)

Pin	Symbol	Note
CN2-1	Vhigh1	460Vrms (6mArms)
CN2-2	N.C.	-
CN2-3	Vlow1	(2V)

CN3: SM02(8.0)B-BHS-1 (JST)

Pin	Symbol	Note				
CN3-1	Vhigh1	460Vrms (6mArms)				
CN3-2	N.C.	-				
CN3-3	Vlow1	(2V)				

^{*1.} This is an output pin and it is active high (+5V) if any Lamp opens / fails.



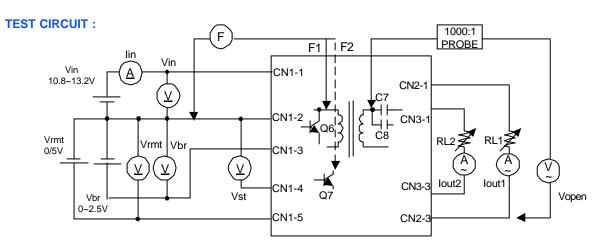
DC-AC INVERTER UNIT

CXA-0271 (5W DUAL OUTPUTS WITH DIMMING FUNCTION)

ELECTRICAL CHARACTERISTICS:

Parameters	Symbol	Conditions			Specifications			11.2	Note		
		Vin (V)	Vrmt (V)	Vbr (V)	Ta (°C)	RL1 (k Ω) RL2 (k Ω)	min.	typ.	max.	Unit	Note
Output Current	lout1 / lout2	12 ± 1.2	5 ± 0.25	0	0 ~ +60	74 74	5.0	6.0	7.0	mArms	
		12 ± 0.1	5 ± 0.25	0	23 ± 5	74 74	5.7	6.0	6.7	mArms	Max Brightness.
		12 ± 0.1	5 ± 0.25	2.5	23 ± 5	74 74	1.2	2.0	2.8	mArms	Min Brightness.
Input Current 1	lin1	12 ± 1.2	5 ± 0.25	0 ~ 1.5	23 ± 5	74 74	-	0.55	0.7	Adc	
Input Current 2	lin2	12 ± 1.2	0 ± 0.25	0 ~ 1.5	23 ± 5	74 74	-	-	1	mAdc	
Frequency	F1	12 ± 1.2	5 ± 0.25	0	23 ± 5	74 74	38	43	48	kHz	
Frequency (Duty)	F2	12 ± 1.2	5 ± 0.25	1.0	23 ± 5	74 74	254	305	356	Hz	
Open Voltage	Vopen	12 ± 1.2	5 ± 0.25	0	23 ± 5	∞	1250	1500	-	Vrms	
	Vst	12 ± 0.1	5 ± 0.25	0 ~ 2.5	23 ± 5	74 ∞	4.5	5.0	5.5	Vdc	RL2 open
Alama Cinnal		12 ± 0.1	5 ± 0.25	0 ~ 2.5	23 ± 5	∞ 74	4.5	5.0	5.5	Vdc	RL1 open
Alarm Signal		12 ± 0.1	5 ± 0.25	0 ~ 2.5	23 ± 5	∞ ∞	4.5	5.0	5.5	Vdc	RI1 & RL2 open
		12 ± 0.1	5 ± 0.25	0 ~ 2.5	23 ± 5	74 74	-	0	0.5	Vdc	Normal

Note 1 : Please keep minimum of 2mm clearance (all directions) between inverter high voltage area as marked on mechanical drawing and any conductors.



General Information Te

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