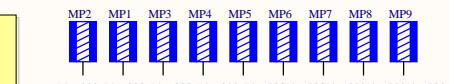


Summary of Known oversights to be corrected in the next design:  
 - In the type counter sheet, the MR should be connected to 3V# instead of Ground  
 - The Wurth PoE Component drawing is mirrored. must be corrected on the footprint side. Error coming from Wurth!

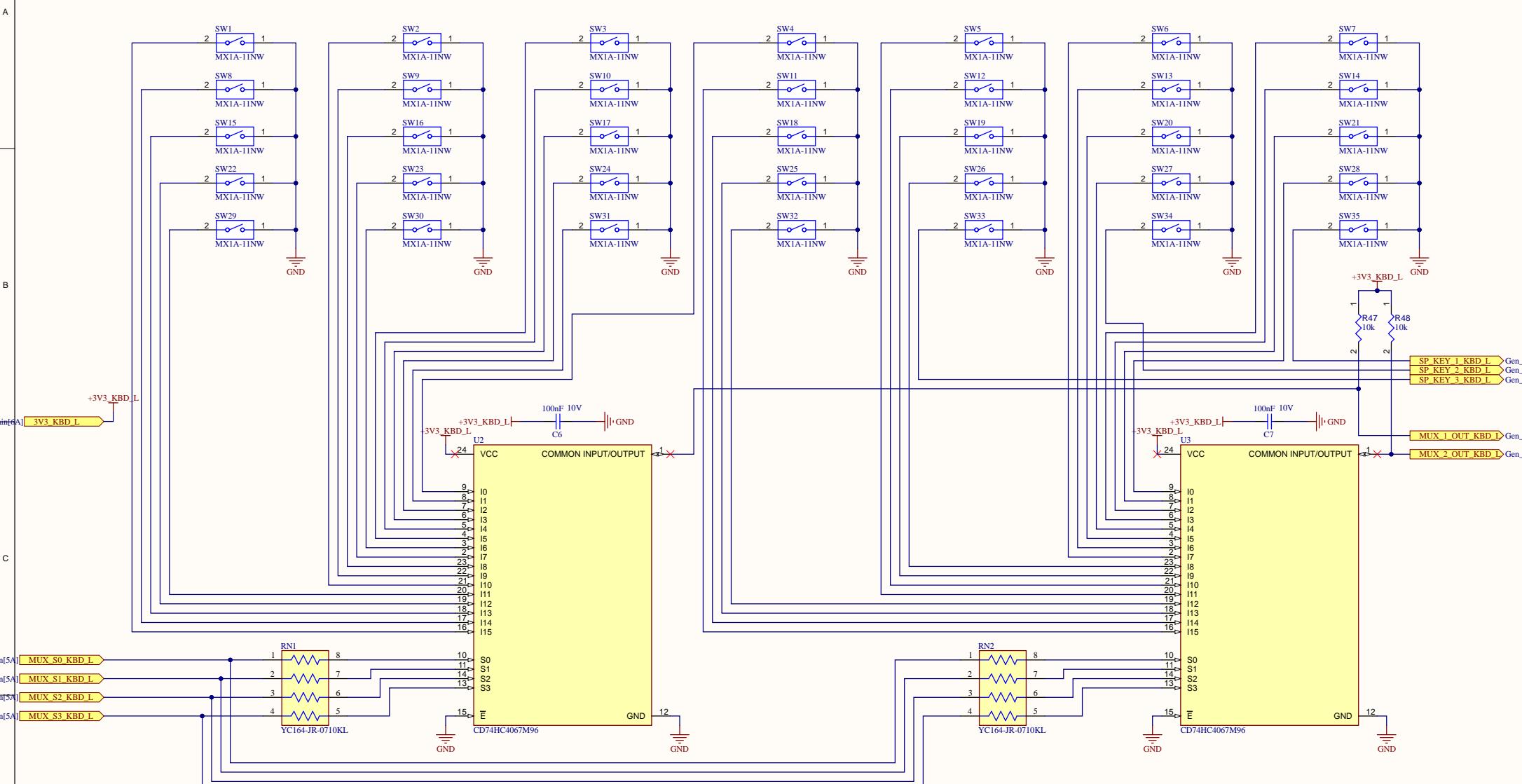
Summary of nice to have improvement:  
 - Open UART lines to the Nextion display, split the UART coming for the ESP for programming FW.  
 - Add neopixel directly as a component on the bottom side of the PCB, so no need to use LED strips.  
 - To enable the LED on the interlock switch, must add additional inverter logic  
 - Add physical reset STM32 reset to one of te key/switch



PROJECT	SUMASTA	
PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:
TITLE		
Generative KAIBoard Main		
SIZE	CAGE CODE	DWG NO.
A3		
SCALE:	FILE NAME	Gen_Ai_Kbd-Main.SchDoc
		Sheet 1 of 11

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REVISION	DESCRIPTION	DATE	APPROVED



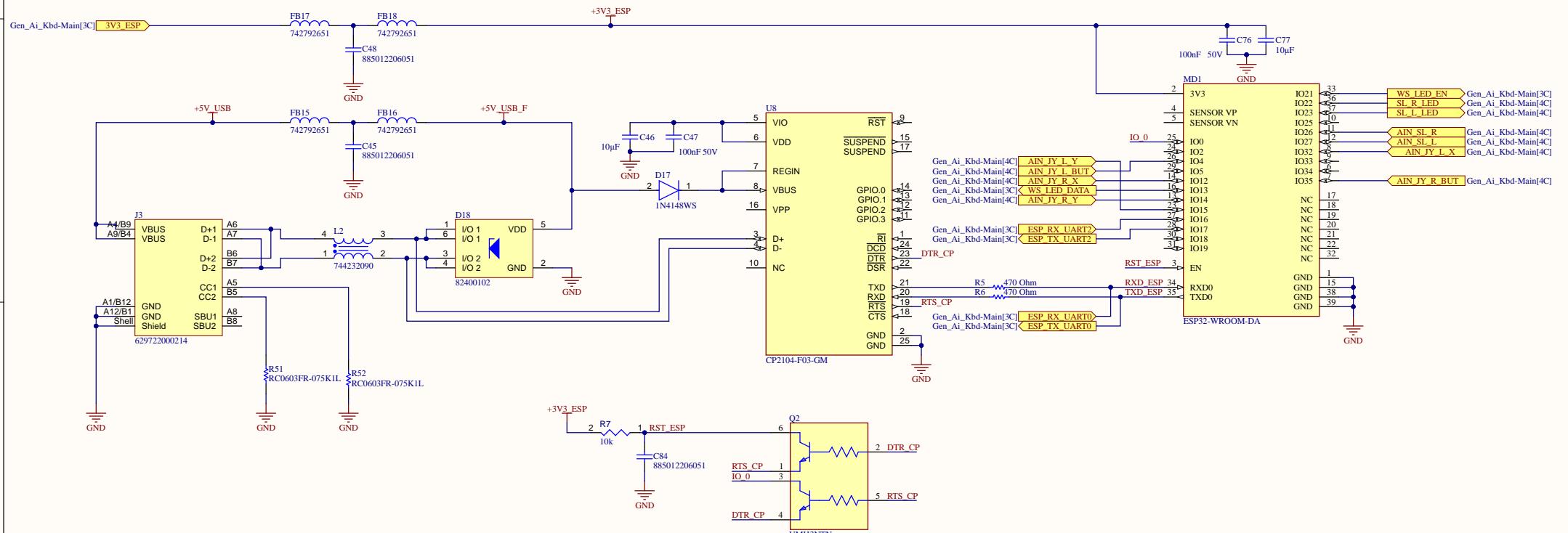
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PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:	
TITLE			
keyboard-Left.SchDoc			
SIZE A3	CAGE CODE	DWG NO.	REV
SCALE: 1:1	FILE NAME	keyboard-Left.SchDoc	SHEET 2 OF 11

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REVISION	DESCRIPTION	DATE	APPROVED

A

A



B

B

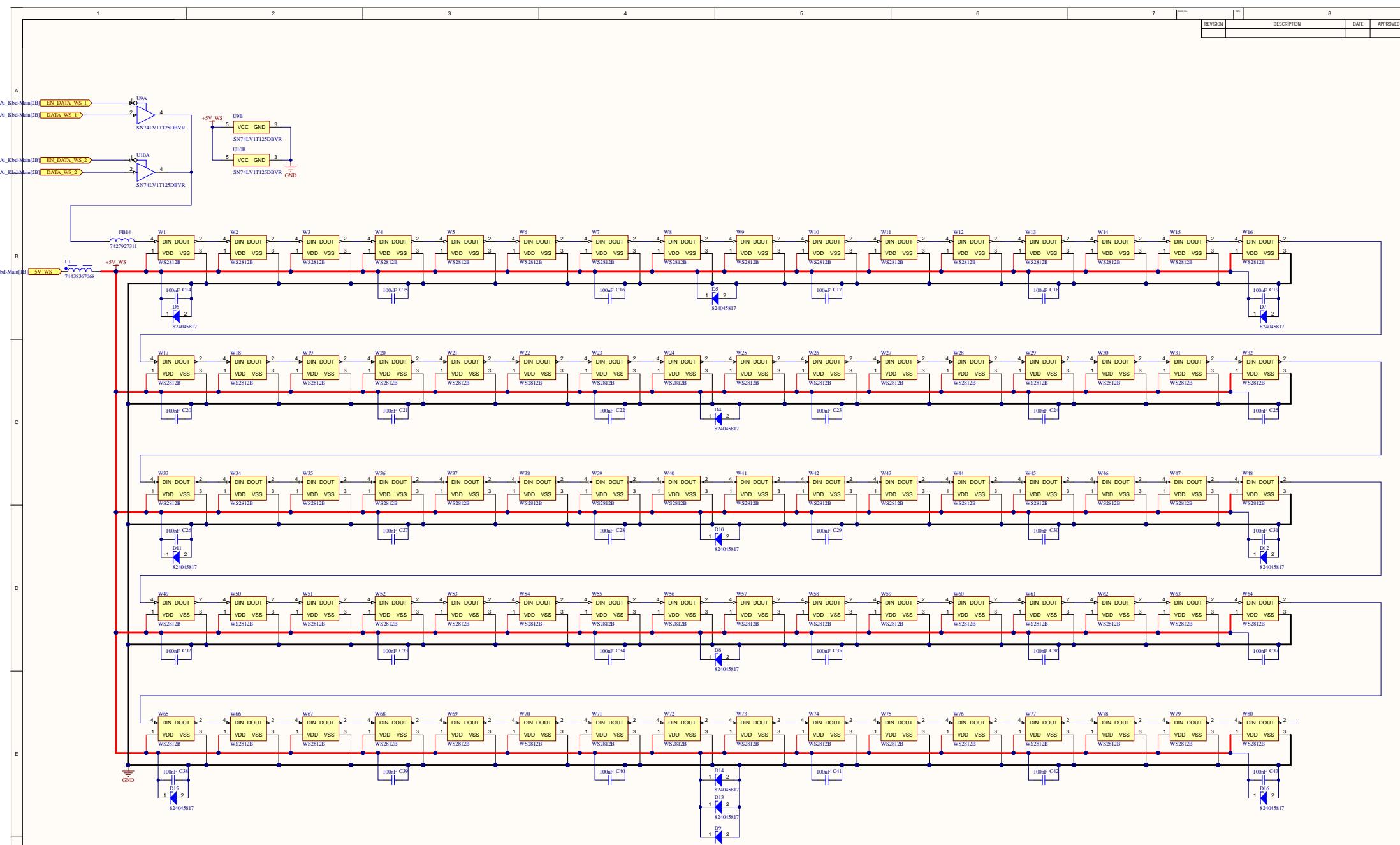
C

C

D

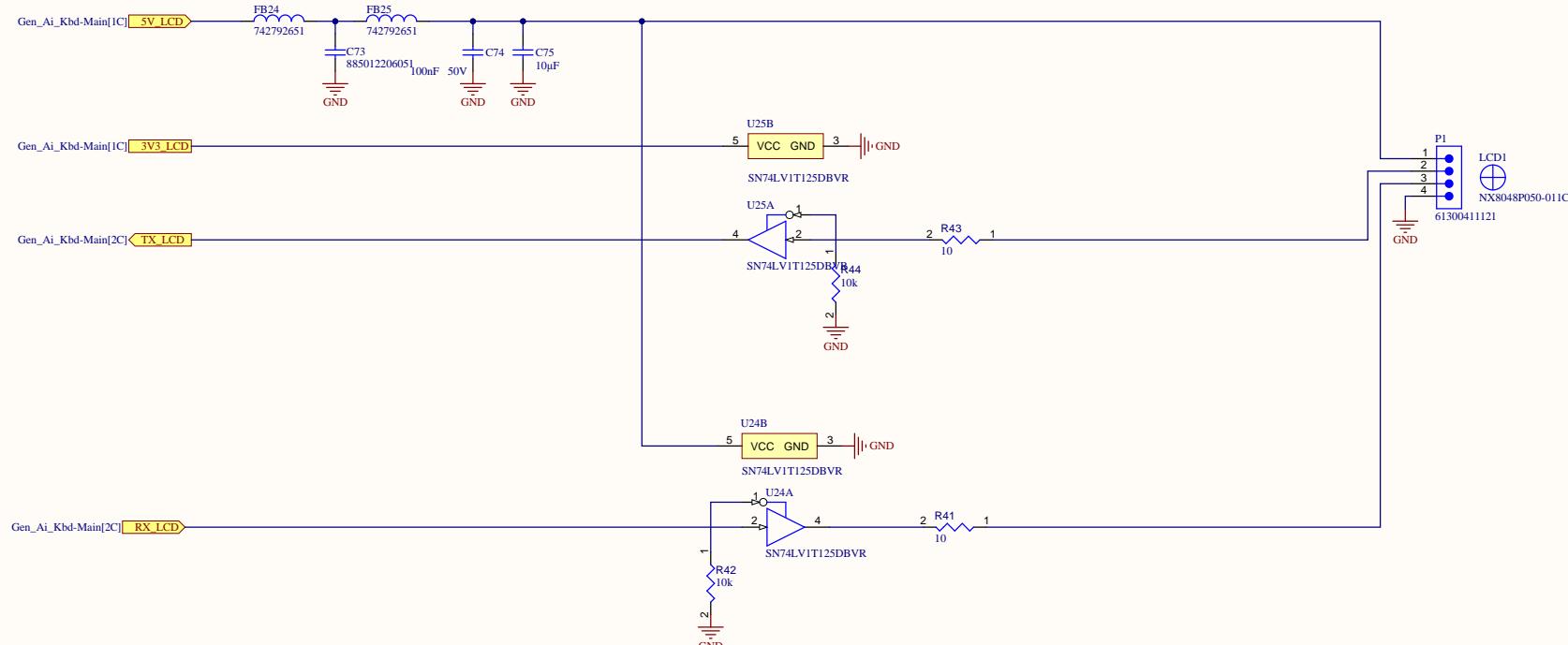
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<b>BluetoothHID.SchDoc</b>		
SIZE	CAGE CODE	DWG NO.
A3		
SCALE:	FILE NAME	BluetoothHID.SchDoc
		SHEET 3 OF 11

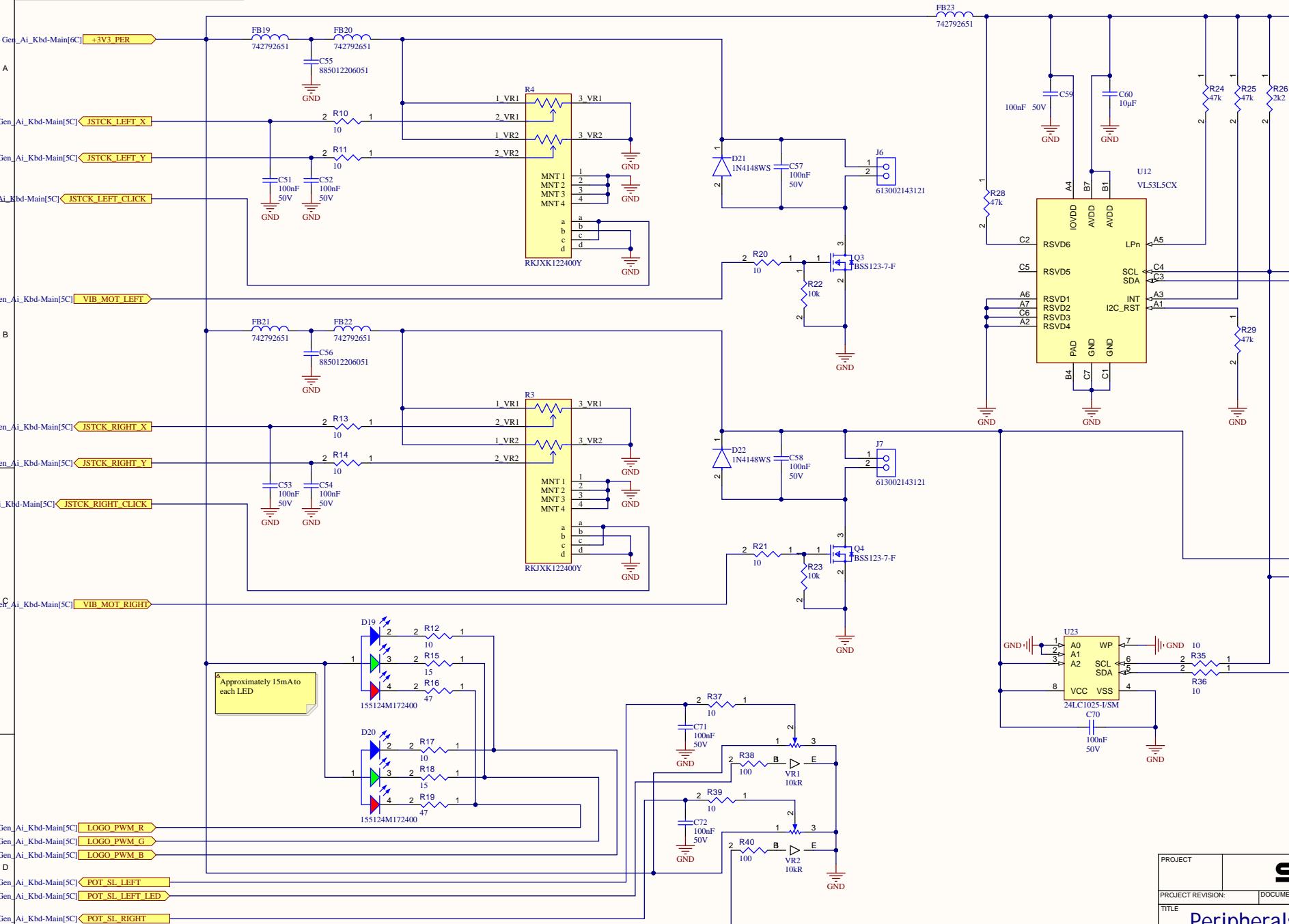


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REVISION	DESCRIPTION	DATE	APPROVED

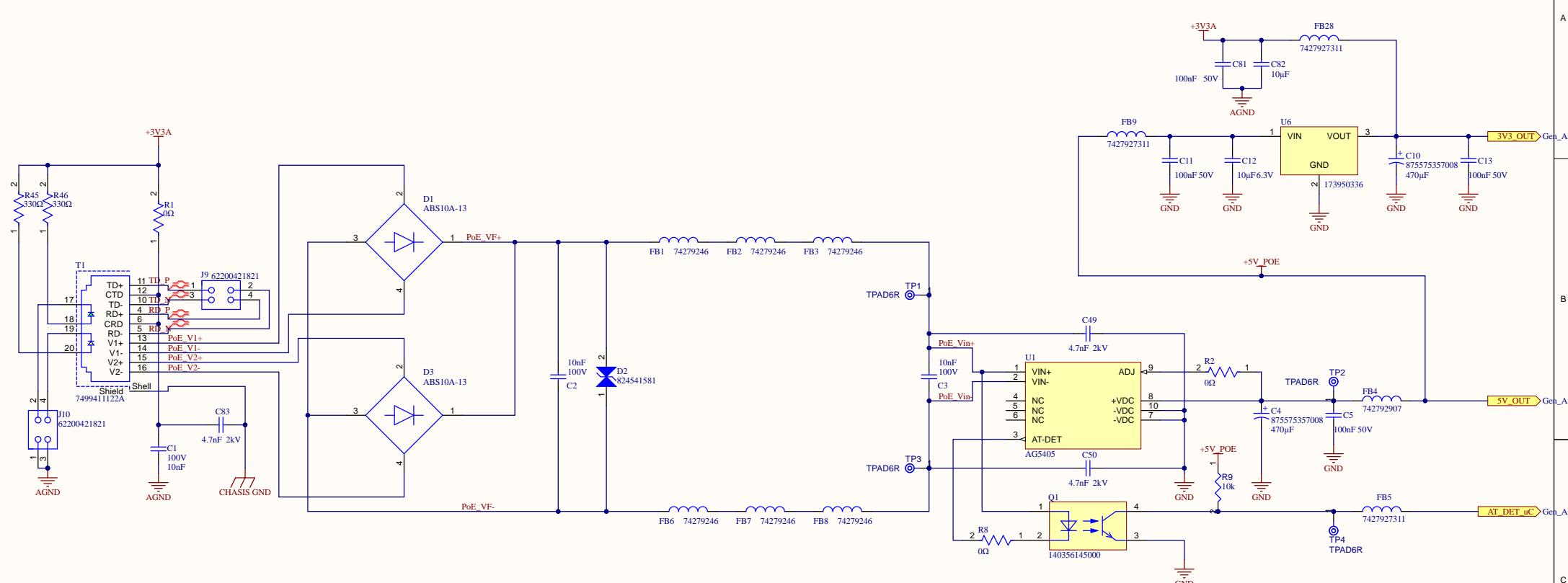


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TITLE	Display.SchDoc		
SIZE	CAGE CODE	DWG NO.	REV
A3			
SCALE:	FILE NAME	Display.SchDoc	SHEET 5 OF 11



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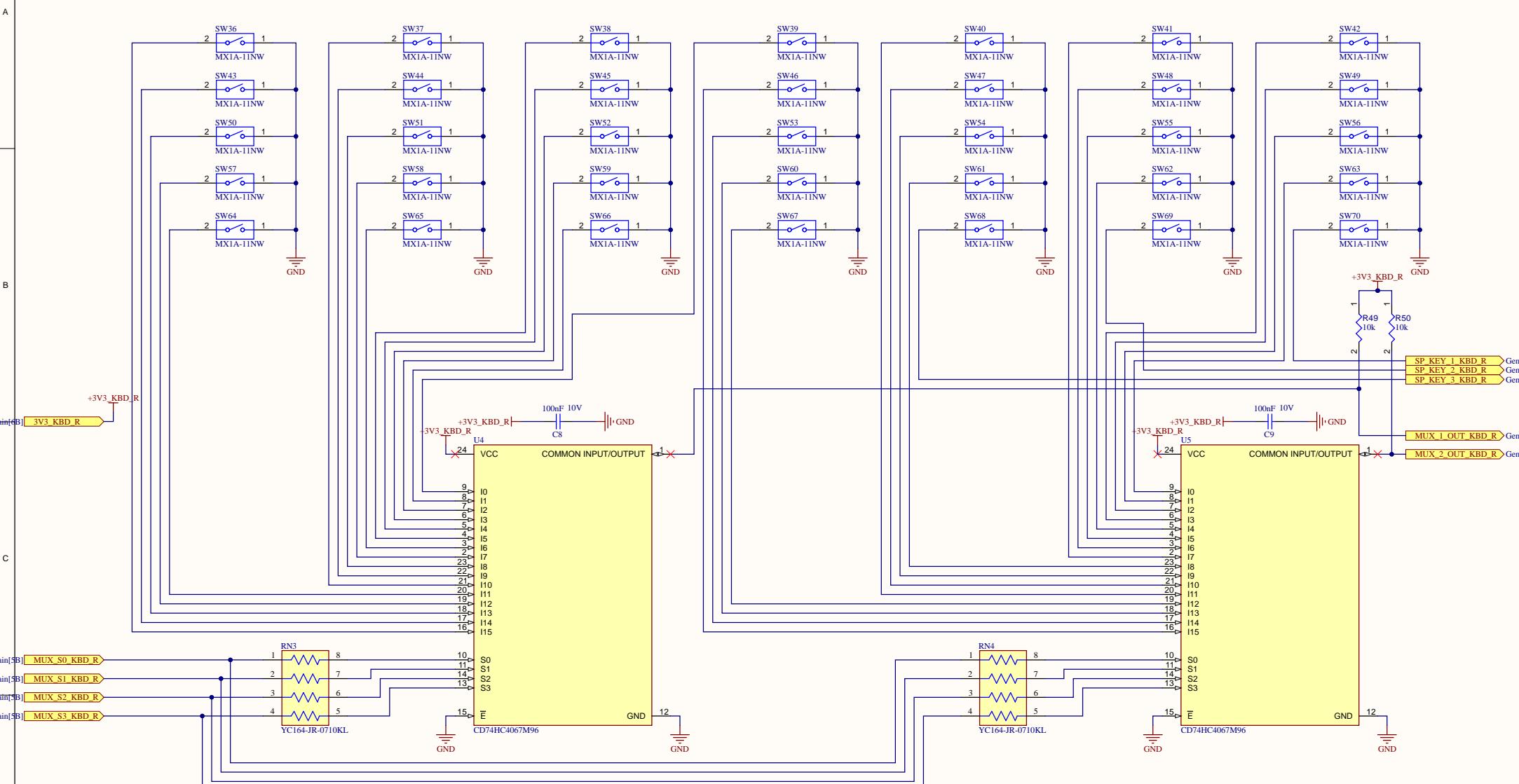
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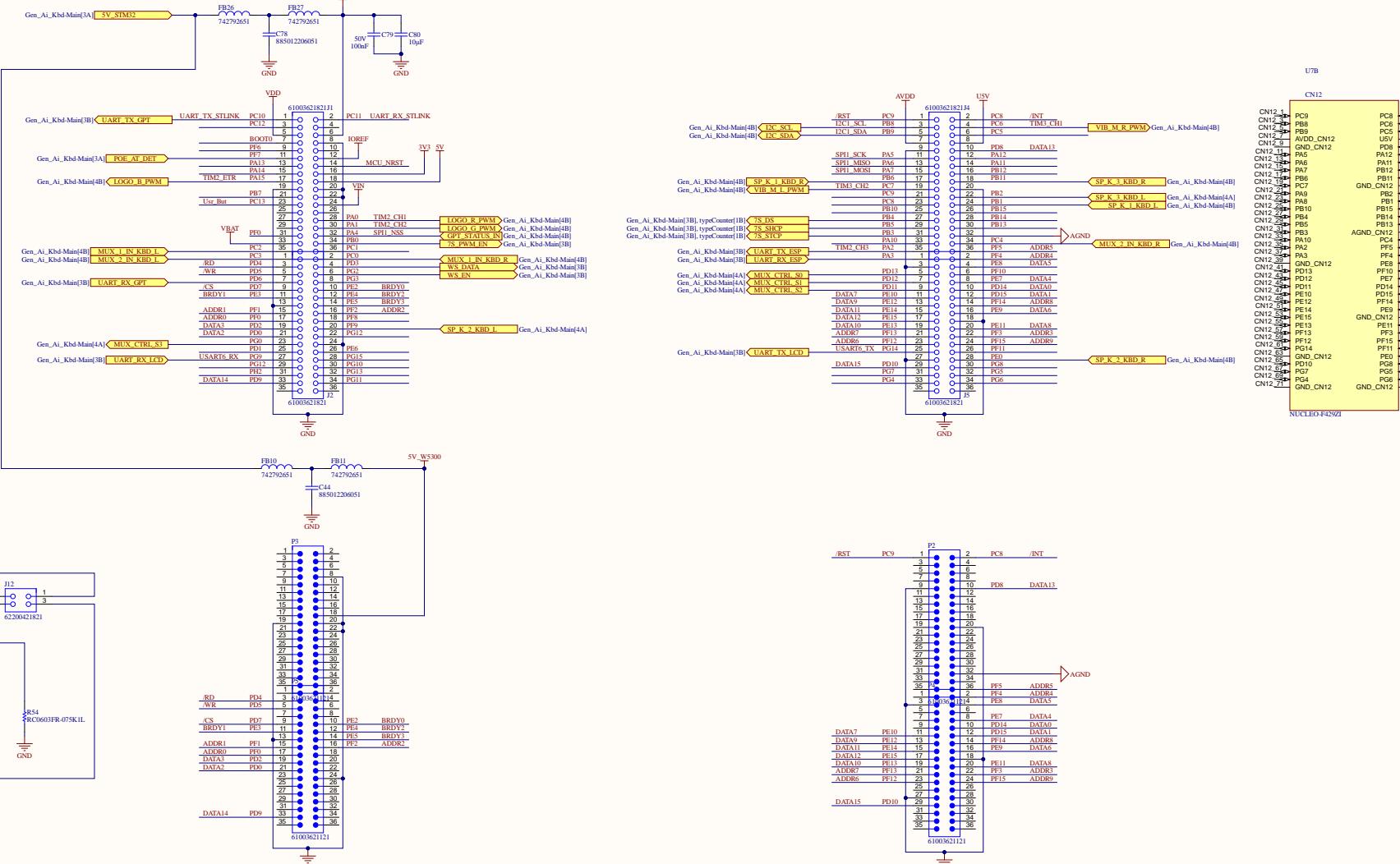
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A3			
SCALE:	FILE NAME	PoE.SchDoc	SHEET 7 OF 11

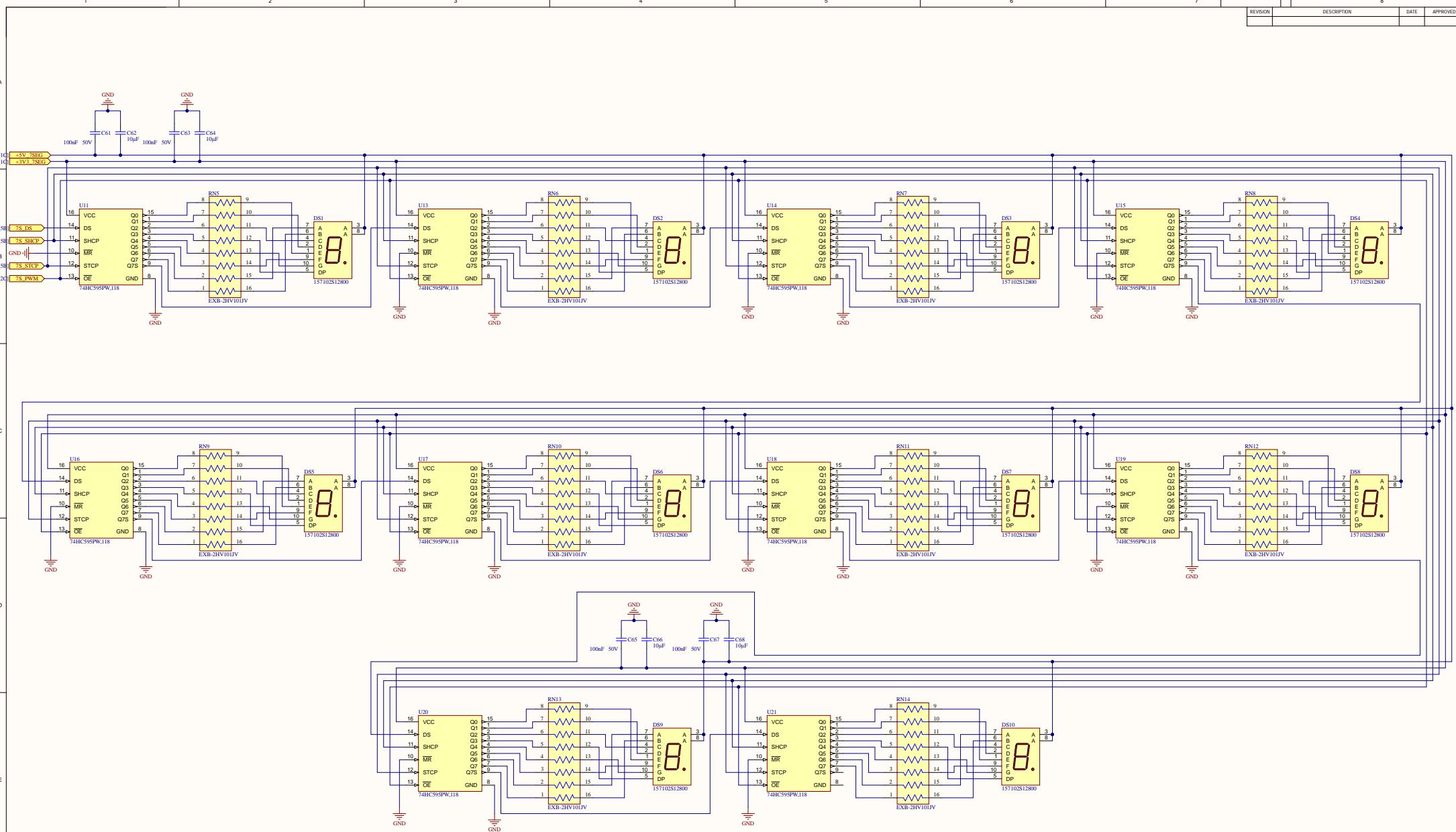
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PROJECT	SUMASTA		
PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:	
TITLE	keyboard-Right.SchDoc		
SIZE	CAGE CODE	DWG NO.	REV
A3			
SCALE:	FILE NAME	keyboard-Right.SchDoc	SHEET 8 OF 11





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REVISION	DESCRIPTION	DATE	APPROVED

A

A

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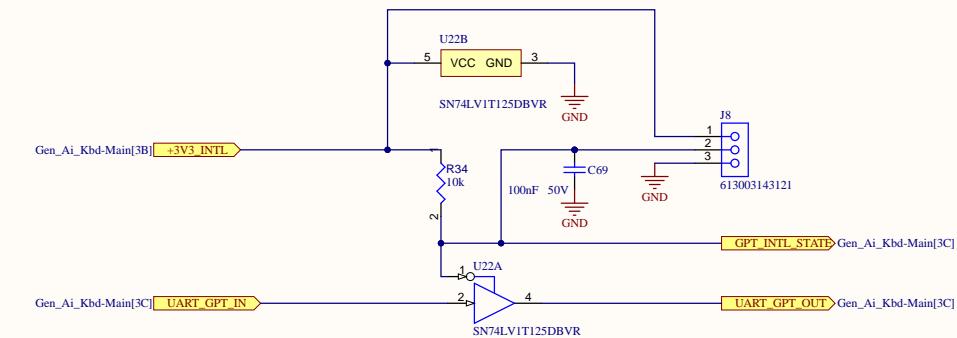
B

C

C

D

D



PROJECT	<b>SUMASTA</b>	
PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:
<b>chatGPTInterlock.SchDoc</b>		
SIZE	CAGE CODE	DWG NO.
A3		
SCALE:	FILE NAME	chatGPTInterlock.SchDoc
	SHEET	11 OF 11

