

FPGA

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
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3	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0
4	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
8	0	0	1	0	0	0	0	0	0	1	1	1	0	0	0	0
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FPGA

22	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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FPGA

44	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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FPGA

66	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
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FPGA

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FPGA

1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	0
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FPGA

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FPGA

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FPGA

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FPGA

0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0
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FPGA

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FPGA

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FPGA

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FPGA

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FPGA

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FPGA

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FPGA

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FPGA

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0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0

FPGA

1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
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1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
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0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	1	1	1	0	0	0	0	0
1	0	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0
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0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

FPGA

0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0
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0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	1	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0

FPGA

85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101
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0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
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0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0
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0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0
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0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0

FPGA

0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0
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0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
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0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0
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0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0

FPGA

0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0
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0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0
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0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0
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0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

FPGA

[illegible]

[illegible]

FPGA

[illegible]

0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	1	0
0	0	0	0	1	0	0
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0	0	0	0	1	0	0
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0	0	0	0	0	1	0
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0	0	0	0	0	0	0
0	0	0	0	0	1	0
0	0	0	0	1	0	0

0	0	0	1	0	1	0
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