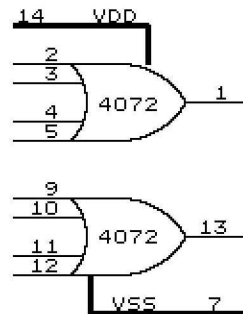
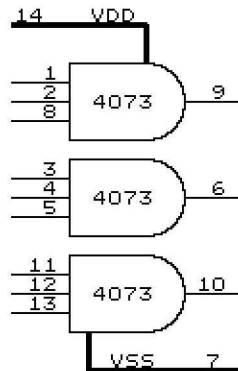


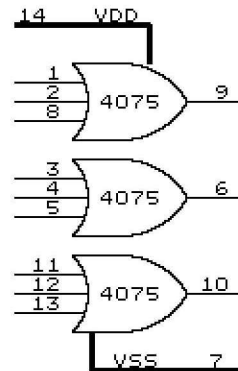
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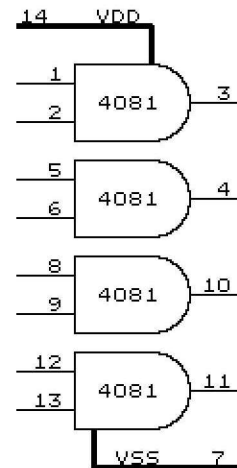
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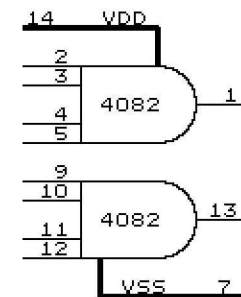
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4081

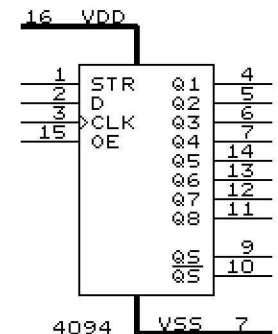


4082



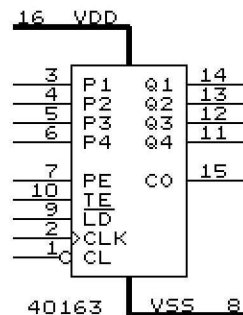
4094

8-BIT SHIFT REG  
WITH OUTPUT LATCH  
TRISTATE OUTPUT  
SIPO



40163

SYNCHRONOUS  
BINARY COUNTER  
CLEAR-SYNCH  
LOAD-SYNCH



40193

SYNC 4-BIT UP/DN  
BINARY COUNTER

PE=LOAD ASYN  
BO=BORROW OUT  
CA=CARRY OUT  
RST=CLR ASYN

