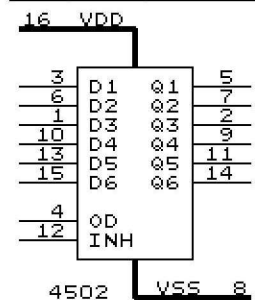


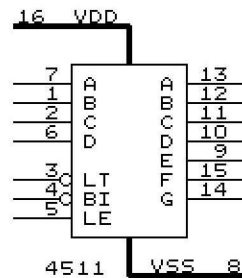
4502

STROBED HEX
INVERTER/BUFFER

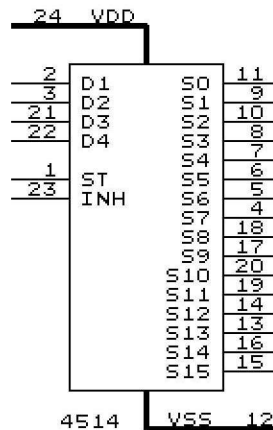
OD	INH	Q
0	0	\overline{D}
0	1	0
1	X	Hi-Z



4511

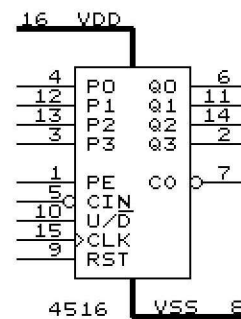
BCD-TO-7 SEGMENT
LATCH/DCOD/DRIVELT=LAMP TEST
BI=BLANKING I/P
LE=LATCH ENABLE

4514

4-BIT LATCHED/
4-TO-16 LINE DECO.
ACTIVE HI OUT
ST= LATCH ENABLE

4516

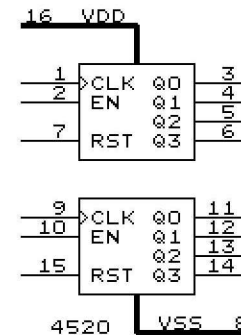
BINARY U/D COUNTER

PE=LOAD SYNCH
CIN=CARRY IN
CO=CARRY OUT
RST=CLR ASYNCH

4520

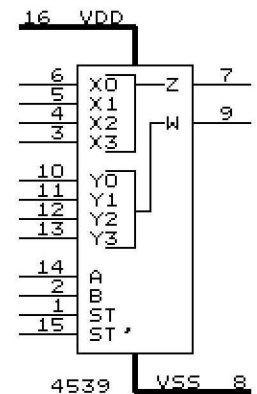
DUAL 4-BIT SYNCH
COUNTER

EN= CLK ENABLE



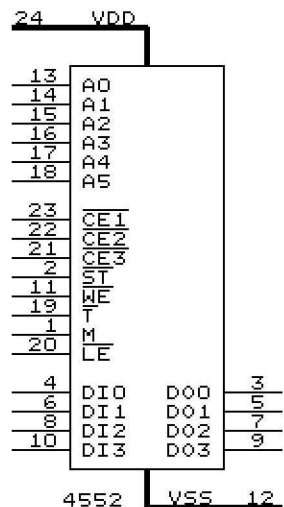
4539

DUAL 4-1 MUX

ST=STROBE X
ST'=STROBE Y

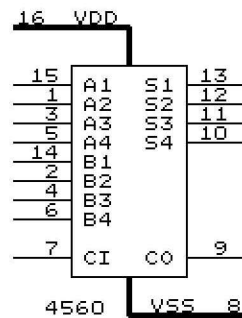
4552

64x4 BIT RAM

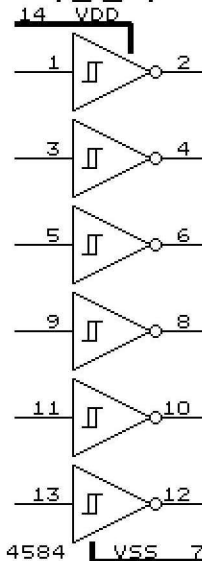


4560

4-BIT BCD ADDER



4584



4585

4-BIT MAGNITUDE
COMPARATOR