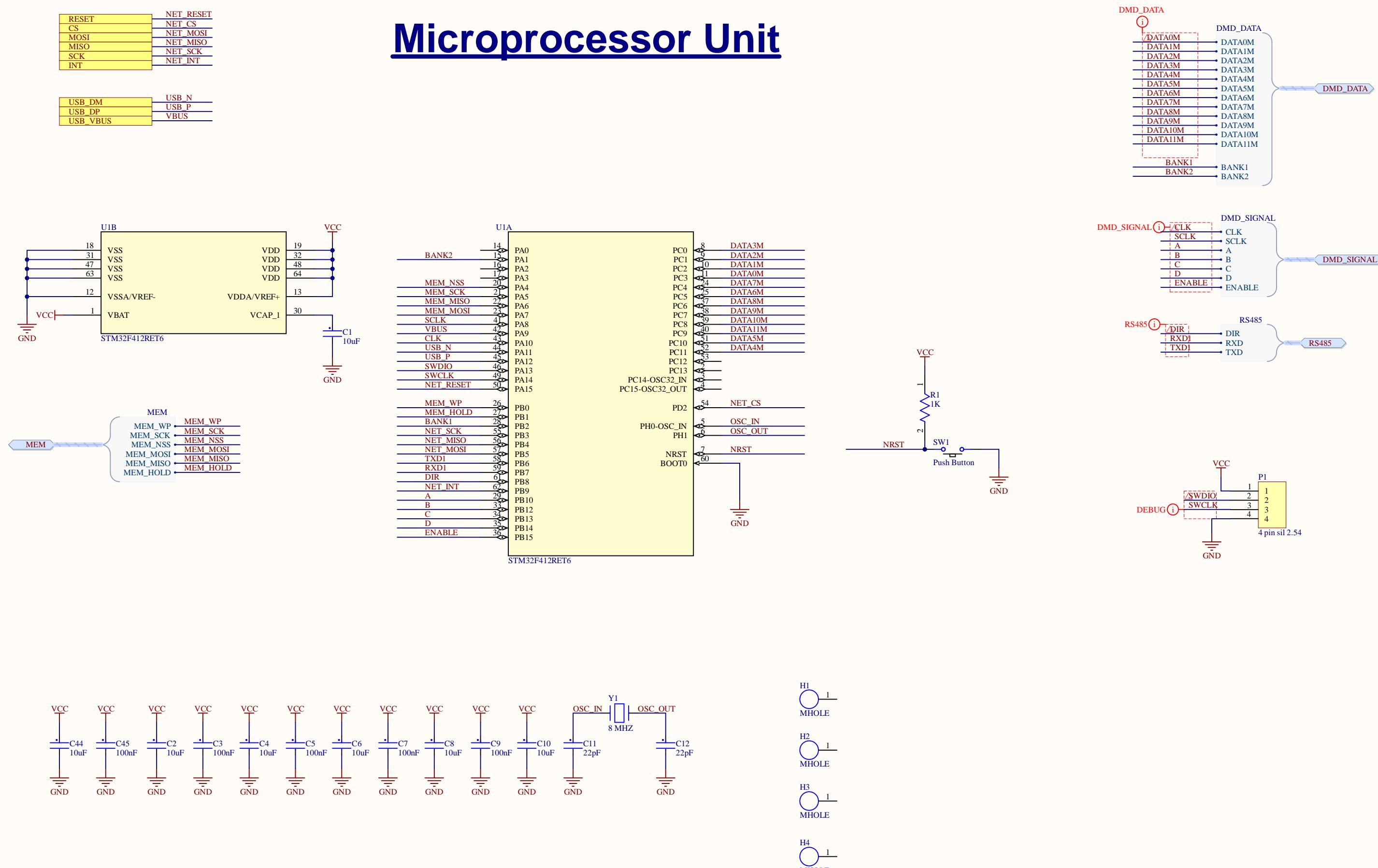


# Microprocessor Unit



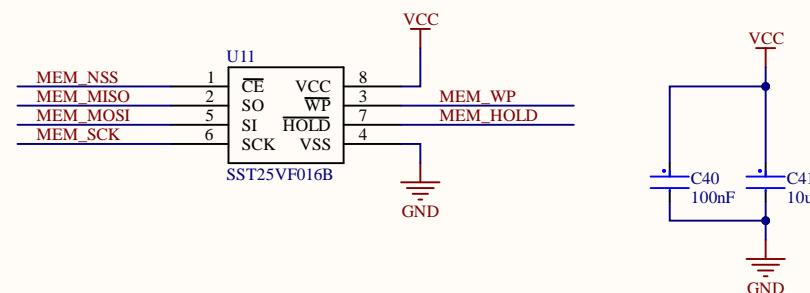
A

# Flash/EEPROM Memory

B



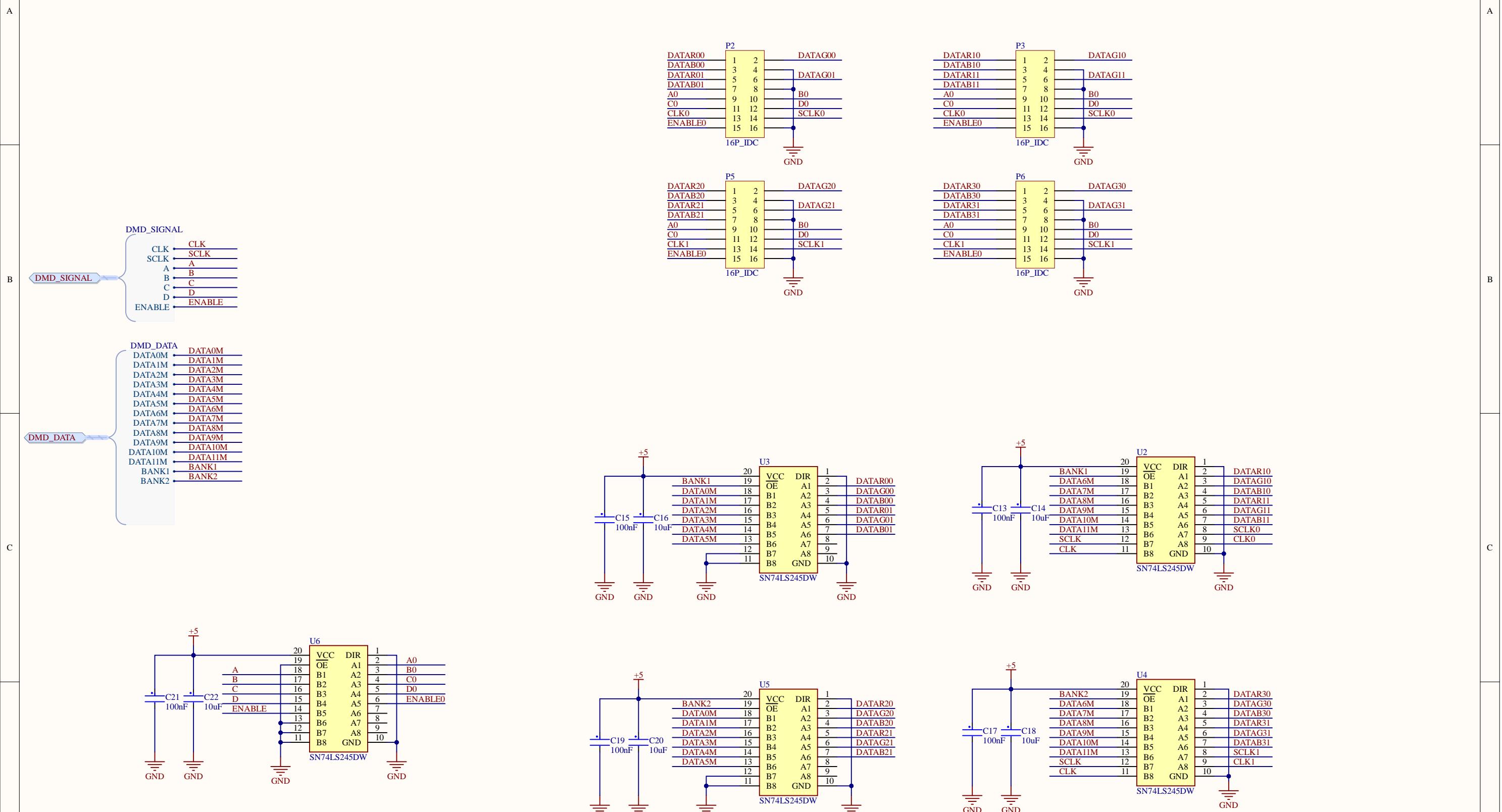
C



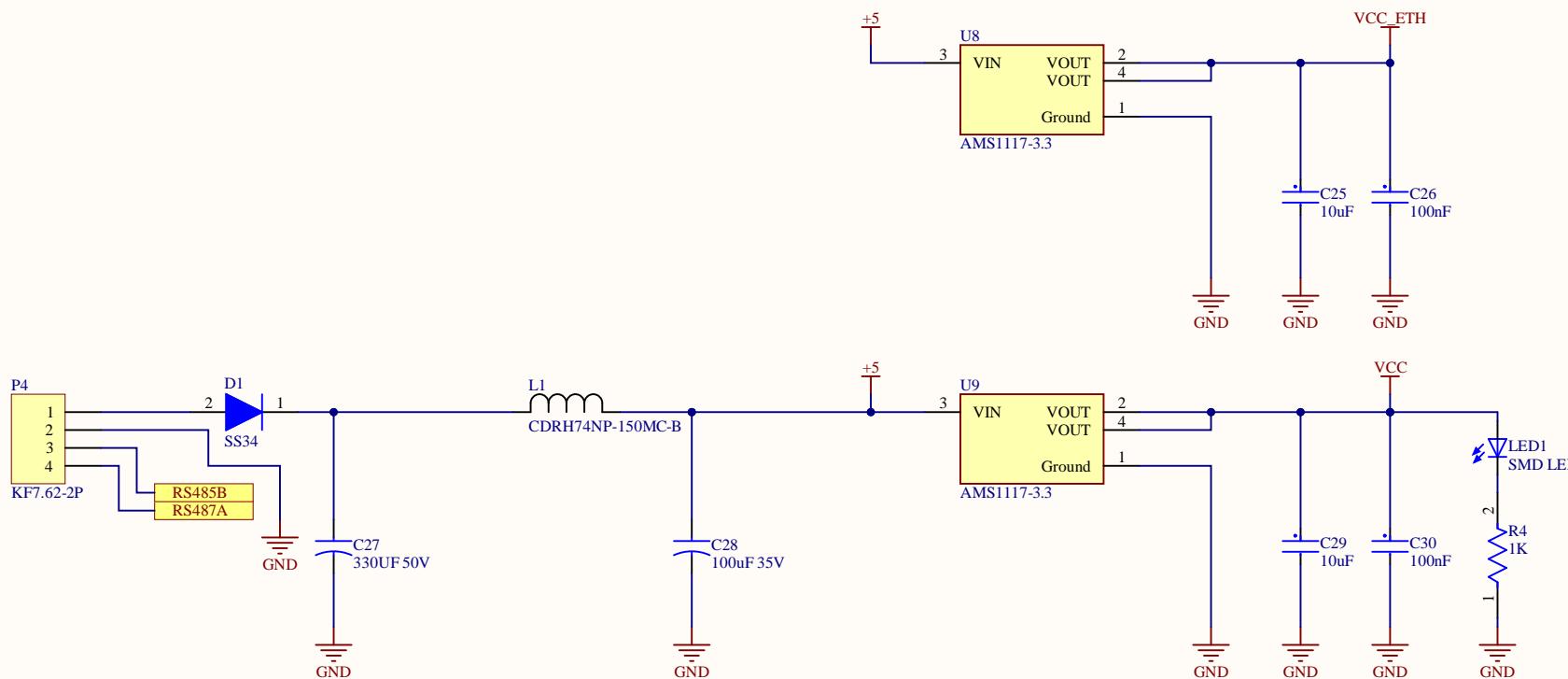
D

Project Name: **DMD\_Card.PrjPcb**
**AFRA**  
**AFRA TECHNOLOGY**
Size: **A4** Doc Num: **2** Version: **V-2.0**Date: **9/24/2023** Time: **5:20:18 PM** Sheet **2** of **8**File: **Flash\_EEPROM\_Memory.SchDoc**Drawn by: **Hanif**

# DMD Output Interface



# Power Management

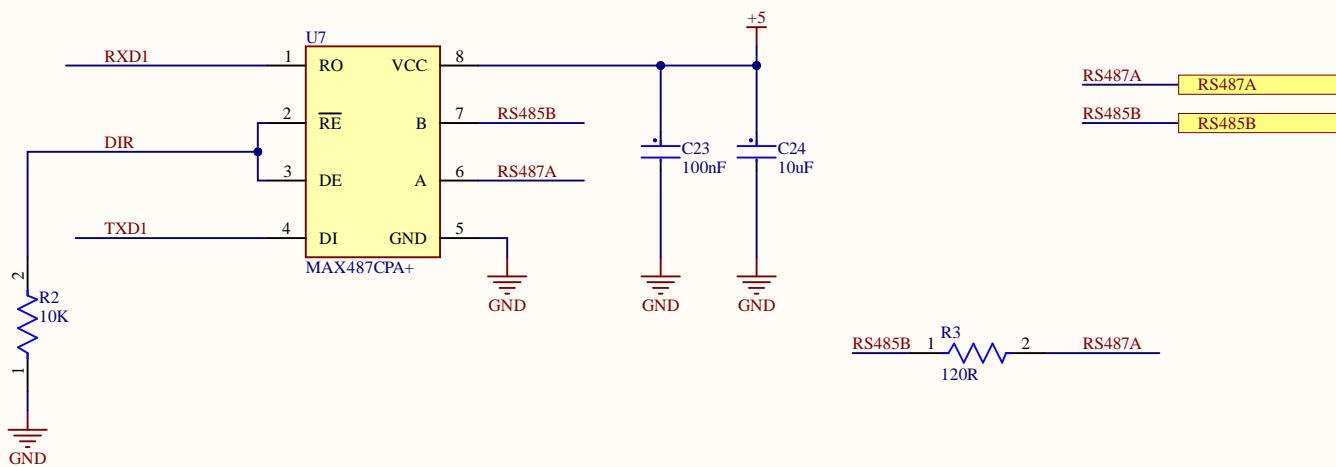


# RS485 Interface

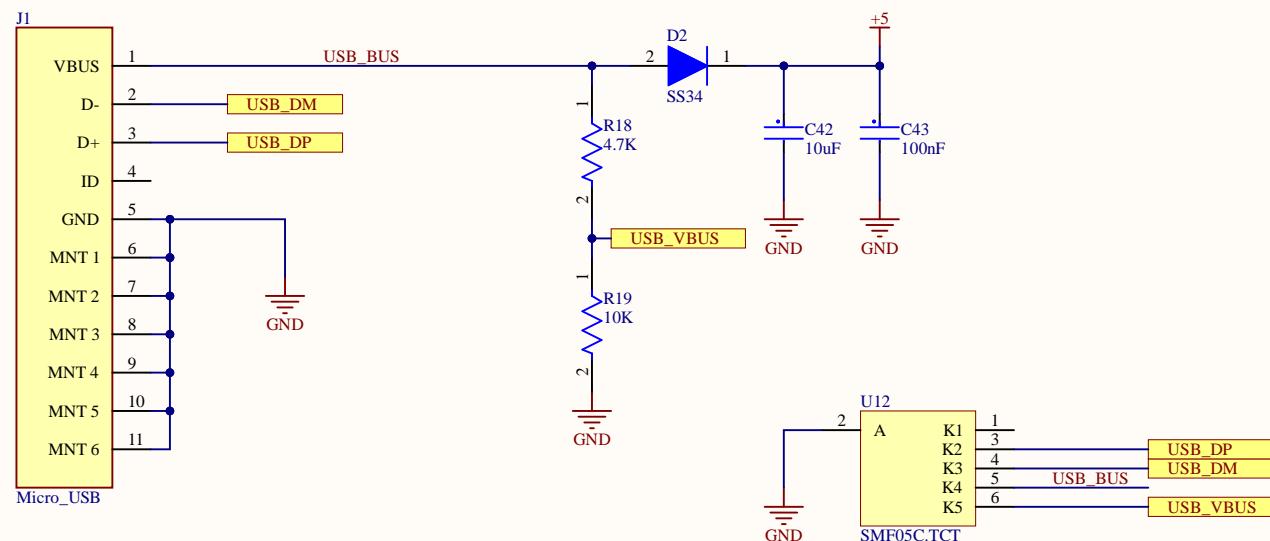
RS485

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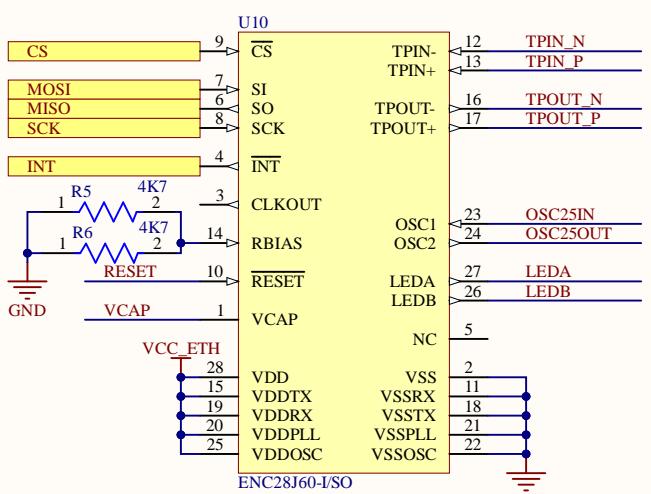
    graph LR
        RS485[RS485] --> RXD1(RXD1)
        RS485 --> TXD1(TXD1)
        RS485 --> DIR(DIR)
    
```



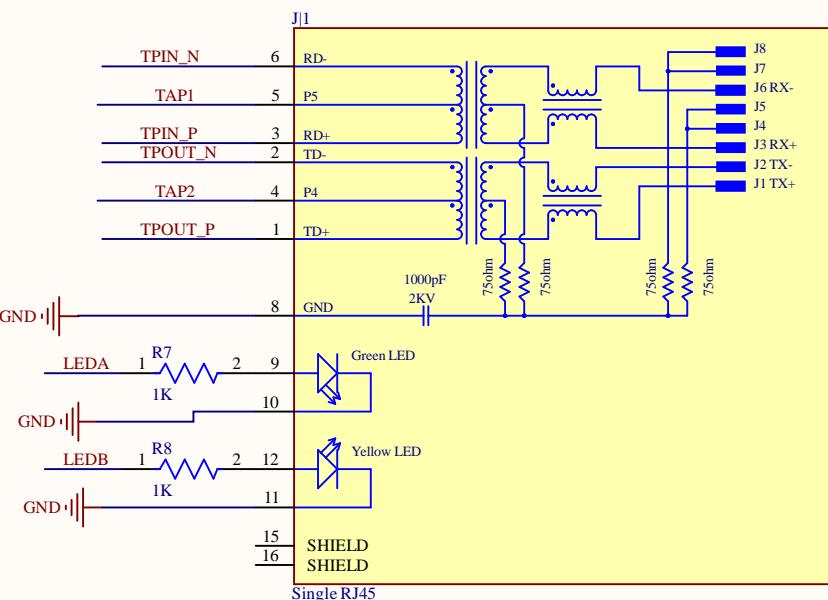
# USB Interface



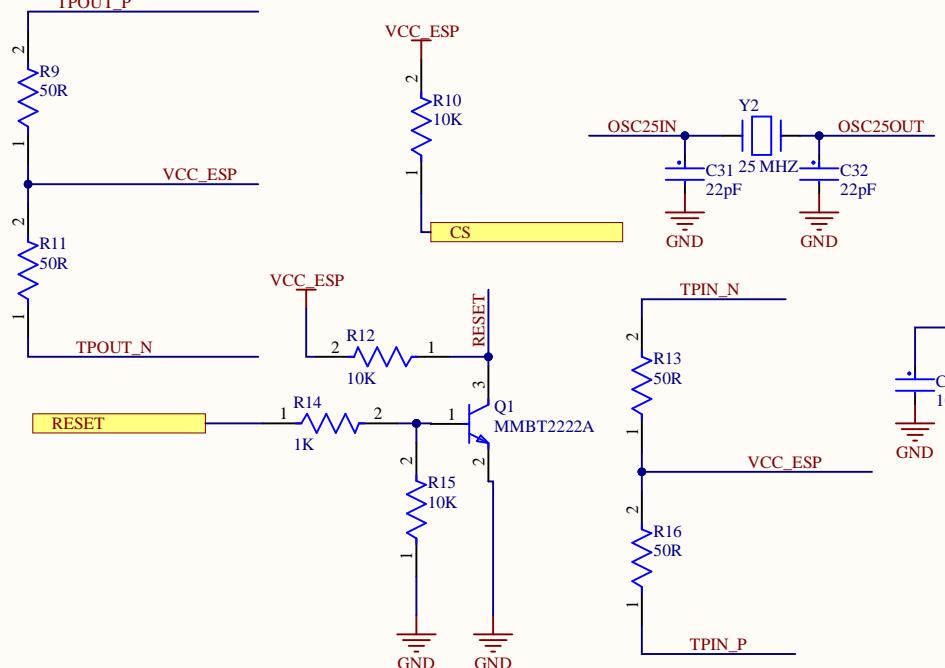
A



B



C



Title

Size	Number	Revision
A		
Date: 9/24/2023	Sheet of	
File: G:\PCB Work\enc.SchDoc		Drawn By:



