Microprocessor & Computer Architecture (µpCA)

UE19CS252

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Microprocessor & Computer Architecture (μpCA)

Block Transfer Instructions

Procedure Call or Subroutine

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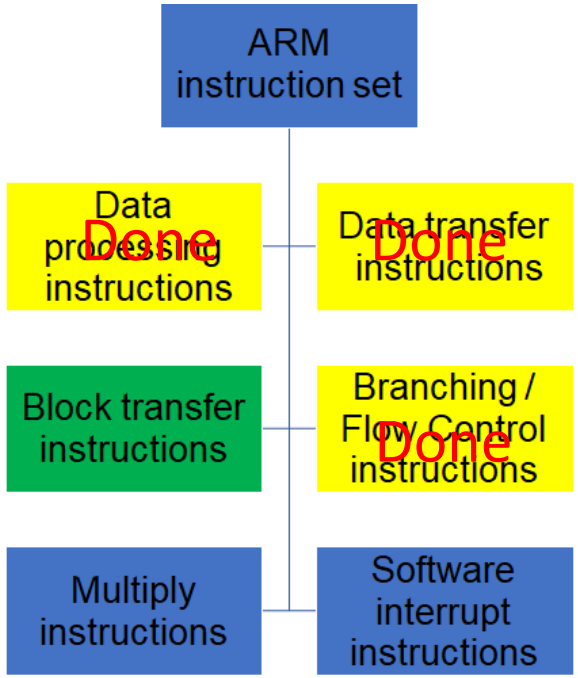
Syllabus

Unit 1: Basic Processor Architecture and Design

- ~~Microprocessor Overview~~
- ~~CISC VS RISC~~
- ~~Introduction to ARM Processor & Applications~~
- ~~ARM Architecture Overview~~
- ~~Different ARM processor Modes~~
- ~~Register Bank~~
- ~~ARM Program structure~~
- ~~ARM Instruction Format~~
- **ARM INSTRUCTION SET**
  - ~~Data Processing Instructions~~
  - ~~Flow Control Instructions~~
  - ~~Data Transfer Instructions~~
  - ~~Block Transfer Instructions (Stack)~~
  - **Procedure Call**



Procedure Call

R0-R7	
R8-R12	
R13 (SP)	
R14 (LR)	
R15 (PC)	**0x0010**
CPSR	

Main Procedure

0x0000	Instruction 1
0x0004	Instruction 2
0x0008	Instruction 3
0x000C	**Procedure Call**
0x0010	**Instruction 4**
0x0014	Instruction 5
	:::::::::::::::::
	Instruction Last

Called Procedure

0x0020	**Instruction 1**
0x0024	Instruction 2
0x0028	Instruction 3
0xxxxx	Return Instruction

General Structure of Procedure Call:

R0-R7	
R8-R12	
R13 (SP)	
R14 (LR)	**0x0010**
R15 (PC)	**0x0020**
CPSR	

Main Procedure

0x0000	Instruction 1
0x0004	Instruction 2
0x0008	Instruction 3
0x000C	**Procedure Call**
0x0010	**Instruction 4**
0x0014	Instruction 5
	.....
	Instruction Last

Called Procedure

0x0020	**Instruction 1**
0x0024	Instruction 2
0x0028	Instruction 3
0xxxxx	Return Instruction

LR=PC

PC= Address of the 1st Instruction

General Structure of Procedure Return

R0-R7	
R8-R12	
R13 (SP)	
R14 (LR)	
R15 (PC)	**0x0010**
CPSR	

Main Procedure

0x0000	Instruction 1
0x0004	Instruction 2
0x0008	Instruction 3
0x000C	**Procedure Call**
0x0010	**Instruction 4**
0x0014	Instruction 5
	:::::::::::::::::
	Instruction Last

Called Procedure

0x0020	**Instruction 1**
0x0024	Instruction 2
0x0028	Instruction 3
0xxxxx	Return Instruction

PC=LR

General Structure of Procedure Call & Return

Main Procedure	
0x0000	Instruction 1
0x0004	Instruction 2
0x0008	Instruction 3
0x000C	**BL Procedure**
0x0010	**Instruction 4**
0x0014	Instruction 5
	:::::::::::::::::
	Instruction Last

Called Procedure	
0x0020	**Procedure: Instruction 1**
0x0024	Instruction 2
0x0028	Instruction 3
0xxxxx	**MOV PC LR**
	or
	BX LR

Procedure Call : Example 1

```
main:   mov r1, #3
        bl foo
        add r2, r0, r1
    swi 0x11
foo:
        mov r0, #2
        bx lr
```

```
main:   mov r1, #3
        bl foo
        add r2, r0, r1
    swi 0x11
foo:
        mov r0, #2
        mov pc, lr
```

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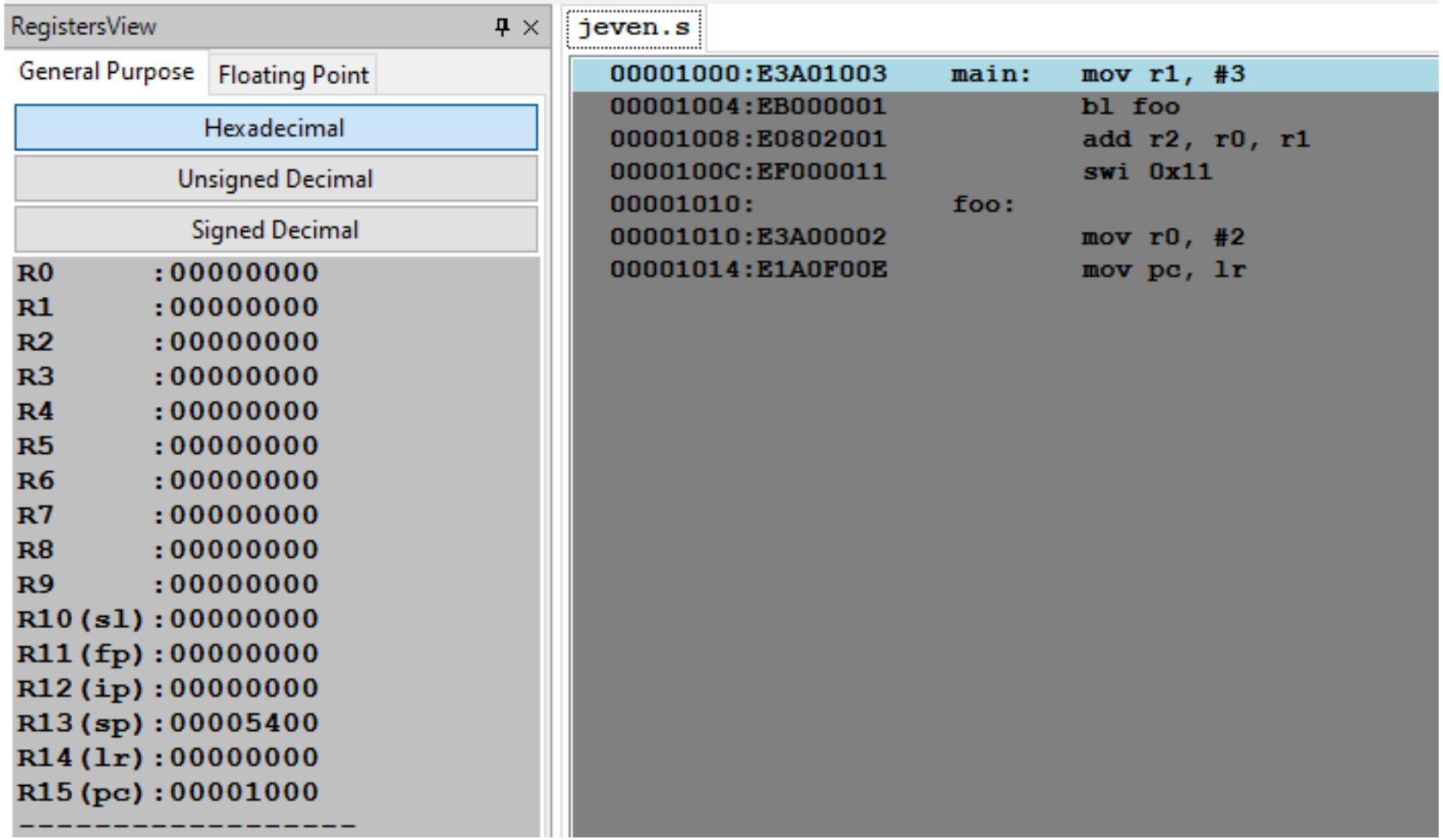
Procedure Call : Example 1



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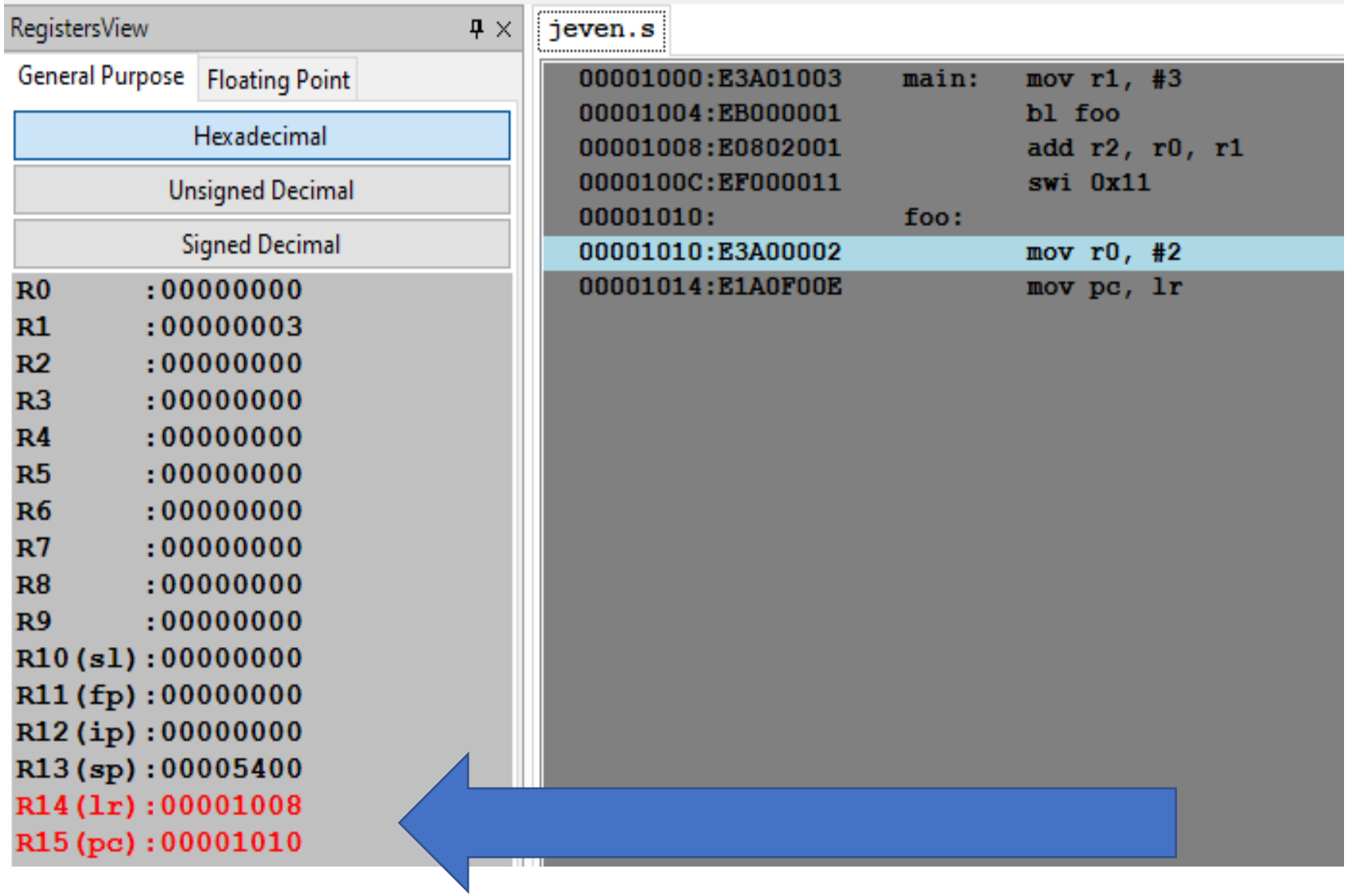
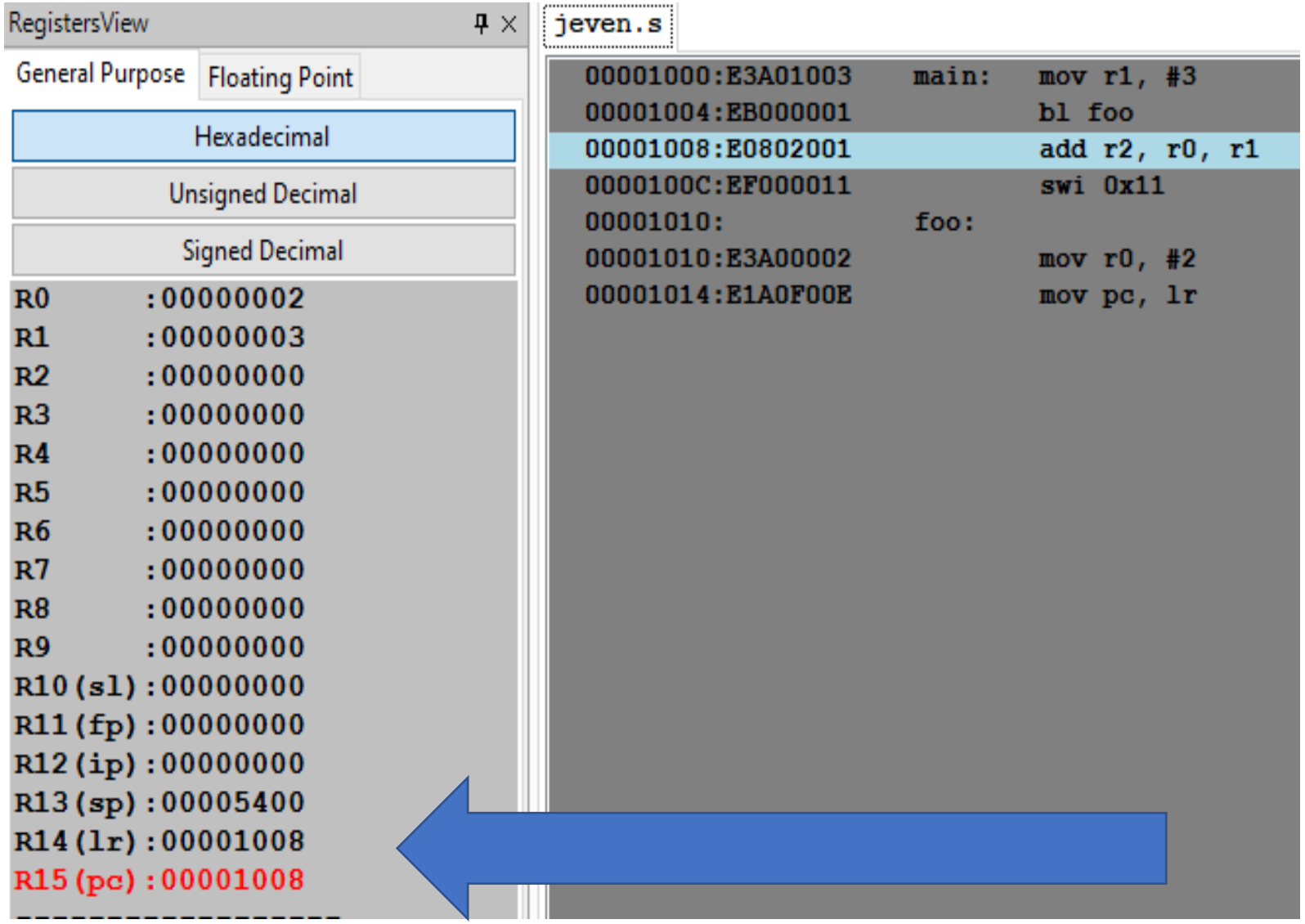
Procedure Call : Example 1



Procedure Call : Example 1



PARAMETER PASSING TO PROCEDURES USING STACK

```
        LDR    R4, =A
        MOV  R1, #25                    ; parameter1
        MOV  R2, #25                    ; parameter2
        STMFD R13!, { R1, R2}           ; parameters are PUSHed on stack.
        BL  LINK
        STR R0, [R4]                    ; return value in Reg. R0.
        SWI 0x11

LINK:  LDMFD R13!, { R4, R5}            ; parameters are POPed from the  stack
        ADD R0, R4, R5                  ;  Result is in register R0.
        MOV PC, LR

  A:    .WORD  0

.END
```

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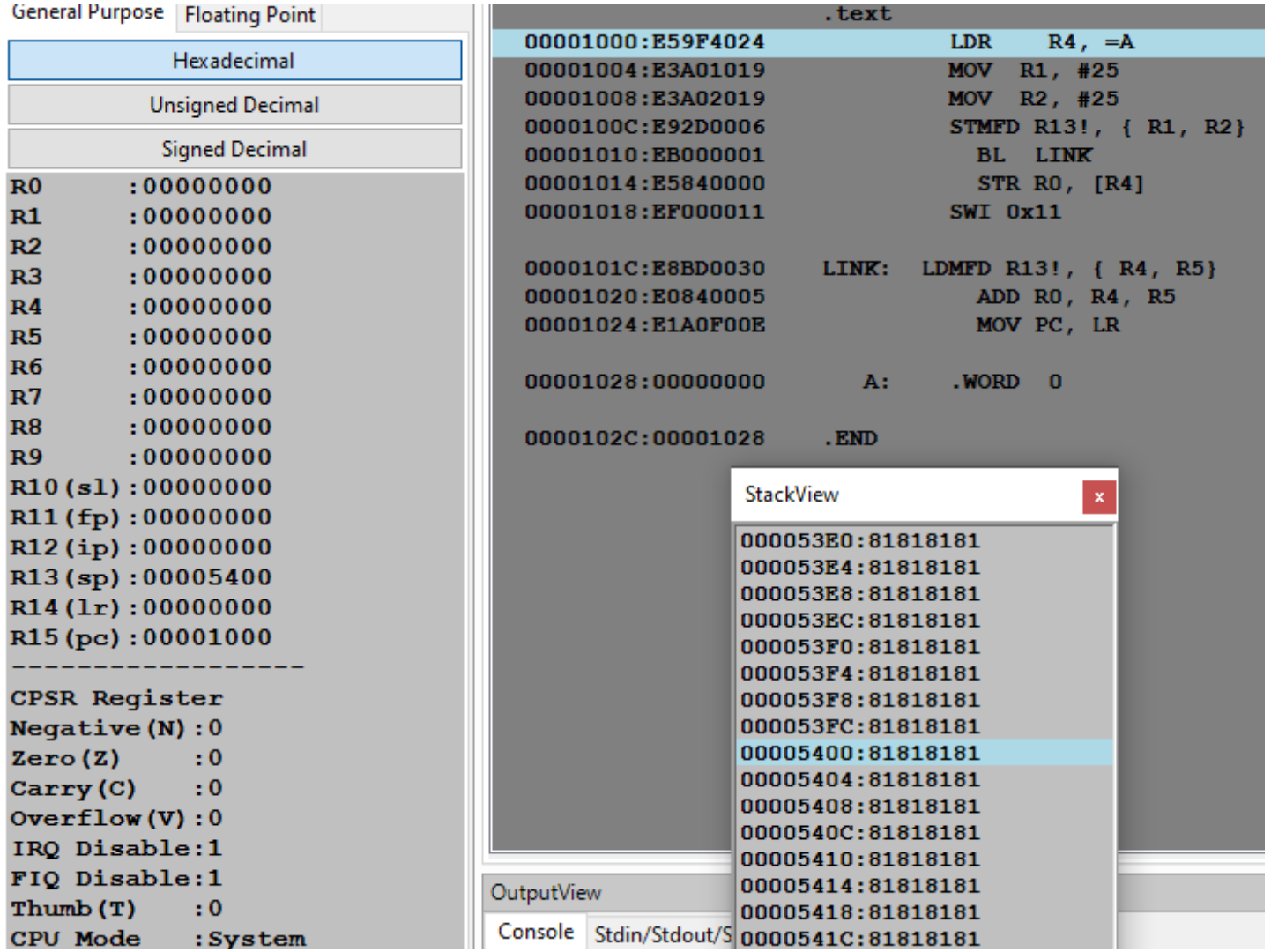
PARAMETER PASSING TO PROCEDURES USING STACK



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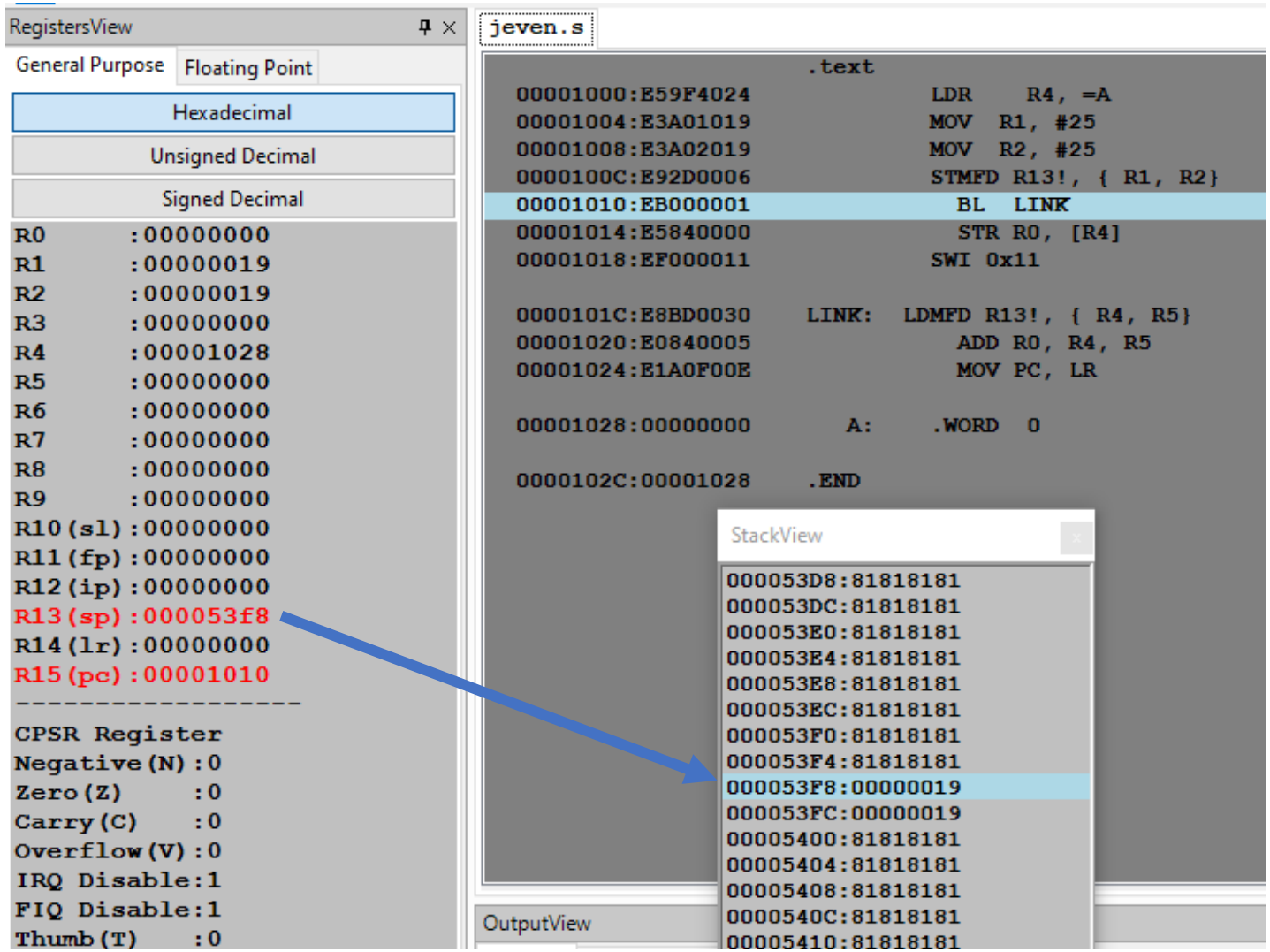

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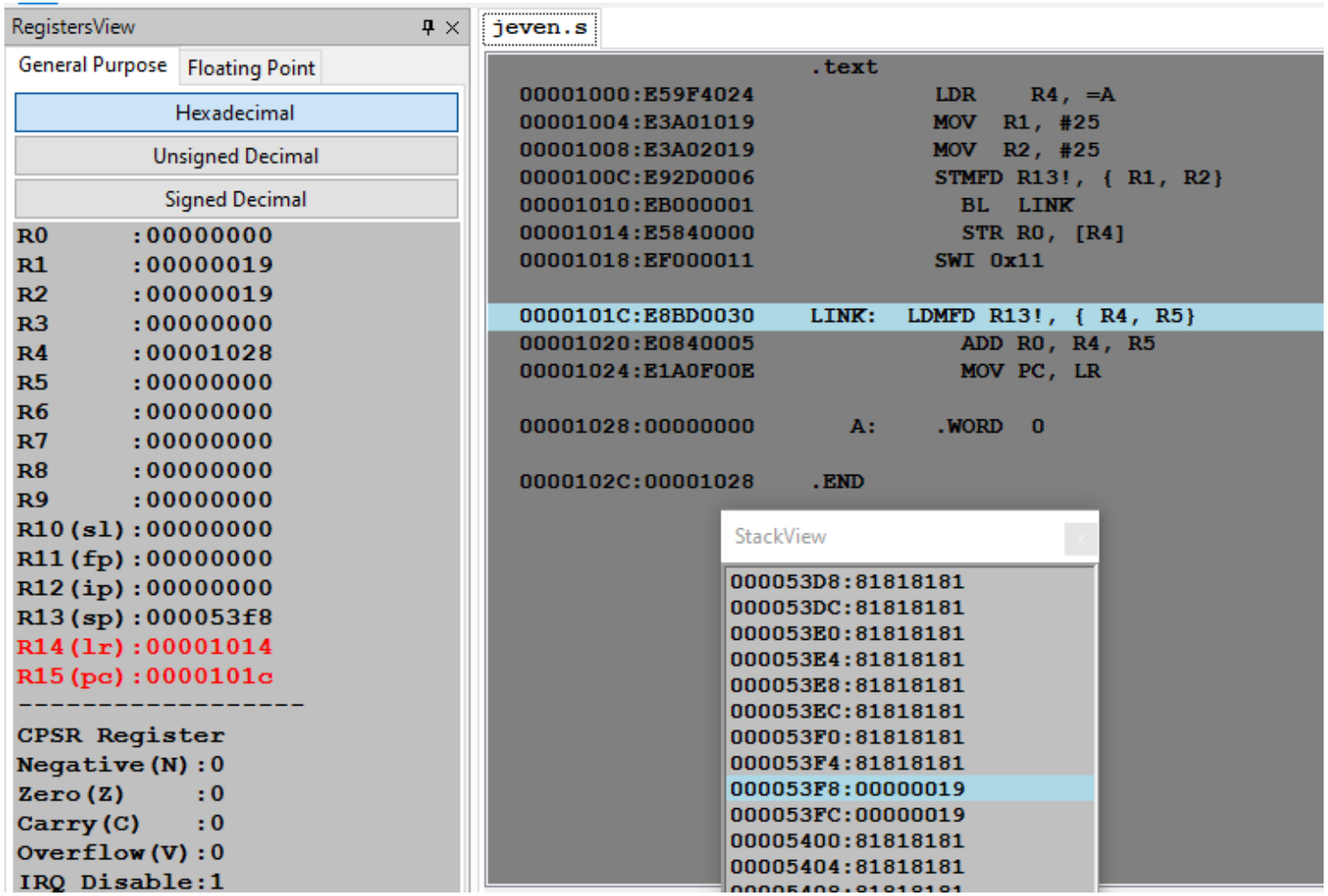

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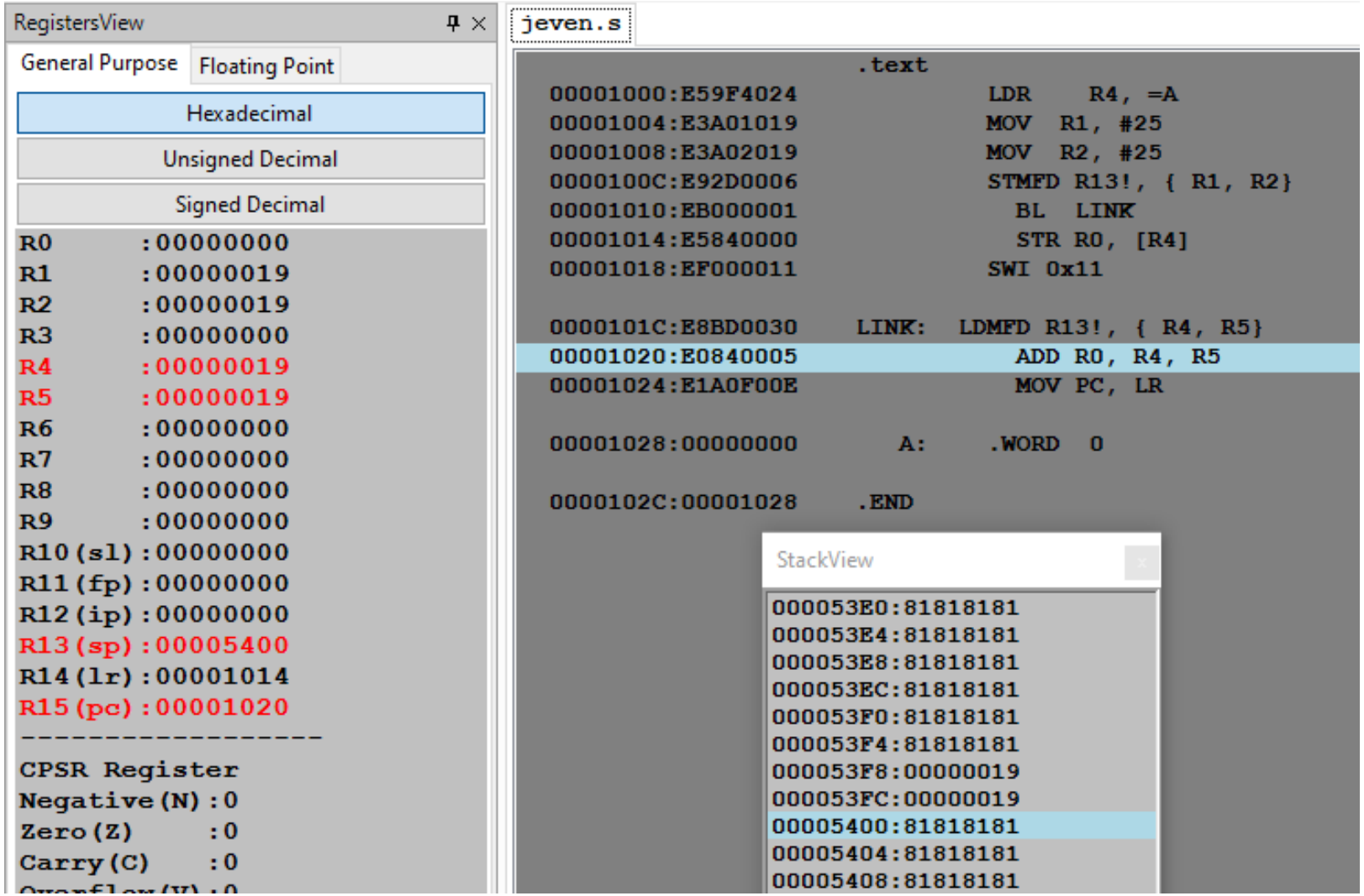

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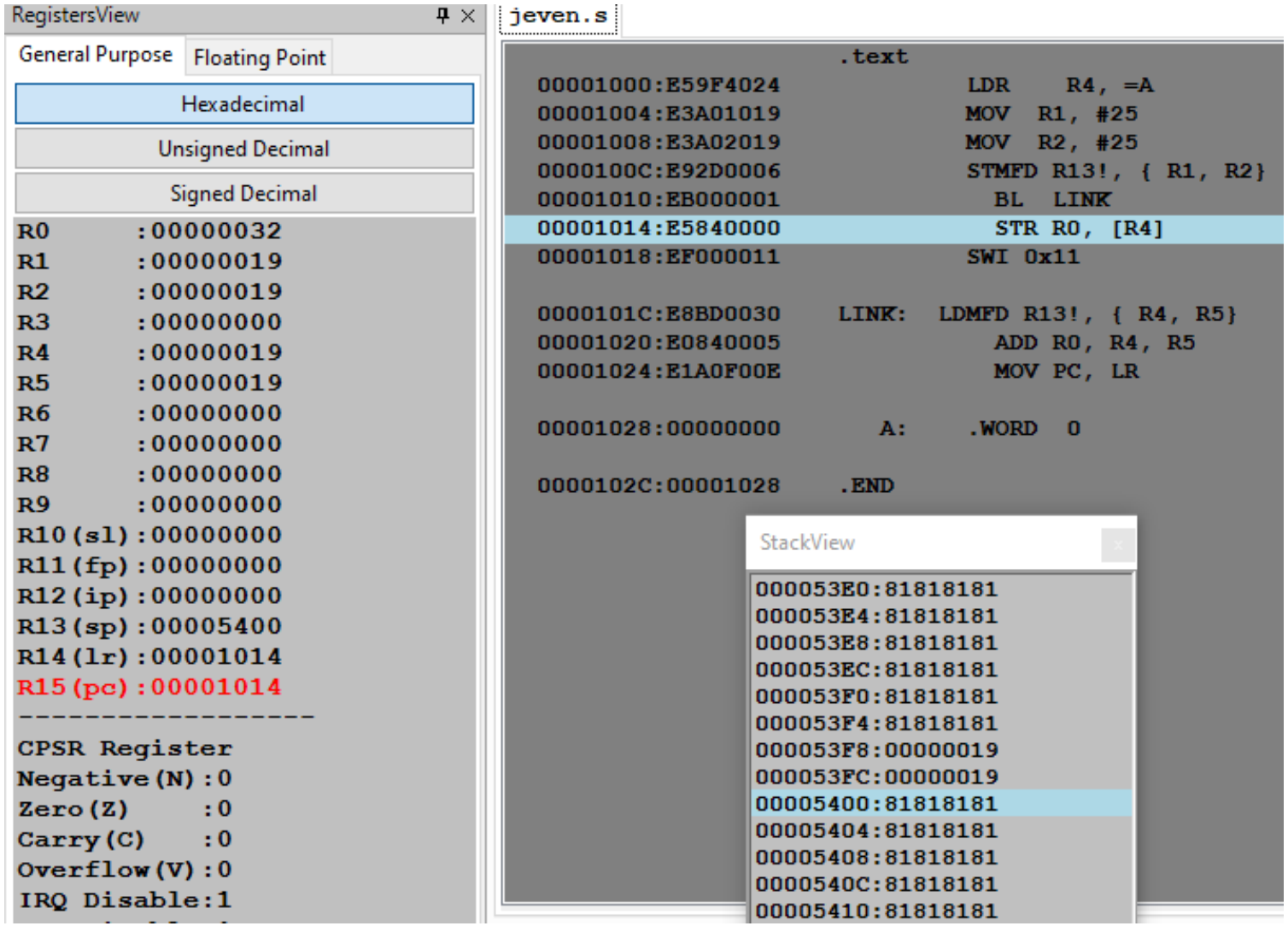

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Nested Procedure Call: MUL(ADD(a,b),c)

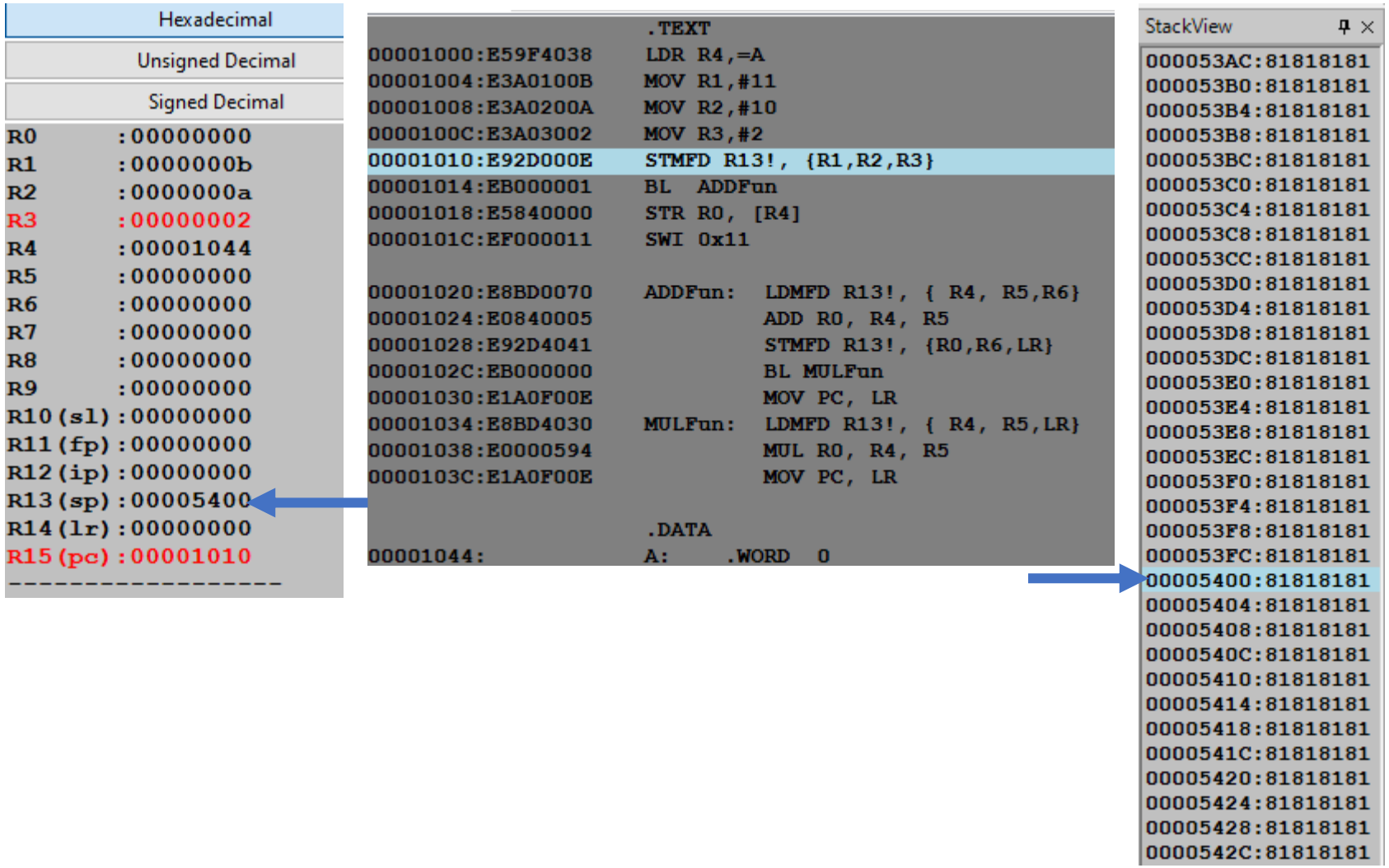
```
.TEXT       ; MAIN Procedure
LDR R4,=A
MOV R1,#11
MOV R2,#10
MOV R3,#2
STMFD R13!, {R1,R2,R3}
BL  ADDFun                          ; Call to ADD Procedure
STR R0, [R4]
SWI 0x11

ADDFun:  LDMFD R13!, { R4, R5,R6}   ; ADD Procedure
        ADD R0, R4, R5
        STMFD R13!, {R0,R6,LR}
        BL MULFun                   ; Call to MUL Procedure
        MOV PC, LR                  ;Return to Main Procedure
MULFun:  LDMFD R13!, { R4, R5,LR}
        MUL R0, R4, R5
        MOV PC, LR                  ; Return to ADD Procedure

.DATA
A:    .WORD  0
```

Microprocessor & Computer Architecture (µpCA)

Nested Procedure Call: MUL(ADD(a,b),c)



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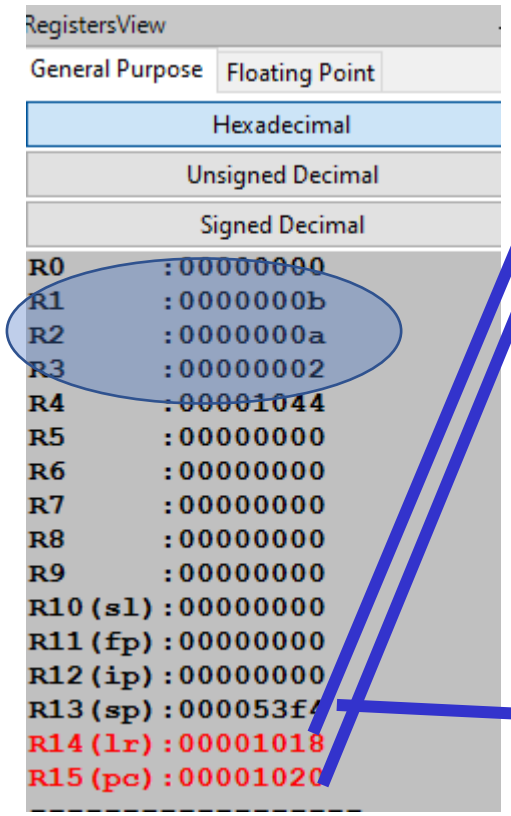
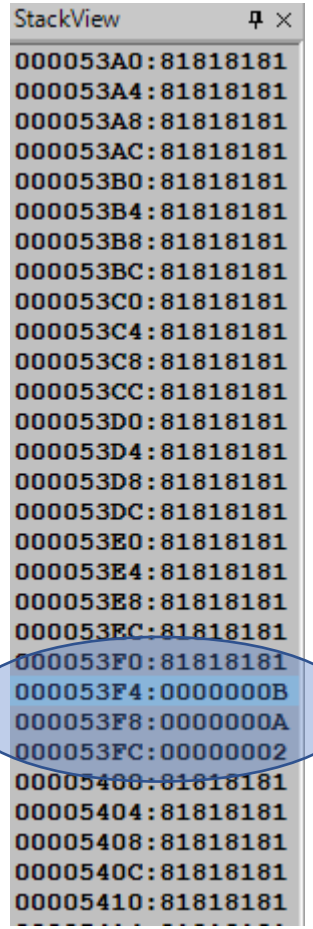
Nested Procedure Call: MUL(ADD(a,b),c)




```
                        .TEXT
00001000:E59F4038       LDR R4,=A
00001004:E3A0100B       MOV R1,#11
00001008:E3A0200A       MOV R2,#10
0000100C:E3A03002       MOV R3,#2
00001010:E92D000E       STMFD R13!, {R1,R2,R3}
00001014:EB000001       BL  ADDProc
00001018:E5840000       STR R0, [R4]
0000101C:EF000011       SWI 0x11
00001020:E8BD0070       ADDProc:  LDMFD R13!, { R4, R5,R6}
00001024:E0840005                 ADD R0, R4, R5
00001028:E92D4041                 STMFD R13!, {R0,R6,LR}
0000102C:EB000000                 BL MULProc
00001030:E1A0F00E                 MOV PC, LR
00001034:E8BD4030       MULProc:  LDMFD R13!, { R4, R5,LR}
00001038:E0000594                 MUL R0, R4, R5
0000103C:E1A0F00E                 MOV PC, LR

                        .DATA
00001044:               A:    .WORD  0
```



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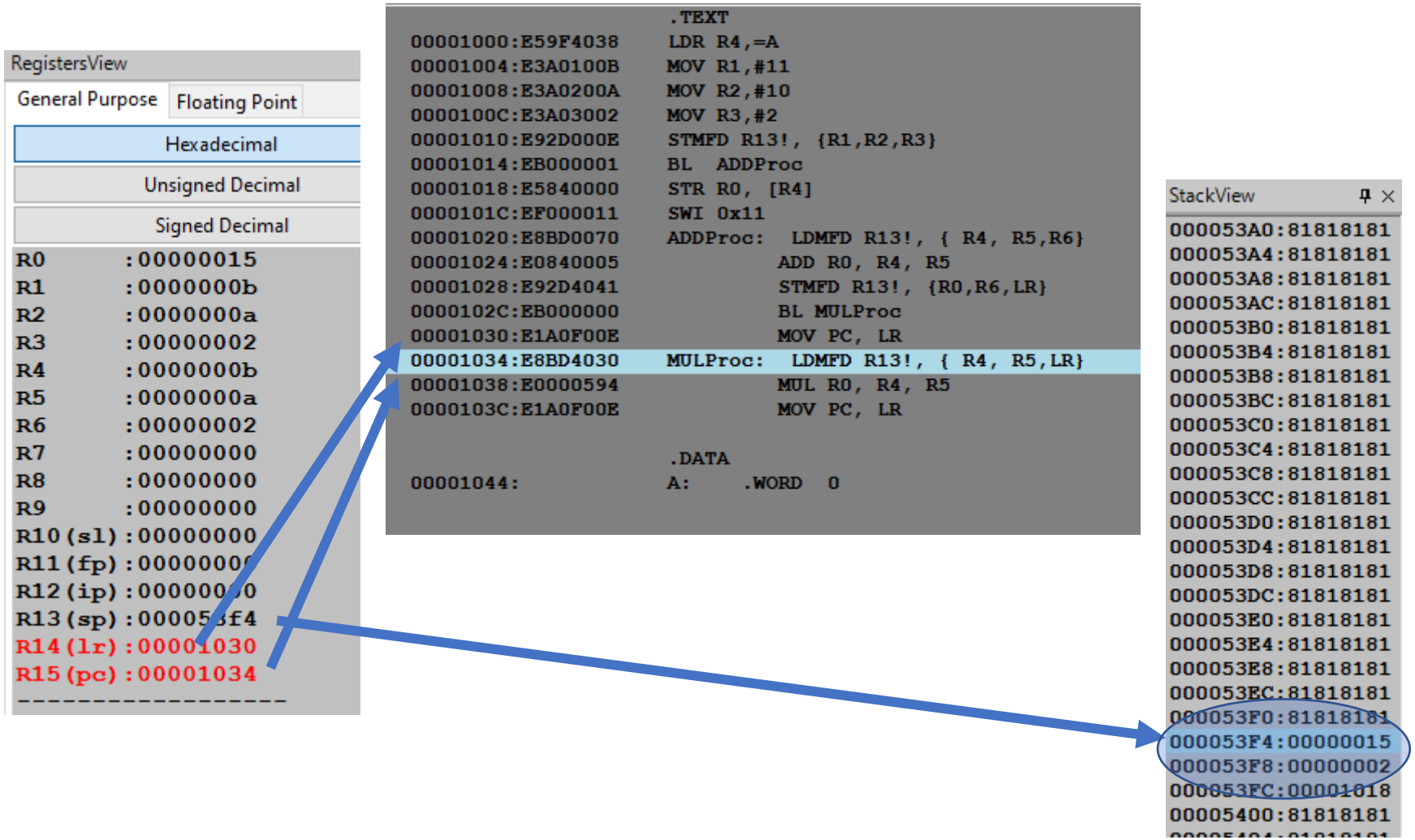
Nested Procedure Call: MUL(ADD(a,b),c)



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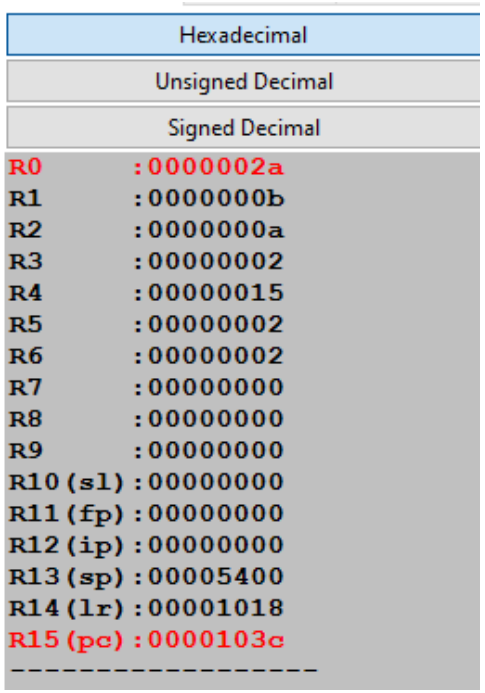


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                        .DATA
00001044:               A:     .WORD  0
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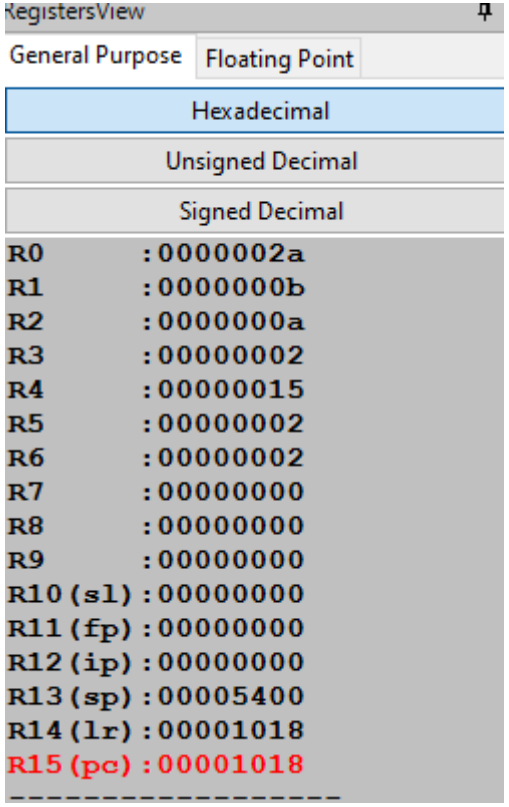
Nested Procedure Call: MUL(ADD(a,b),c)

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                        .DATA
00001044:               A:    .WORD  0
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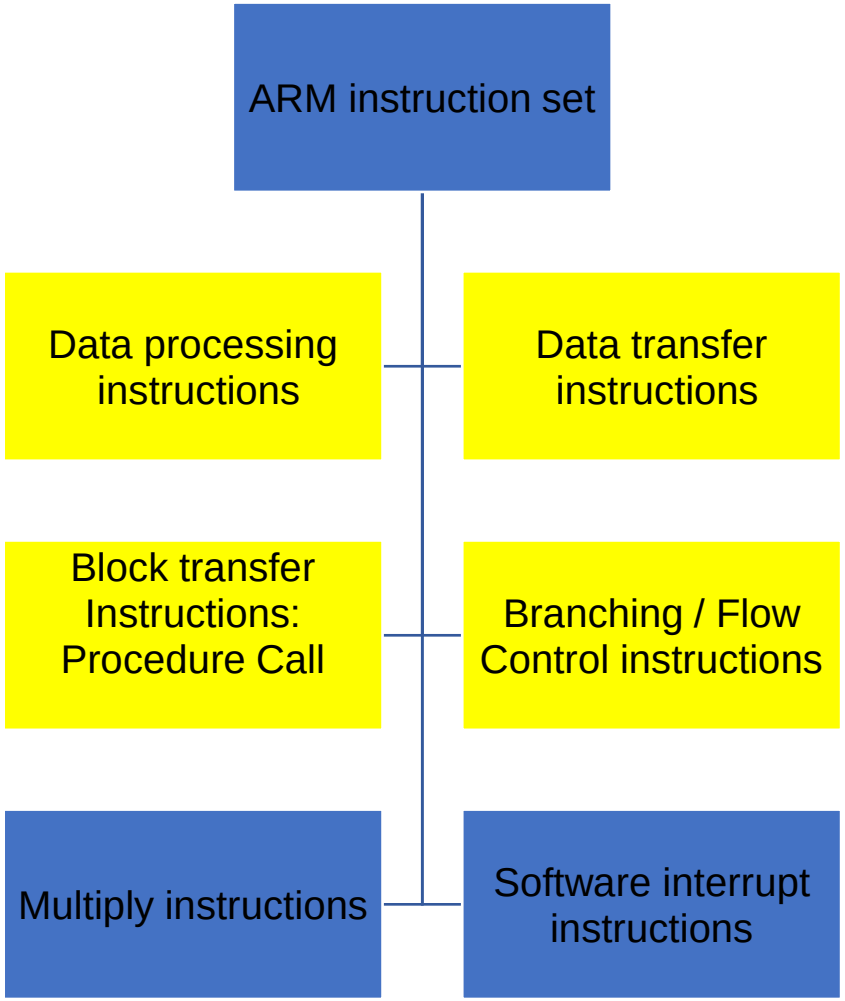
NEXT Session : Multiplication



THANK YOU

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