

**UE19CS252** 

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# **Pipeline Processor: Branch Prediction 2**

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#### **Syllabus**

#### **Unit 1: Basic Processor Architecture and Design**

#### **Unit 2: Pipelined Processor and Design**

- 3-Stage ARM Processor
- 5-Stage Pipeline Processor
- Introduction to Pipeline Processor
- What May Go Wrong?
- Introduction to Hazards, Stalls,
- Structural Hazards
- Data Hazard
- RAW, WAR, WAW Hazards
- Attacking Data Hazard
- Software Approach vs ardware Approach
- Control Hazards
- Branch History Table
- 1 bit Branch Prediction
- 2 bit Branch Prediction





**Text 1:** "Computer Organization and Design", Patterson, Hennessey, 5th Edition, Morgan Kaufmann, 2014.

**Reference 1:** "Computer Architecture: A Quantitative Approach", Hennessey, Patterson, 5th Edition, Morgan Kaufmann, 2011.

#### **Pipelining: Basic and Intermediate Concepts** Appendix C Introduction C-2 The Major Hurdle of Pipelining—Pipeline Hazards C-11 How Is Pipelining Implemented? C-30 **C.4** What Makes Pipelining Hard to Implement? C-43 Extending the MIPS Pipeline to Handle Multicycle Operations C-51 Putting It All Together: The MIPS R4000 Pipeline C-61 Crosscutting Issues C-70 C.8 Fallacies and Pitfalls C-80 Concluding Remarks C-81 **C.10** Historical Perspective and References C-81 Updated Exercises by Diana Franklin C-82

#### **Towards 2-Bit Predictor**

#### What is the problem with 1-Bit predictor



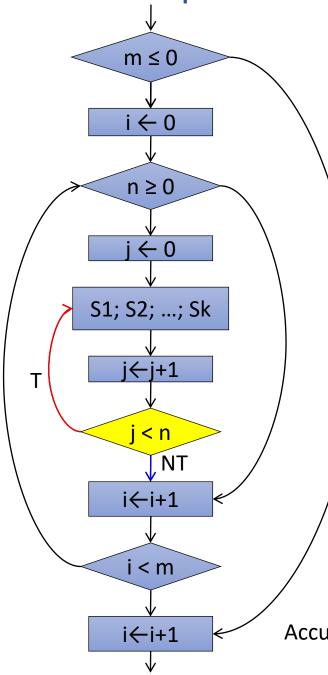
 branches with same lower order bits will reference the same entry, causing mutual prediction

#### Shortcomings with loops

- Always mispredict twice for every loop
  - Mispredict upon exiting a loop, since this is a surprise
  - If we repeat the loop, we'll miss again since we'll predict, branch not taken



#### What is the problem with 1-Bit predictor



		1-bit		
i	j	Pred	Outc	
0	0	NT	Т	
0	1	Т	Т	
0	n	Т	NT	
1	0	NT	Т	
1	1	Т	Т	

If m= 100 & n=10

2 miss prediction per Iteration

 $2 \times m$  miss predictions for iterations

2x100= 200 miss predictions

8 x n Correct Predictions (Single Iteration)

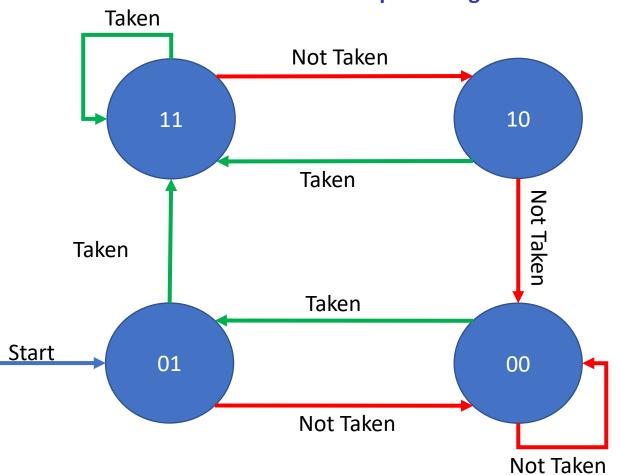
8x10x100= 8000 Correct Predictions for nxm iterations

Accuracy of Correct Predictions = 8000/100= 80%



#### Two -bit Predictor





00: Strong Not Taken

01: Weak Not Taken

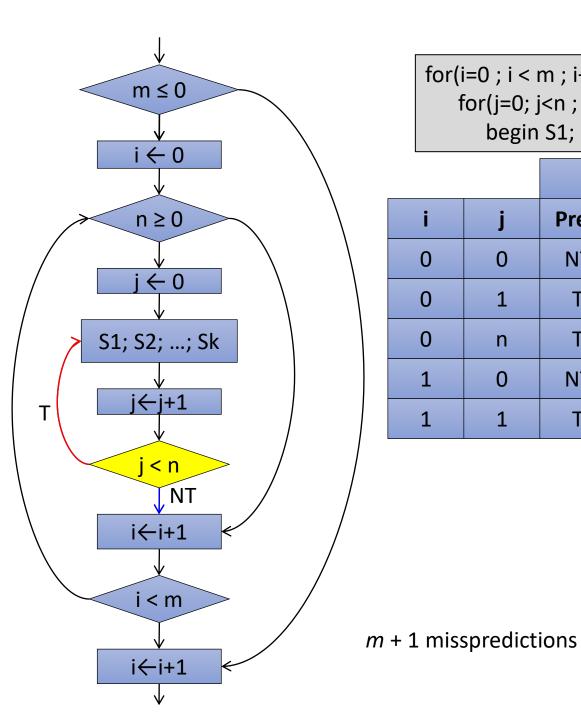
10: Weak Taken

11: Strong Taken



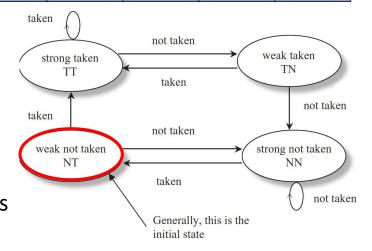
2<sup>nd</sup> bit is conviction bit (How much sure that the prediction is correct)



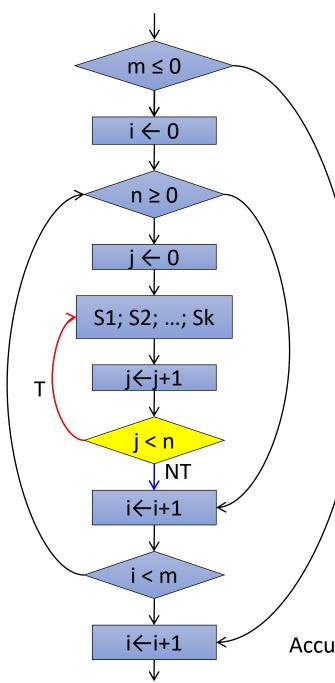


for(i=0; i < m; i++) for(j=0; j<n; j++) begin S1; S2; ...; Sk end;

		1-bit		2-bit		
i	j	Pred	Outc	State	Pred	Outc
0	0	NT	Т	wNT	NT	Т
0	1	Т	Т	sT	Т	Т
0	n	T	NT	sT	T	NT
1	0	NT	T	wT	T	Т
1	1	Т	Т	sT	Т	Т







for(i=0; i < m; i++) for(j=0; j<n; j++) begin S1; S2; ...; Sk end;

m + 1 misspredictions

		1-bit		2-bit		
i	j	Pred	Outc	State	Pred	Outc
0	0	NT	Т	wNT	NT	Т
0	1	Т	Т	sT	Т	Т
0	n	Т	NT	sT	Т	NT
1	0	NT	Т	wT	Т	Т
1	1	Т	Т	sT	Т	Т

If m= 100 & n=10

1 miss prediction per Iteration

1 + *m* miss predictions for iterations

1+100= 101 miss predictions

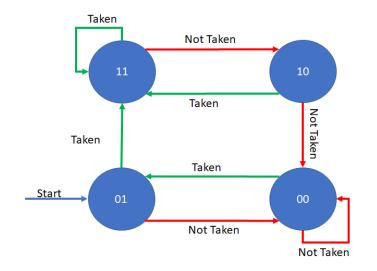
9 x n Correct Predictions (Single Iteration)

9x10x100= 9000 Correct Predictions for nxm iterations

Accuracy of Correct Predictions = 9000/101≈ 90%



Two -bit Predictor: Initial state: 01



00: Strong Not Taken

01: Weak Not Taken

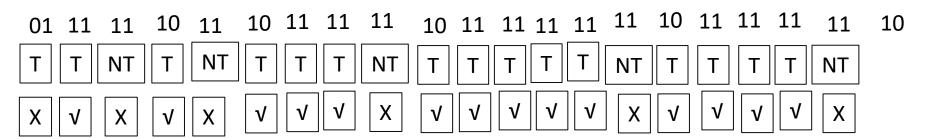
10: Weak Taken

11: Strong Taken

Consider a program with two branch statement with the following behavior

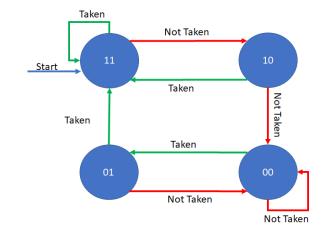
T T NT T NTT T T NT T T T TT NT T T T NT

6 miss predictions





Two -bit Predictor: Initial state: 11



00: Strong Not Taken

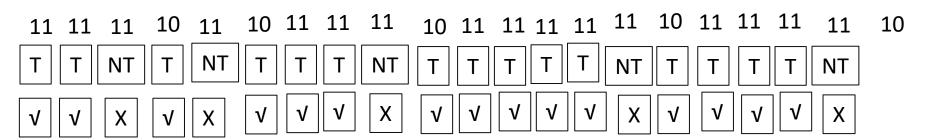
01: Weak Not Taken

10: Weak Taken

11: Strong Taken

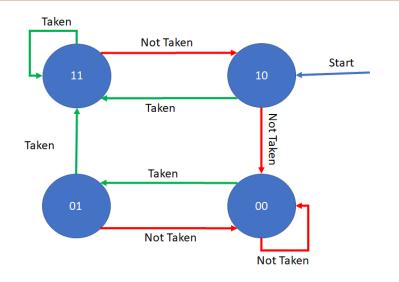
Consider a program with two branch statement with the following behavior

5 miss predictions





Two -bit Predictor: Initial state: 10



00: Strong Not Taken

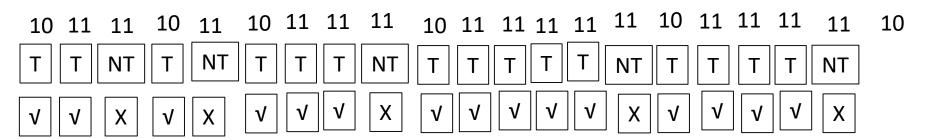
01: Weak Not Taken

10: Weak Taken

11: Strong Taken

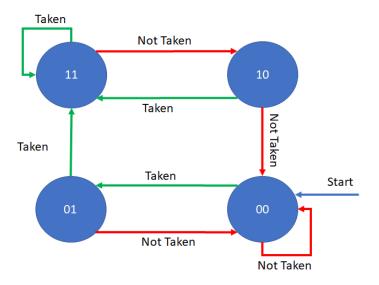
Consider a program with two branch statement with the following behavior

5 miss predictions





Two -bit Predictor: Initial state → 00



00: Strong Not Taken

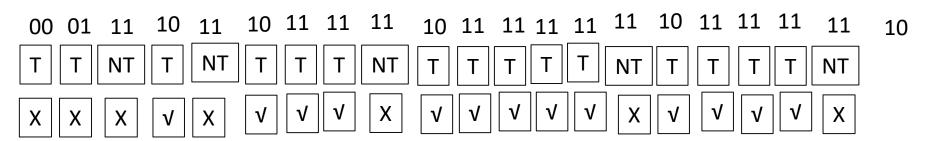
01: Weak Not Taken

10: Weak Taken

11: Strong Taken

Consider a program with two branch statement with the following behavior

7 miss predictions





#### **Think About It**



Which predictor is best suitable if the following is the likely outcome of branch instruction? Suggest the suitable initial state

T NT T NT T NT T NT T TN

NT NT NT NT NT NT NT NT

#### **Next Session**



# **Performance Analysis**



## **THANK YOU**

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