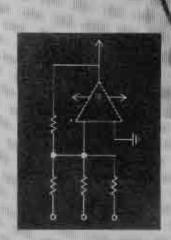
MARCHER">

Cal. No. 276-5011

Engineer's Mini-Notebook

Op Amp



Forrest M. Mims III

PARISO SHAPE, A CHIEDNIC TANCH COMPORATION

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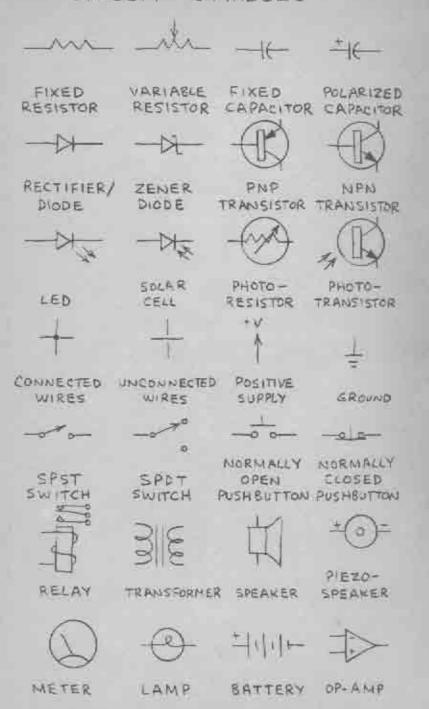
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PRINTED IN U.S.A.

Radio Shack

CIRCUIT SYMBOLS



GINFFR'S BY FORREST M. MIMS, III

FIRST EDITION

A SILICONCEPTS M BOOK

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THIS BOOK INCLUDES STANDARD APPLICATION CIRCUITS AND CIRCUITS DESIGNED BY THE AUTHOR, EACH CIRCUIT WAS ASSEMBLED AND TESTED BY THE AUTHOR AS THE BOOK WAS DEVELOPED. AFTER THE BOOK WAS COMPLETED. THE AUTHOR REASSEMBLED EACH CIRCUIT TO CHECK FOR ERRORS. WHILE REASONABLE CARE WAS EXERCISED IN THE PREPARATION OF THIS BOOK, VARIATIONS IN COMPONENT TOLERANCES AND CONSTRUCTION METHODS MAY CAUSE THE RESULTS YOU DETAIN TO DIPFER FROM THOSE GIVEN HERE, THEREFORE THE AUTHOR AND RADIO SHACK ASSUME NO RESPONSIBILITY FOR THE SUITABILITY OF THIS BOOK'S CONTENTS FOR ANY APPLICATION. SINCE WE HAVE NO CONTROL OVER THE USE TO WHICH THE INFORMATION IN THIS BOOK IS PUT, WE ASSUME NO LIABILITY FOR ANY DAMAGES RESULTING FROM ITS USE. OF COURSE IT IS YOUR RESPONSIBILITY TO DETERMINE IF COMMERCIAL USE, SALE OR MANUFACTURE OF ANY DEVICE THAT INCORPORATES INFOR-MATION IN THIS BOOK INFRINGES ANY PATENTS, COPYRIGHTS OR OTHER RIGHTS.

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HISTORICAL NOTE

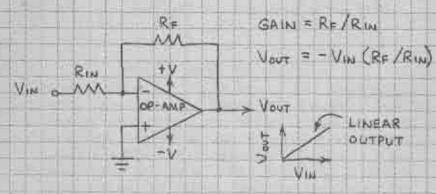
THE OPERATIONAL AMPLIFIER WAS DEVELOPED FOR USE IN ANALOG COMPUTERS IN THE 1940S. EARLY OP-AMPS USED VACUUM TURES AND WERE LARSE IN SIZE AND CONSUMED CONSIDERABLE POWER. IN 1947 FAIRCHILD SEMICONDUCTOR INTRODUCED THE FIRST INTEGRATED CIRCUIT OP-AMP. TODAY'S IC OP-AMPS ARE FAR SUPERIOR TO THEIR VACUUM TURE PREDECESSORS. AND THEY ARE MUCH SMALLER AND CAN BE PURCHASED FOR AS LITTLE AS A DOLLAR OR TWO.

INTRODUCTION

THE OPERATIONAL AMPLIFIER OR OP-AMP IS A HIGH PERFORMANCE LINEAR AMPLIFIER WITH AN AMAZING VARIETY OF USES. THE OP-AMP HAS TWO IMPUTS, INVERTING (+) AND ONE OUTPUT. THE POLARITY OF A SIGNAL APPLIED TO THE INVERTING IMPUT IS REVERSED AT THE OUTPUT. A SIGNAL APPLIED TO THE NON-INVERTING IMPUT RETAINS ITS POLARITY AT THE OUTPUT.

THE GAIN (DEGREE OF AMPLIFICATION) OF AN OP-AMP IS DETERMINED BY A FEEDBACK RESISTOR THAT FEEDS SOME OF THE AMPLIFIED SIGNAL FROM THE OUTPUT TO THE INVERTING INPUT. THIS REDUCES THE AMPLITUDE OF THE OUTPUT SIGNAL, HENCE THE GAIN. THE SMALLER THE RESISTOR, THE LOWER THE GAIN.

HERE IS A BASIC INVERTING AMPLIFIER MADE WITH AN OP-AMP:



THE GAIN IS INDEPENDENT OF THE SUPPLY VOLTAGE, NOTE THAT THE UNUSED INPUT IS GROUNDED. THEREFORE THE OP-AMP AMPLIFIES THE DIFFERENCE BETWEEN THE INPUT (VIN) AND GROUND (O VOLTS). THE OP-AMP IS THEN A DIFFERENTIAL AMPLIFIER.

THE FEEDBACK RESISTOR (RA) AND AN OP-AMP FORM A CLOSED FEEDBACK LOOP. WHEN RA IS OMITTED, THE OP-AMP IS SAID TO BE IN ITS OPEN LOOP MODE. THE OP-AMP THEN EXHIBITS MAXIMUM GAIN, BUT ITS OUTPUT THEN SWINGS FROM FULL ON TO FULL OFF OR VICE VERSA FOR VERY SMALL CHANGES IN INFUT VOLTAGE. THEREFORE THE OPEN LOOP MODE IS NOT PRACTICAL FOR LINEAR AMPLIFICATION. INSTEAD THIS MODE IS USED TO INDICATE WHEN THE VOLTAGE AT ONE INPUT DIFFERS FROM THAT AT THE OTHER. IN THIS MODE THE OP-AMP IS CALLED A COMPARATOR SINCE IT COMPARES ONE INPUT VOLTAGE WITH THE OTHER.

POWERING OP-AMPS

MOST OP-AMPS AND CP-AMP CIRCUITS REQUIRE A DUAL POLARITY POWER SUPPLY. HERE IS A SIMPLE DUAL POLARITY SUPPLY MADE FROM TWO 9-VOLT BATTERIES:

IMPORTANT: THE LEADS FROM THE SUPPLY TO THE DP-AMP SHOULD BE SHORT AND DIRECT. IF THEY EXCEED ABOUT & INCHES, THE OP-AMP'S SUPPLY PINS MUST BE BYPASSED BY CONNECTING A O.1 MF CAPACITOR BETWEEN EACH POWER SUPPLY PIN AND GROUND. OTHERWISE THE OP-AMP MAY OSCILLATE OR FAIL TO OPERATE PROPERLY, ALWAYS USE FRESH BATTERIES. BOTH MUST SUPPLY THE SAME VOLTAGE. BE SURE THE BATTERY CLIPS ARE CLEAN AND TIGHT. NEVER APPLY AN INPUT SIGNAL WHEN THE POWER SUPPLY IS SWITCHED OFF.

OP-AMPS ARE CHARACTERIZED BY DOZENS OF SPECIFICATIONS, SOME OF WHICH ARE GIVEN ON THE FOLLOWING PAGES, THOSE WHOSE MEANING IS NOT DEVICUS ARES

INPUT OFFSET VOLTAGE - EVEN WITH NO INPUT VOLTAGE AN OP-AMP SIVES A VERY SMALL OUTPUT VOLTAGE. THE OFFSET VOLTAGE IS THAT WHICH, WHEN APPLIED TO ONE INPUT, CAUSES THE OUTPUT TO BE AT O VOLTS.

COMMON MODE REJECTION RATIO - THIS IS A MEASURE OF THE ABILITY OF AN OP-AMP TO REJECT A SIGNAL SIMULTANEOUSLY APPLIED TO BOTH INPUTS.

BANDWIDTH - THE FREQUENCY RANGE OVER WHICH AN OP-AMP WILL FUNCTION. THE FREQUENCY AT WHICH THE GAIN FALLS TO I IS THE UNITY GAIN FREQUENCY.

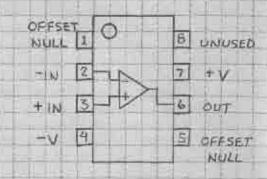
SLEW RATE - THE RATE OF CHANGE IN THE OUT PUT OF AN OP-AMP IN VOLTS PER MICROSECOND WHEN THE GAIN IS L.

CIRCUIT ASSEMBLY TIPS

YOU CAN USUALLY SUBSTITUTE DIFFERENT OP-AMPS IN A CIRCUIT. FOR EXAMPLE, USE A 1458 DUAL OP-AMP IN A CIRCUIT THAT REQUIRES TWO 741 OP-AMPS. BE SURE TO KEEP TRACK OF PIN DIFFERENCES. FOR VERY HIGH INPUT RESISTANCE AND LOW OPERATING CURRENT, USE CMOS OP-AMPS. USE A HIGH-IMPEDANCE VOLTMETER TO MONITOR THE OUTPUT OF AN OP-AMP THAT IS AMPLIFYING A d.C. VOLTAGE. IF A CIRCUIT FAILS TO WORK, REMOVE INPUT SIGNAL FIRST. THEN DISCONNECT POWER AND CHECK THE WIRING, USE FRESH BATTERIES.

741 OP-AMP

THE 741 IS A
HIGHLY POPULAR
GENERAL PURPOSE
OP-AMP. IT IS
SIMPLE TO USE,
RELIABLE, AND
INEXPENSIVE.
IT IS USED IN
MOST CIRCUITS
IN THIS BOOK.



MAXIMUM RATINGS

SUPPLY VOLTAGE #18 V
POWER DISSIPATION 500 MW
DIFFERENTIAL INPUT VOLTAGE #30 V
INPUT VOLTAGE (NOTE 1) #15 V
OUTPUT SHORT CIRCUIT TIME INDEFINITE
OPERATING TEMPERATURE 0°C TD 70°C

NOTE 1: INPUT VOLTAGE SHOULD NOT EXCEED SUPPLY VOLTAGE WHEN SUPPLY VOLTAGE IS LESS THAN #15 VOLTS.

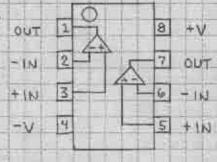
CHARACTERISTICS (NOTE 2)

INPUT OFFSET VOLTAGE 2 TO 6 MV
INPUT RESISTANCE 3 TO 2 M IL
VOLTAGE GAIN 20,000 TO 200,000
COMMON-MODE REJECTION RATIO 70 TO 40 dB
BANDWIDTH 5 TO 1.5 MHz
SLEW RATE 5 V / MSEC
SUPPLY CURRENT 1.7 TO 2.8 MA
POWER CONSUMPTION 50 TO 85 MW

NOTE 2: VALUES SHOWN ARE TYPICAL OR MINIMUM TO TYPICAL.

1458 DUAL OP-AMP

THE 1458 INCLUDES
TWO INDEPENDENT, OUT 1
GENERAL PURPOSE
OP-AMPS IN A -IN 2
SINGLE PACKAGE,
THE AMPLIFIERS +IN 3
SHARE COMMON
POWER SUPPLY PINS. -V 4
USE TO REPLACE
TWO 741 OP-AMPS.



MAXIMUM RATINGS

SUPPLY VOLTAGE
POWER DISSIPATION
DIFFERENTIAL INPUT VOLTAGE
INPUT VOLTAGE (NOTE 1)
OUTPUT SHORT CIRCUIT TIME
OPERATING TEMPERATURE

18 V 400 MW # 30 V # 15 V INDEFINITE 0°C TO 70°C

NOTE 1: INPUT VOLTAGE SHOULD NOT EXCEED SUPPLY VOLTAGE IS LESS THAN \$15 V.

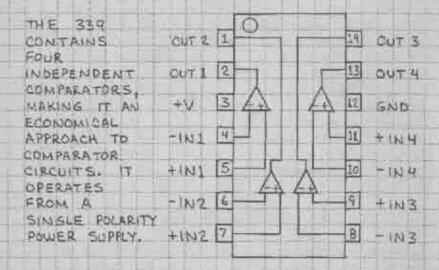
CHARACTERISTICS (NOTE 2)

INPUT OFFSET VOLTAGE 1 TO 6 MV INPUT RESISTANCE 3 TO 1 M.IL VOLTAGE GAIN 20,000 TO 140,000 COMMON-MODE REJECTION RATIO 70 TO 90 &B SUPPLY CURRENT (NOTE 3) 3 TO 5.6 MA POWER CONSUMPTION B5 MW

NOTE 2: VALUES SHOWN ARE TYPICAL OR MINIMUM TO TYPICAL.

NOTE 3 : BOTH AMPLIFIERS.

339 QUAD COMPARATOR



MAXIMUM RATINGS

SUPPLY VOLTAGE #36V OR ±18V
POWER DISSIPATION S70 WW
DIFFERENTIAL INPUT VOLTAGE 36 V
INPUT VOLTAGE +3V TO +36V
OUTPUT SHORT CIRCUIT (NOTE 1) CONTINUOUS
OPERATING TEMPERATURE 0°C TO 70°C

NOTE 1: OK TO SHORT DUTPUT TO GROUND, DO NOT SHORT DUTPUT TO +V SINCE CHIP WILL OVERHEAT.

CHARACTERISTICS (NOTE 2)

INPUT OFFSET VOLTAGE #3 TO #20 MV
VOLTAGE GAIN 2,000 TO 30,000
SUPPLY CURRENT 8 TO 2 MA
OUTPUT SINK CURRENT 6 TO 16 MA

NOTE 2; VALUES SHOWN ARE MINIMUM TO TYPICAL,

386 AUDIO AMPLIFIER

SIMPLE TO USE +GAIN 1 0 8 GAIN AUDIO AMPLIFIER WITH SAIN OF - N 2-BYPASS 20. OPERATES FROM SINGLE +4 POLARITY SUPPLY. + IN CONNECT TOME GND 4 COT CAPACITOR BETWEEN PINS 1 AND 8 FOR GAIN OF 200.

MAXIMUM RATINGS

SUPPLY VOLTAGE
POWER DISSIPATION
INPUT VOLTAGE
OPERATING TEMPERATURE

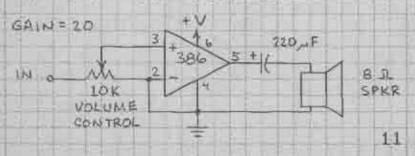
+15 V 660 mW ±0.4 V 0°C TO 70°C

CHARACTERISTICS

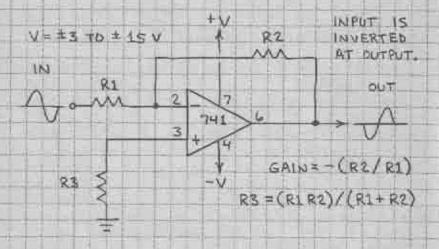
SUPPLY VOLTAGE RANGE
STANDBY CURRENT
OUTPUT POWER
VOLTAGE GAIN
BANDWIDTH
TOTAL HARMONIC DISTORTION
INPUT RESISTANCE

+4 TO + 12 V 4 TO 8 mA 250 TO 325 mW 20 TO 200 300 KHZ 0.2 % 50 K IL

TYPICAL APPLICATION



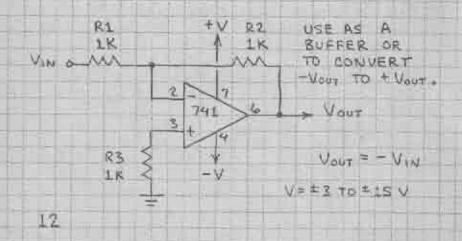
BASIC INVERTING AMPLIFIER



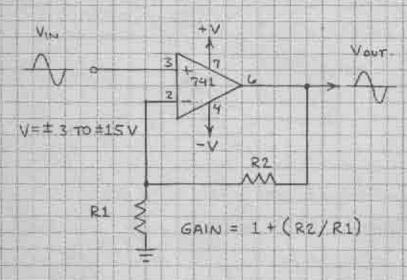
EXAMPLE: F RI = 1000 OHMS AND R2 = 10,000 OHMS, THEN GAIN IS -(10,000/1000) OR -10.

THIS IS ONE OF THE MOST COMMON OP-AMP CIRCUITS. FOR A NON-INVERTED OUTPUT USE THE AMPLIFIER ON THE FACING PAGE,

UNITY-GAIN INVERTER



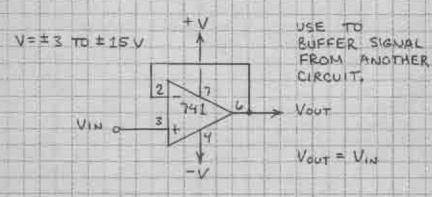
NON-INVERTING AMPLIFIER



EXAMPLE: IF RI = 1,000 OHMS AND R2 = 10,000 OHMS, THEN GAIN IS 1+ (10,000 / 1,000) OR 11.

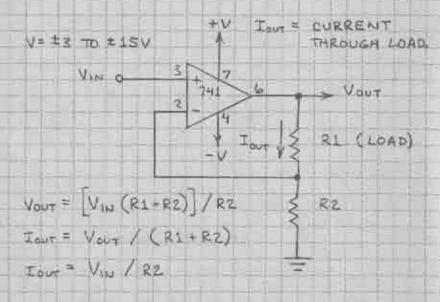
NOTE THAT VOUT IS AN AMPLIFIED BUT

UNITY-GAIN FOLLOWER

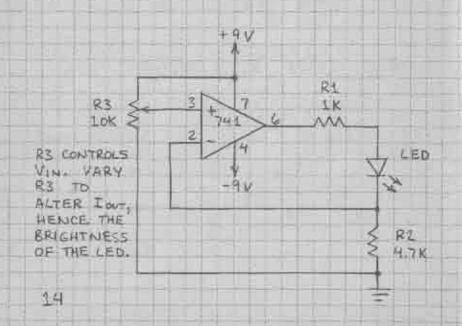


13

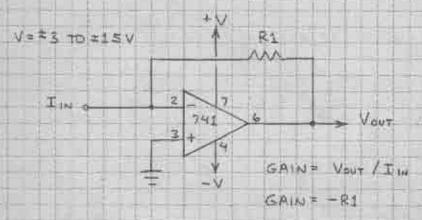
TRANSCONDUCTANCE AMPLIFIER



THIS CIRCUIT IS A VOCTAGE TO CURRENT CONVERTER. HERE'S HOW IT PERMITS AN INPUT VOLTAGE TO CONTROL THE BRIGHTNESS OF AN LED!

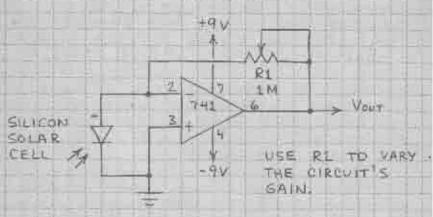


TRANSIMPEDANCE AMPLIFIER



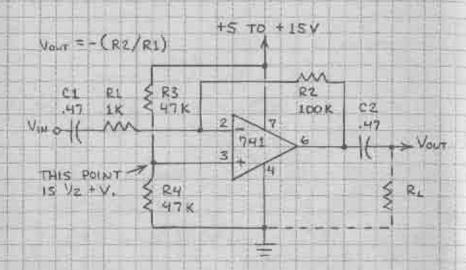
EXAMPLE: IF RI = 1,000 DHMS THEN GAIN =

THIS CIRCUIT IS A CURRENT-TO-VOLTAGE
CONVERTER. HERE'S HOW IT TRANSFORMS
THE CURRENT GENERATED BY A SOLAR CELL
INTO AN OUTPUT VOLTAGE:



THIS CIRCUIT CAN AMPLIFY THE SIGNAL FROM NON-CURRENT GENERATORS LIKE THERMISTORS AND PHOTORESISTORS, CONNECT ONE SIDE OF DEVICE TO +9 V AND THE OTHER TO PIN 2. GROUND PIN 3.

SINGLE - SUPPLY AMPLIFIER



THIS IS AN INVERTING AMPLIFIER DESIGNED TO OPERATE FROM A SINGLE-POLARITY SUPPLY. WITH THE VALUES FOR RI AND RZ GIVEN ABOVE, THE GAIN IS 100. CAPACITORS C1 AND CZ MUST BE USED. THEREFORE THIS GIRCUIT WILL AMPLIFY A FLUCTUATING AC SIGNAL BUT NOT A DC SIGNAL.

CI SHOULD BE APPROXIMATELY 1/(2TT flow RI).

(FLOW IS THE LOW FREQUENCY CUTOFF OR 300 Hz

FOR THE CIRCUIT ABOVE.) CZ SHOULD BE

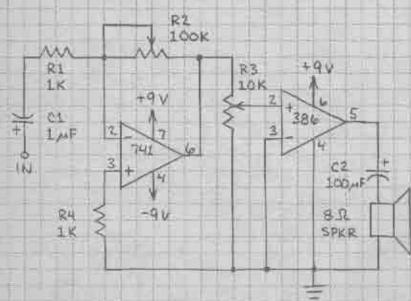
APPROXIMATELY 1/(2TT Flow RI). (RI IS

THE LOAD RESISTANCE.)

THE OUTPUT FROM A DUAL-SUPPLY OF-AME CAN FLUCTUATE ABOVE AND BELOW GROUND (O VOCTS). HERE THE DIVIDER FORMED BY R3 AND R4 SETS VOOT AT 1/2 +V. THE OUTPUT THEN FLUCTUATES ABOVE AND BELOW 1/2 +V LIKE THIS:

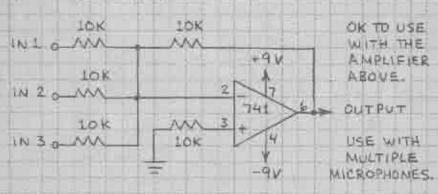
1/2 V - NAME OUTPUT SIGNAL

AUDIO AMPLIFIER

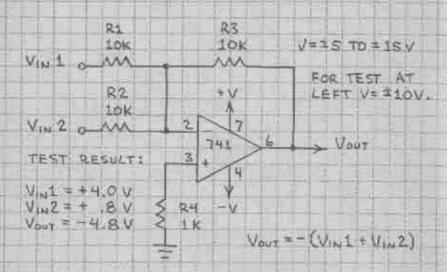


THE 741 IS A PREAMPLIFIER. R2 CONTROLS
ITS GAIN. THE 386 IS A POWER AMPLIFIER.
R3 CONTROLS THE VOLUME OF THE SPEAKER.
OK TO USE FIXED LOCK RESISTOR FOR R2.
(REDUCE RESISTANCE OF R2 IF CIRCUIT OSCILLATES
OR GIVES DISTORTED OUTPUT.) IMPORTANT: 8Y PASS
THE POWER SUPPLY CONNECTIONS WITH O.1 AF
CAPACITORS.

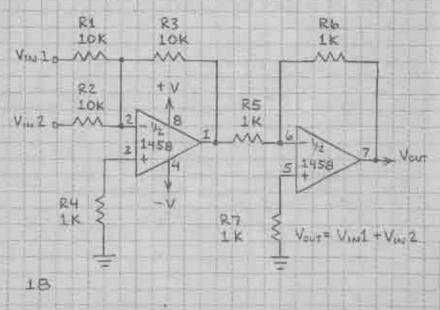
AUDIO MIXER



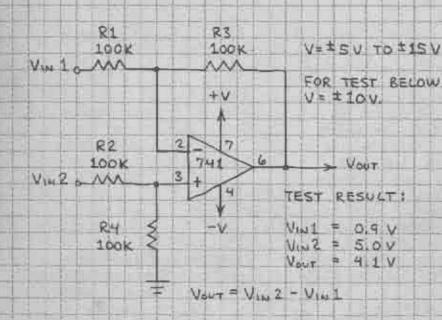
SUMMING AMPLIFIER



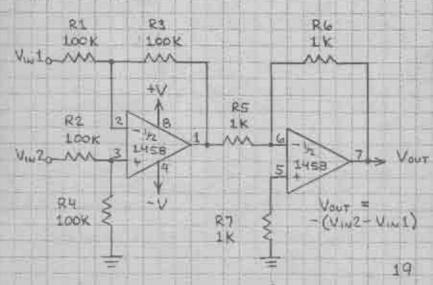
THE CUTPUT OF THE SUMMING AMPLIFIER IS THE SUM OF THE INPUT VOLTAGES. THE SUM OF THE INPUTS SHOULD NOT EXCEED TO LESS A VOLT OR TWO. OK TO ADD MORE INPUTS. (USE LOK RESISTOR TO PIN 2 FOR EACH INPUT.) THE CIRCUIT BELOW PRESERVES THE POLARITY OF VIN:



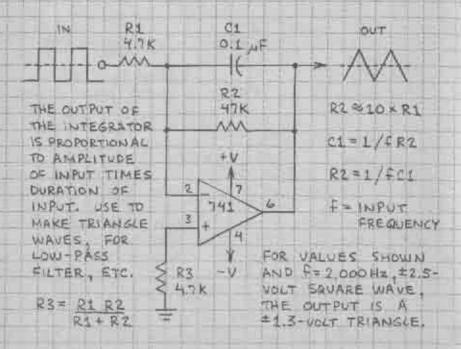
DIFFERENCE AMPLIFIER



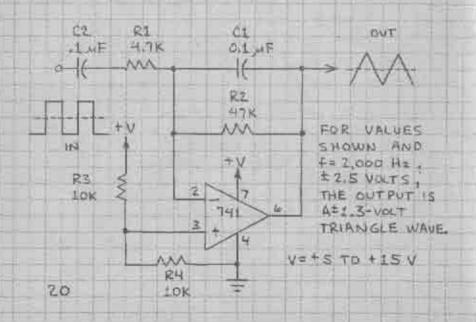
THE DUTPUT OF THE DIFFERENCE AMPLIFIER IS VIN 2 - VIN 1. THE INPUT VOLTAGES SHOULD NOT EXCEED \$ V. THE CIRCUIT BELOW REVERSES THE POLARITY OF VIN 2 - VIN 1:



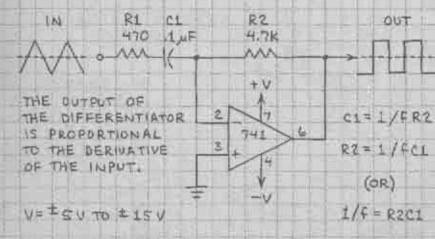
DUAL-SUPPLY INTEGRATOR



SINGLE-SUPPLY INTEGRATOR



DUAL-SUPPLY DIFFERENTIATOR



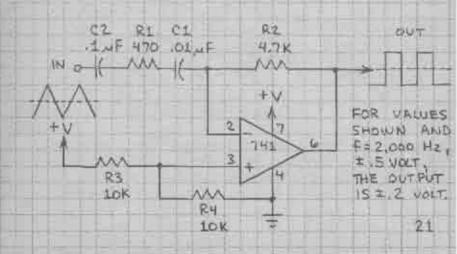
FOR VALUES SHOWN AND \$= 2,000 Hz, \$2.5 - VOLT TRIANGLE WAVE, THE OUTPUT IS A \$ 10 - VOLT SQUARE WAVE.

THE DIFFERENTIATOR WILL TRANSFORM A SQUARE WAVE INTO PULSES!

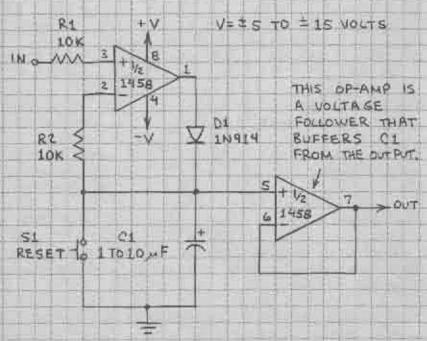
F= 2,000 Hz , V= ±10 V



SINGLE-SUPPLY DIFFERENTIATOR



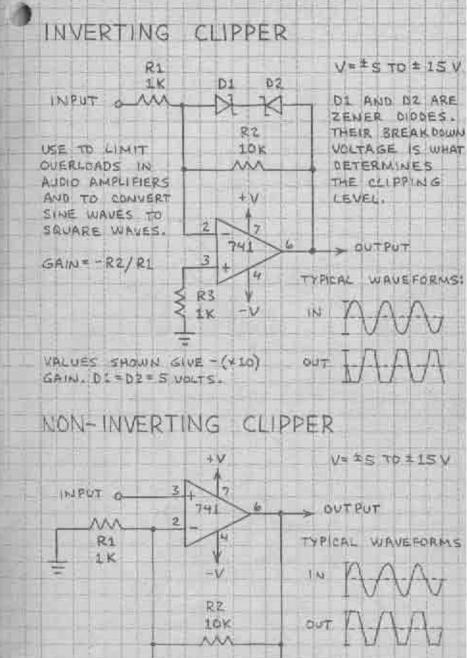
PEAK DETECTOR



THIS CIRCUIT FOLLOWS AN INCOMING VOLTAGE SIGNAL AND STORES THE MAXIMUM VOLTAGE IN C1. PRESS S1 TO DISCHARGE C1 AND RESET CIRCUIT. CONNECT A VOLTMETER FROM CUTPUT TO GROUND TO MEASURE THE PEAK VOLTAGE STORED IN C1. THE CIRCUIT FUNCTIONS LIKE THIS:



NOTE HOW THE SUTPUT FOLLOWS THE PRECEDING HIGH (PEAK) INPUT. ALSO NOTE THAT THE CHARGE ON CI WILL GRADUALLY LEAK AWAY, CL IN THE TEST CIRCUIT FELL 10 MILLIVOLTS / SECOND.



D1 D2

GAIN = 1 + R2/R1

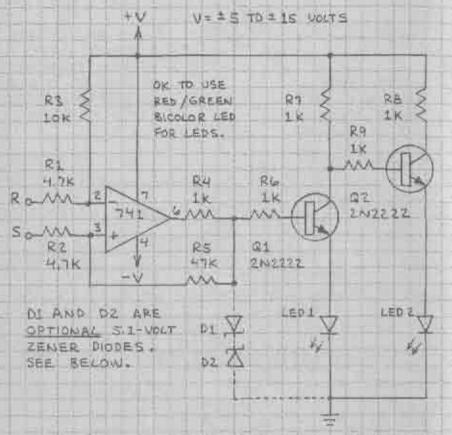
VALUES SHOWN

GIVE YIL GAIN.

23

D1 = D2 = 5 V

BISTABLE RS FLIP-FLOP



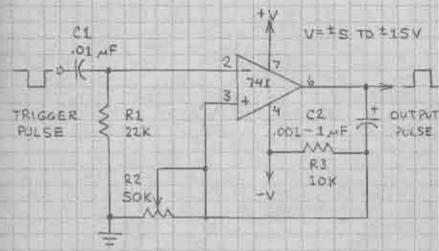
THIS CIRCUIT DEMONSTRATES HOW AN ANALOG CHIP CAN PERFORM A DIGITAL LOGIC FUNCTION. (THE COMPARATOR IS ANOTHER EXAMPLE.)
HERE IS THE TRUTH TABLE:

INPUT		LED		L
R	5	1	2	
GND	+V	ON	OFF	1
GND	-V	OFF	ON	E
+V	GND	OFF	ON	
+V	SND	ON	OFF	

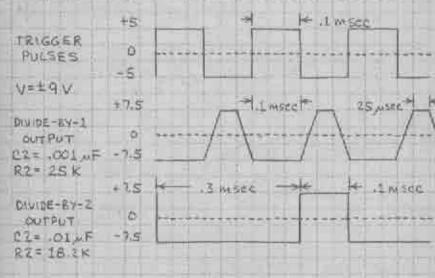
THESE OUTPUTS
HAVE MEMORY
AND HOLD THEIR
STATE EVEN WHEN
SIMPUT FLOATS.

LIMIT DUTPUT LEVEL.

MONOSTABLE MULTIVIBRATOR



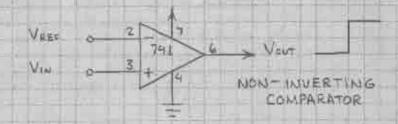
A NEGATIVE TRIGGER PULSE CAUSES THE OP-AMP OUTPUT TO SWING FROM LOW TO HIGH FOR A TIME APPROXIMATELY EQUAL TO R2 x C2. USE TO DIVIDE AN INCOMING SIGNAL AND TO CONVERT AN IRREGULAR NPUT PULSE TO A UNIFORM OUTPUT PULSE, TYPICAL RESULTS:



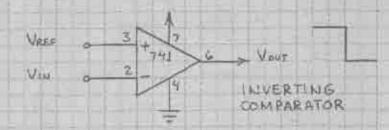
NOTE: USE THE SSS FOR MORE VERSATILITY.

BASIC COMPARATOR

A COMPARATOR IS AN ANALOG CIRCUIT
THAT MONITORS TWO INPUT VOLTAGES,
ONE VOLTAGE IS CALLED THE REFERENCE
VOLTAGE (VREF) AND THE OTHER IS CALLED
THE INPUT VOLTAGE (VIN). WHEN VIN
RISES ABOVE OR FAULS BELOW VREF, THE
OUTPUT OF THE COMPARATOR CHANGES
STATES. SOME CIRCUITS (LIKE THE 339)
ARE DESIGNED SPECIFICALLY AS
COMPARATORS. DUE TO ITS VERY HIGH
OPEN-LOOP GAIN, AN OP-AMP WITHOUT A
FEEDBACK RESISTOR CAN FUNCTION AS
A COMPARATOR.

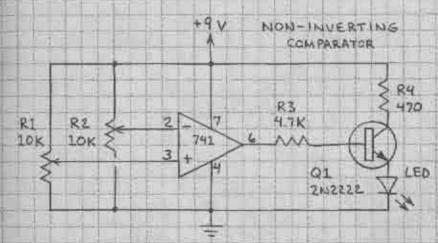


WHEN VIN EXCEEDS VREE OUTPUT SWITCHES FROM LOW TO MIGH.



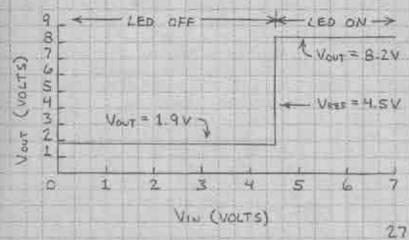
WHEN VER EXCEEDS VW , OUTPUT SWITCHES FROM HIGH TO LOW.

BASIC COMPARATOR (CONT.)

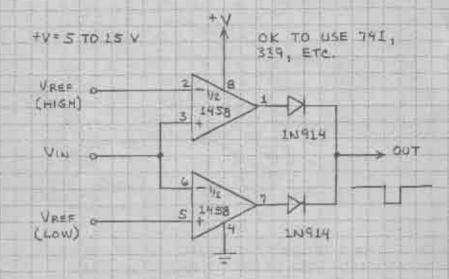


BUILD THIS SIMPLE CIRCUIT ON A PLASTIC BREADBOARD TO LEARN BASICS OF THE COMPARATOR. RI AND RZ FUNCTION AS VOLTAGE DIVIDERS THAT SUPPLY A RANGE OF VOLTAGES TO BOTH 741 INPUTS. QI SWITCHES CURRENT TO THE LED WHEN THE DUTPUT OF THE 741 GGES HIGH. THE CIRCUIT WORKS LIKE THIS:

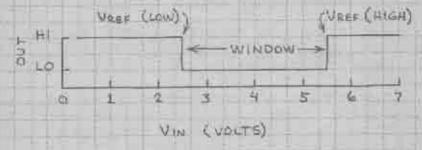
ASSUME R2 IS SET TO ITS CENTER POSITION TO GIVE VREF = 4.5 VOLTS (9V/2 = 4.5 V).
R1 THEN CONTROLS VIN.



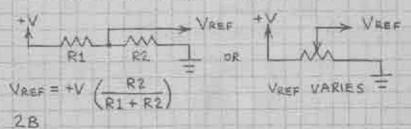
BASIC WINDOW COMPARATOR



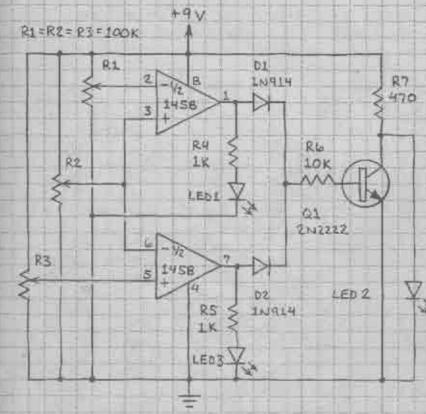
THIS IS AMONG THE MOST VERSATILE OF COMPARATOR CIRCUITS. ASSUME VREF (HIGH) IS 5.5 VOLTS AND VREF (LOW) IS 2.5 VOLTS. CIRCUIT THEN OPERATES LIKE THIS:



ONE OR BOTH REFERENCE VOLTAGES CAN BE



WINDOW COMPARATOR (CONT.)

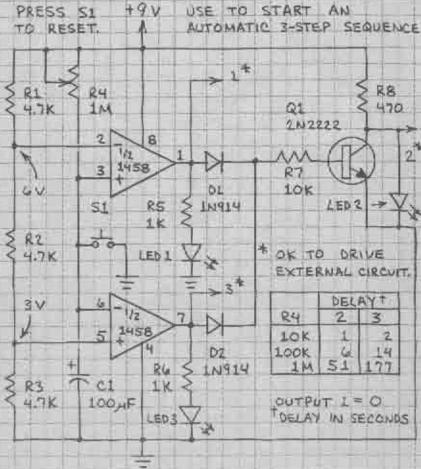


BUILD THIS CIRCUIT ON A BREADBOARD TO LEARN BASICS OF THE WINDOW COMPARATOR. USE VOLTMETER TO SET VREF HIGH (RI) AND VREF LOW (R3). (CONNECT PROBES ACROSS PIN 2 OF 145B AND GROUND; ADJUST R1. REPEAT FOR PIN 5 AND GROUND; ADJUST R3.) ADJUST R2 TO VARY VIN.

VIN AT OR ABOVE VREE HIGH : LED 1 ON VIN WITHIN WINDOW: LED 2 ON VIN AT OR BELOW VREE LOW! LED 3 ON

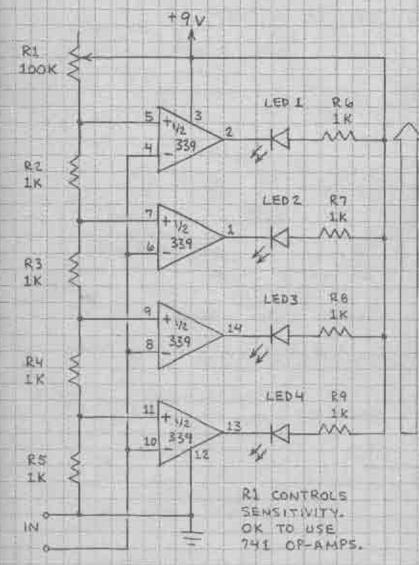
WHEN VIN IS BELOW 0.6 VOLT, BOTH LED L.

3-STEP SEQUENCER



THIS IS A WINDOW COMPARATOR THAT
SUPPLIES A 3-STEP SEQUENCE OF OUTPUT
SIGNALS. PRESSING SL DISCHARGES CL AND
LIGHTS LED L (AND LED 2 BRIEFLY). CL THEN
CHARGES THROUGH RM. AS CHARGE ON CL
PASSES 3 AND 6 VOLTS, LEDS 2 AND 3 GLOW
IN SEQUENCE. REDUCE R2 TO BALANCE
TIME DELAY SEQUENCE AND REDUCE DELAY
TIME. DELAYS SHOWN WILL VARY WITH
TOLERANCE OF C1.

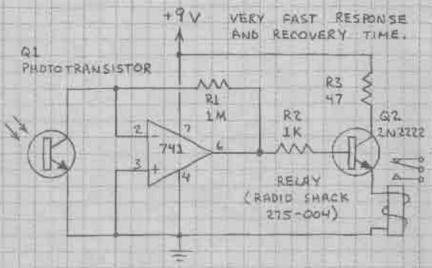
BARGRAPH VOLTMETER



LEDS GLOW IN SEQUENCE AS INPUT VOLTAGE RISES. LEDS ALSO RESPOND TO CHANGE IN RESISTANCE AT INPUT. TOUCH INPUTS WITH PINGER TO OBSERVE. CONNECT CAS CELL ACROSS INPUTS TO MAKE LIGHTMETER.

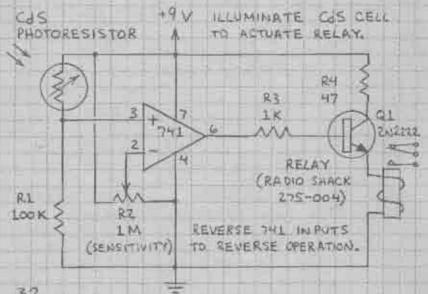
LIGHT-ACTIVATED RELAYS

PHOTOTRANSISTOR:

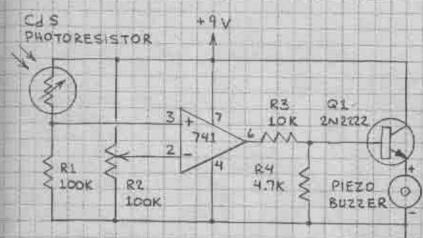


ILLUMINATE QL TO ACTIVATE RECAY.

PHOTORESISTOR:

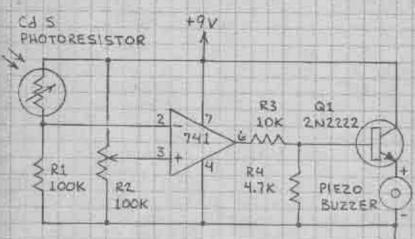


LIGHT-ACTIVATED ALERTER



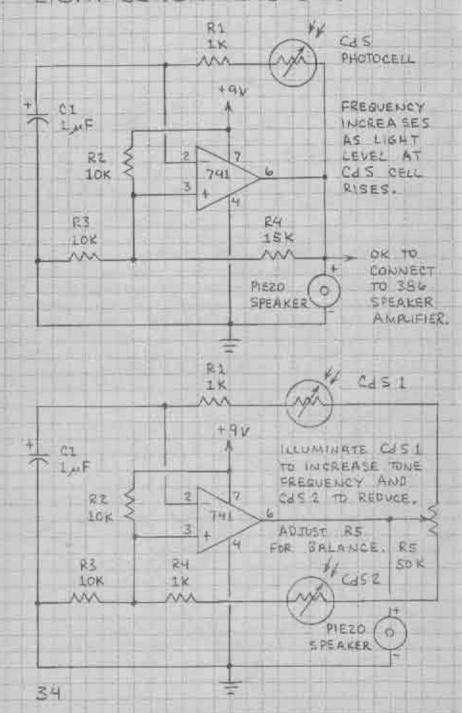
BUZZER EMITS TONE WHEN PHOTOCELL IS TILLUMINATED. RZ CONTROLS SENSITIVITY.
RH KEEPS QL OFF UNTIL THE 741 OUTPUT
GOES HIGH. USE AS SUN-ACTIVATED WAKEUP
ALARM AND OPEN REFRIGERATOR DOOR ALARM.

DARK-ACTIVATED ALERTER

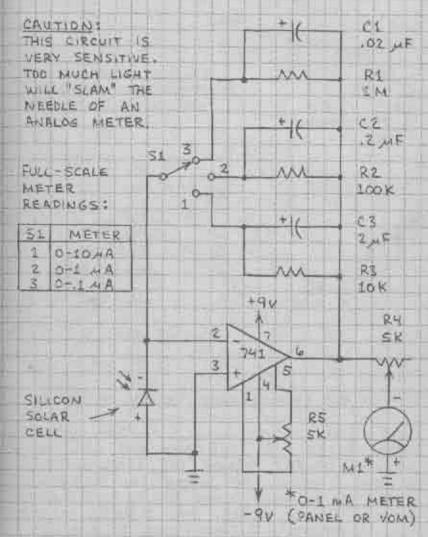


IDENTICAL TO ABOVE CIRCUIT EXCEPT TIMPUTS TO 741 REVERSED. OK TO REPLACE PIEZO BUZZER WITH RELAY (NO. 275-004).

LIGHT-SENSITIVE OSCILLATORS

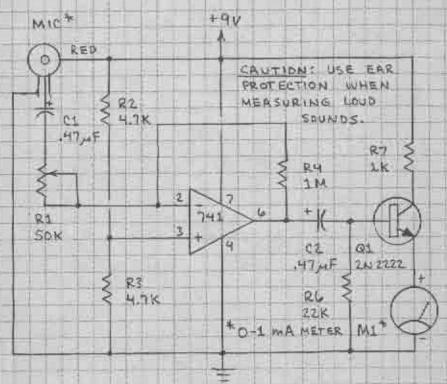


HIGH-SENSITIVITY LIGHT METER



THIS CIRCUIT IS BASED UPON THOSE USED IN SOME PRECISION, LABORATORY-QUALITY LIGHT METERS. TO ZERO METER, CONNECT PIN 2 TO GROUND AND ADJUST OFFSET (RS) UNTIL METER READS O. THEN DISCONNECT PIN 2 FROM GROUND. RY IS AN OPTIONAL CONTROL FOR ALTERING SENSITIVITY OF THE CIRCUIT.

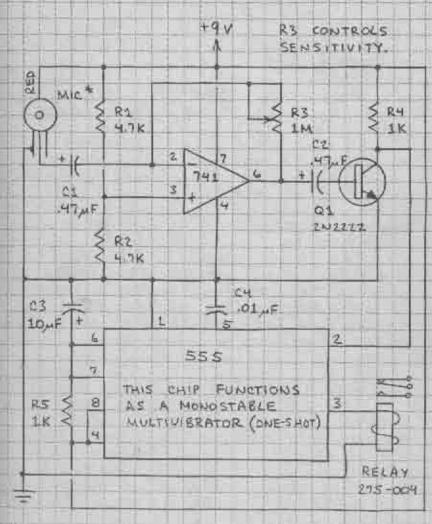
SOUND-LEVEL METER



*MICROPHONE (RABIO SHACK 270-092 OR SIMILAR).

THIS SIMPLE CIRCUIT IS AN EFFECTIVE SOUNDLEVEL METER. R1 CONTROLS THE GAIN OF
THE 741 OP-AMP, HENCE THE SENSITIVITY
OF THE CIRCUIT. THE METER CAN BE A PANEL
METER OR A MULTIMETER SET TO READ CURRENT.
THE CIRCUIT WAS TESTED WITH A PIEZO BUZZER
THAT EMITTED A G.S. KHZ TONE AT A SOUND
PRESSURE OF 9C dB. WHEN THE BUZZER
WAS 2" FROM THE MICROPHONE AND R1 WAS
SET FOR MAXIMUM GAIN, THE METER
INDICATED 1 MA. AT 12" THE PUTPUT FELL
TO 0.4 MA. NORMAL SPEECH AT 12" GAVE
FLUCTUATING SIGNAL UP TO 10 MA.

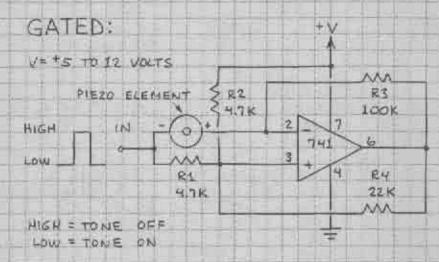
SOUND-ACTIVATED RELAY



* MICROPHONE (RADIO SHACK 270-092 OR SIMILAR).

THIS CIRCUIT TRIPS RELAY IN RESPONSE TO LOUD SOUND (VOICE, CLAP, ETC.). RS AND C3 CONTROL TIME RELAY STAYS PULLED IN (VALUES SHOWN GIVE ~12 SECONDS). IMPORTANT: USE OLLAF CAPACITOR ACROSS POWER SUPPLY PINS OF BOTH THE 7M1 AND SSS. REDUCE RESISTANCE OF R3 TO REDUCE SENSITIVITY.

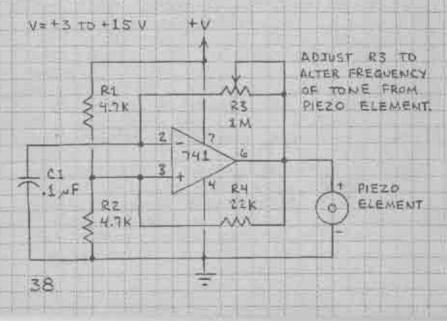
PIEZO ELEMENT DRIVERS



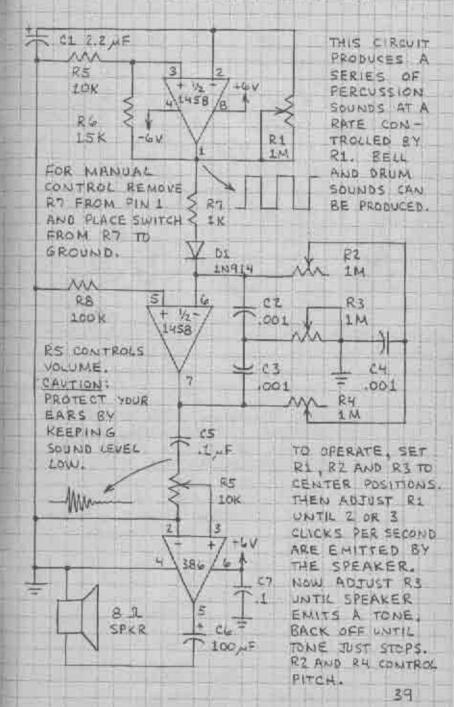
THIS CIRCUIT IS AN ASTABLE MULTIVIERATOR IN WHICH A PIEZO ELEMENT DOUBLES AS THE TIMING CAPACITOR AND THE TONE SOURCE.

TRIGGER WITH LOGIC SIGNAL OR BY CONNECTING SWITCH FROM INPUT TO GROUND.

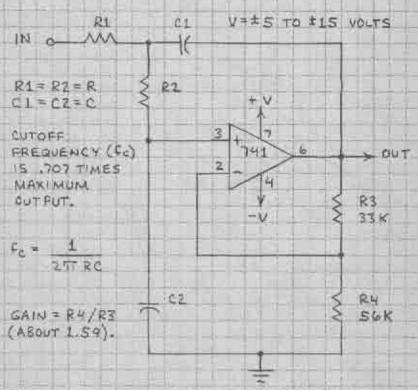
VARIABLE FREQUENCY



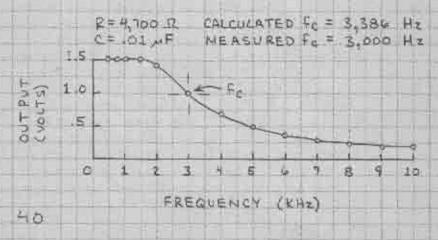
PERCUSSION SYNTHESIZER



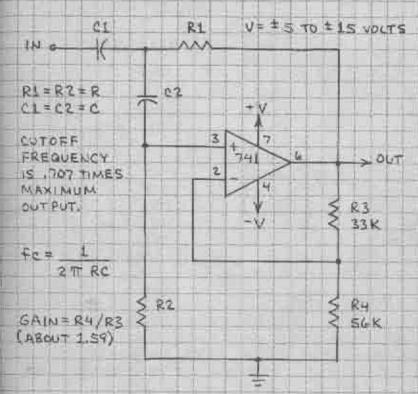
LOW-PASS FILTER



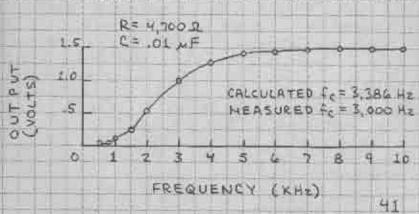
THIS IS AN EQUAL COMPONENT SALLEN+KEY FILTER: R3 SHOULD BE .586 x R4. SHOWN 3ELOW IS RESPONSE OF FILTER WHEN INPUT WAS A 1-VOLT SINE WAVE:



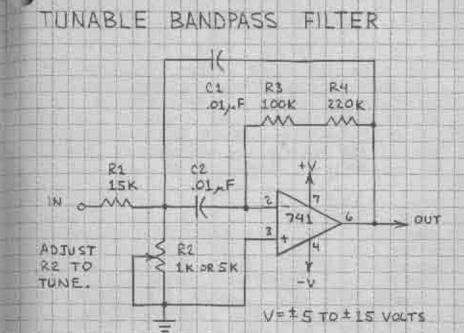
AIGH-PASS FILTER



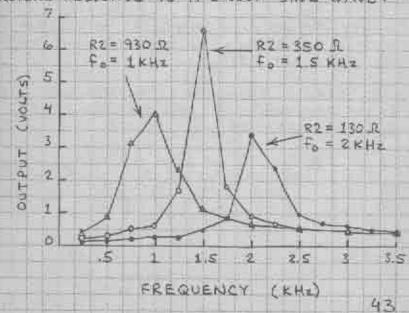
THIS CIRCUIT IS IDENTICAL TO THE EQUAL COMPONENT SALLEN-KEY FILTER ON FACING PAGE EXCEPT RI AND RE AND CI AND CE HAVE BEEN INTERCHANGED. BELOW IS RESPONSE WHEN INPUT WAS A 1-VOLT SINE WAVE:



60-HZ NOTCH FILTER WIEN BRIDGE IN o R3 R1= R2= R3 = R4=R5=27K 27K RIL C2 27K < .LAFT 13 2TTRC +741 - CUT 22 C1 84 27K Just 27K R5 27K MAN TWIN TEE R=R1=R2= 2×R3 C = C1 = C2 = C3/2 741 DUT IN. AM M 0-Ri 82 fo= 470K 470K 2TRC R3 220K .01 USE THESE FILTERS CL TO BLOCK POWER 02 LINE HUM. ,005 .005 1 WIEN BRIDGE . TWIN TEE GRAPH SHOWS .5 RESULTS FOR TEST VERSIONS OF BOTH FILTERS. 300 INPUT WAS 1- VOLT 100 200 PEAK-TO-PEAK SINE WAVE. 60 Hz FREQUENCY (Hz) 42

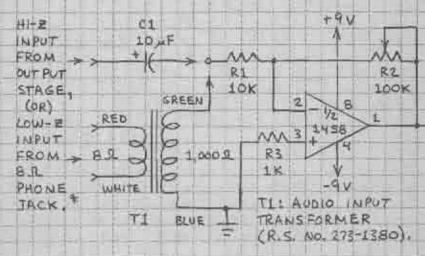


THIS FILTER CAN BE TUNED BY R2 TO PASS A NARROW FRE QUENCY BAND RETWEEN A FEW HUNDRED HZ AND ABOUT 3,000 HZ. USE TO DETECT PRESENCE OF A TONE IN A SIGNAL. ACTUAL RESPONSE TO A L-VOLT SINE WAVE!



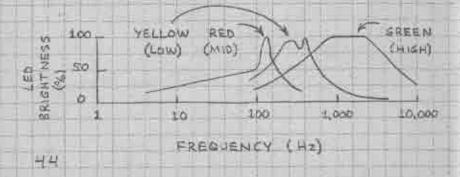
MINI-COLOR ORGAN

THIS ARRAY OF ACTIVE FILTERS WILL CONVERT THE AUDIO SIGNAL FROM A SMALL RADIO OR TAPE PLAYER INTO A FLICKERING PATTERN OF COLORS, R2 CONTROLS GAIN OF THE INPUT AMPLIFIER BELOW. USE RADIO/TAPE PLAYER VOLUME CONTROL AND R2 TO ADJUST INTENSITY OF LEDS.

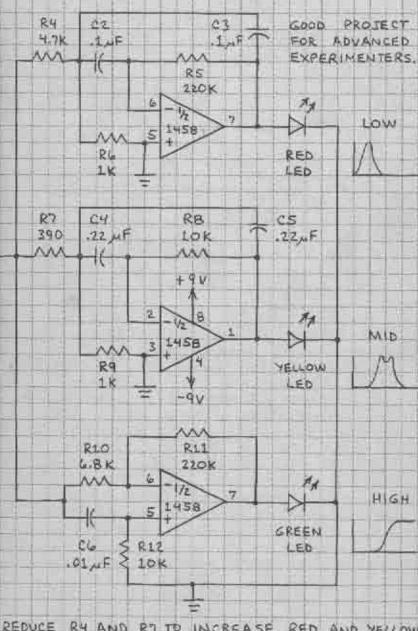


*INSERT PHONE PLUS COMNECTED TO TI PART WAY IN PHONE JACK SO SPEAKER WILL NOT BE SWITCHED OFF.

LEDS VARY IN BRIGHTNESS. EXPERIMENT WITH DIFFERENT LEDS FOR SEST RESULTS. HERE IS ACTUAL RESPONSE OF CIRCUIT!



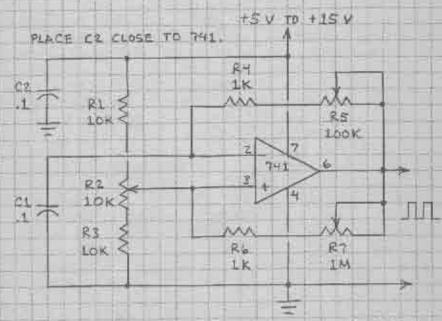
MINI-COLOR ORGAN (CONT.)



REDUCE RY AND RY TO INCREASE RED AND YELLOW BRIGHTNESS. INCREASE RIL TO INCREASE GREEN BRIGHTNESS.

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SQUARE WAVE GENERATOR



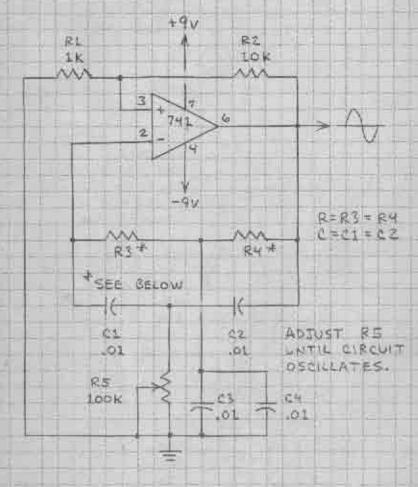
THIS CIRCUIT IS AN EASILY ADJUSTABLE
SQUARE WAVE SEMERATOR. THE TIMING
COMPONENTS ARE C1, R4, R5, R4 AND R7.
R1-R2-R3 CONTROL THE DURATION
(OR "WIDTH") OF THE PULSES. THE PULSES
ARE SYMMETRICAL WHEN R2 IS AT ITS
CENTER POSITION. OR TO CONNECT R2
DIRECTLY TO +V AND =, THEREBY
ELIMINATING R1 AND R3. TYPICAL RESULTS:

C1	FREQUENCY
.001	11,480 Hz
.047	3, 848 Hz
01	2,155 Hz
.047	462 14=
.1	227 Hz
,47	45 Hz
1.0	24 Hz

FOR THESE RESULTS,
R1-R2-R3 REPLACED BY
H.7K FROM PIN 3 TO
+V AND H.7K FROM
PIN 3 TO GROUND.
R4+ R5 = 100K,
R6+R7 = 22K, AND
+V = +12 VOLTS.

OK TO ADD FOLLOWER STAGE TO BUFFER OUTPUT.

SINE WAVE OSCILLATOR

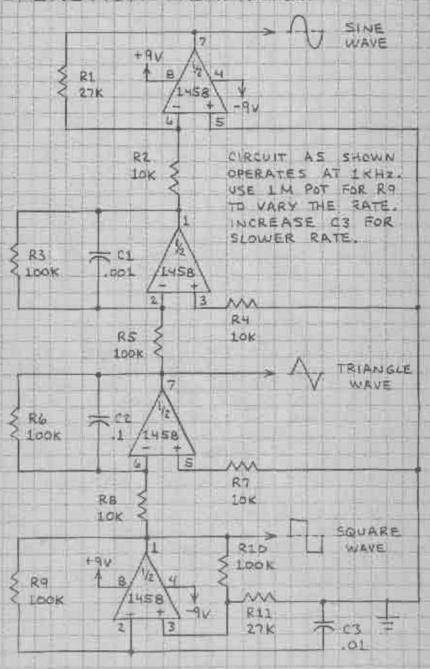


R3, R4, R5, C1, C2, C3, AND C4 FORM A
TWIN-TEE FILTER. WHEN CONNECTED
IN THE FEEDBACK LOOP OF AN OP-AMP,
THE RESULTING CIRCUIT GENERATES A
SINE WAVE. THE FREQUENCY IS L/(2T/RC).

TYPICAL RESULTS FROM TEST CIRCUIT:

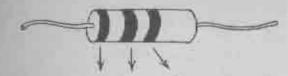
83 = R4	FREQUENCY
4.7 K	2926 Hz
LOK	135 G Hz
15 k	927 Hz

FUNCTION GENERATOR



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RESISTOR COLOR CODE



BLACK BROWN × 10 RED × 100 3 × 1,000 DRANGE YELLOW 4 × 10,000 5 × 100,000 GREEN 6 × 1,000,000 BLUE 7 × 10,000,000 VIOLET 8 × 100,000,000 SRAY q WHITE

FOURTH BAND INDICATES TOLERANCE (ACCURACY):
GOLD = 15 % SILVER = 10% NONE = 20%

OHM'S LAW; V=IR R=VI ±=V/R P=VI=I*R

ABBREVIATIONS

A = AMPERE R = RESISTANCE F = FARAD V = VOLT I = CURRENT W = WATT P = POWER IL = OHM

M (MEG-) # # 1,000,000

K (KILO-) # * 1,000

M (MILLI-) # .001

M (MICRO-) # .000 001

M (NANO-) # .000 000 001

P (PICO-) # .000 000 000