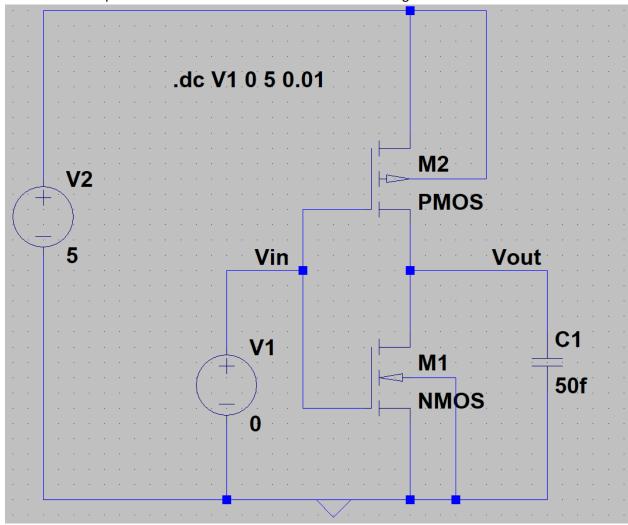
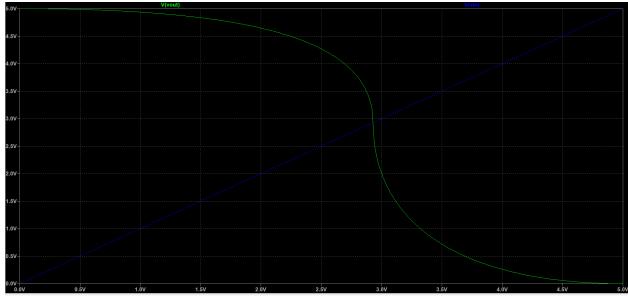
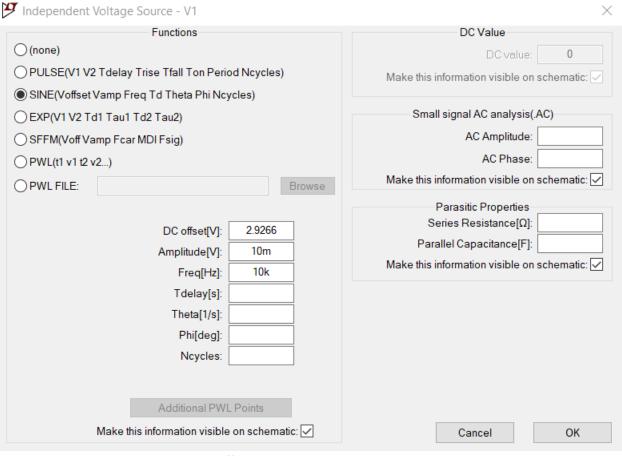
Experiment 2 steps:-

- 1) For nmos and pmos, go to components and select nmos4 and pmos4.
- 2) In length and width sections enter 2u for nmos and 4u for pmos, 0.5u length for both of them.
- 3) VDD should be 5V
- 4) Do the DC sweep of input Vin ie V1 from 0-5V, with increment of 0.01, plot the output.
- 5) The intersection point between Vin and Vout is the threshold voltage Vm.



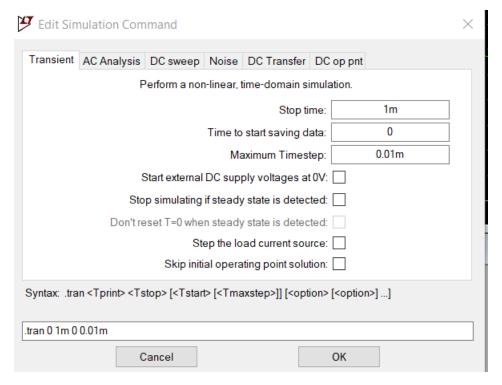


- 6) Change the widths of pmos as indicated in manual and plot outputs.
- 7) For the next part we have to give a sinusoidal wave input. Right click on V1 and go to advanced. Do as follows.



In edit simulation cmd do this. In DC offset write the value you get as Vm

8)



- 9) Plot the output along with Vin and measure the gain.
- 10) Now change the DC offset by a little amount and measure the gain again. Take 5-7 readings with different values of DC offsets and see how gain changes. Don't deviate too much from the original DC offset.

