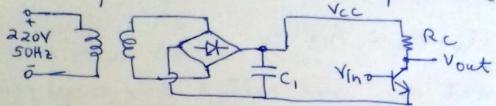
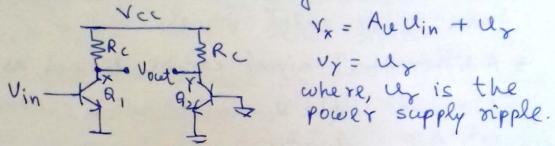


*Recall the rectifier circuit studied in "Introduction to Electronics". If that circuit powers a CE amplifier as shown below, then the 50Hz power-supply hum would show up in the output.



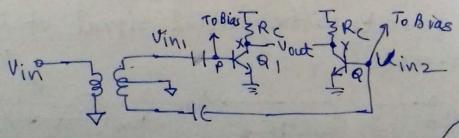
* what if we do the following: -



The noise on Vcc, which is the power supply hum would get cancelled out, giving us a clean output voltage.

Thus, Ux - Uy = Vout = Aulin.

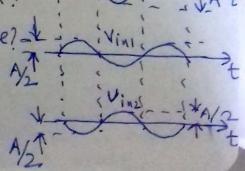
* Instead of grounding the base of \$2 above (Keep in mind Ac ground) can we do something different. What if we do the following,



How do win, and winz look?

=> Vin, and line are out-of-phase? -k.

=) Amplitude of vini and vinz are same but half of amplitude of vin.



Thus, Uin = -Uinz. and Ux = Au Ulini + Ur. Uy = - Au linz + Uf. [| Uin | - | Uinz | = (Uin) => Ux-Vy = Ace lin * Uin, and Vinz are called differential signal and the transformer generates differential signal from single-ended signal. * A differential signal can be defined as follows!-V1 = Vo sin (et) + Vcm V2 = - Vo sin(wt) + Vcm where, v, and v2 are differential signal Vo is the amplitude of V, and V2 Vcm is the DC level over which the AC signal rides and is therefore called secommon-mode (CM) * (v,-v2) has a total peak-to-peak differential swing of 4 Vo. * How to generate differential signal of different common-mode?

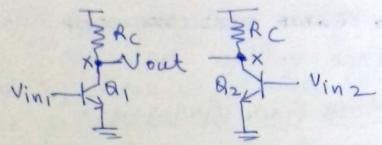
Vin 3 & Hira Vinz

Vinz

Vinz

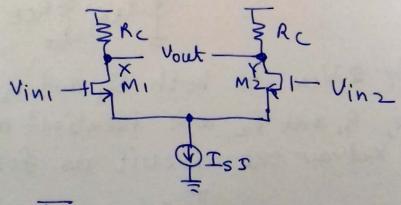
Vinz

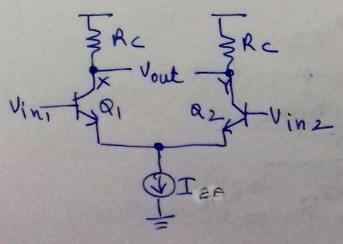
* Psuedo- Differential Amplifier:



- -> Here input CM is approx. 0.7 V.= Vbelon.
- → If Ic is the bias current flowing through &1 and &2 then output CM is Vcc IcRc.
- ⇒ If input CM changes by 20mV then bias current changes by 2.2 times and output CM changes to (Vcc 2.2 IcRc) ⇒ a drastic change as it would compromise the output voltage swing. Why?? Recall swing limitations in (E and C5 amplifier.

* HOW DO YOU MAKE THE OUTPUT CM INSENSITIVE
TO INPUT COMMON MODE:-





* ADD a tail current source as shown here.

* In order to ensure that
Output CM is insensitive
to Input CM we have to
make sure that M1, M2
are in saturation and R1, R2
are in active mode.

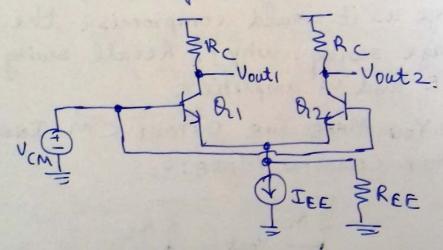
* Also, we have to make sure that Iss and

IEE as between mets to make sure their

compliance voltage met.

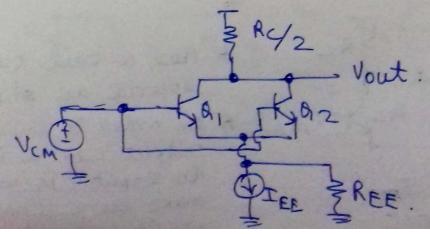
COMMON MODE GAIN ANALYSIS: -

- * Ideally the si differential amplifier is insensitive to input CM variation.
- * However, if the tail current source has finite impedance then the output CM changes with variation in input CM.
- * In-order to analyze input CM response we do the following:



=> Apply Q. Vcm to both Q, and Q2.

=) since, 9, and 92 are identical and then we can redraw the circuit as follows:





Thus, $A_{CM} = \frac{V_{OUT}}{V_{CM}} = \frac{2g_{m1/2}}{1 + 2g_{m1/2}R_{EE}} \cdot \frac{R_C}{2}.$

as, Q, and Q2 come in parallel and the combination is degenerated by REE.

* For the same amplifier, differential mode gain is,

ADM = + gm1,2 Rc.

MOTE: - Sign of the gain does not matter because of the differential nature of the input and output.

