CS452 Project

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July 27, 2012

Part I Usage

1 Executable

The executable is located at /u/cbiocca/cs452/project/kernel.elf (checksum: c725bd067c1a130f41cl To remake the executable:

cd /u/cbiocca/cs452/project/src/make prod

2 Running the program

- 1. The program can be started using the standard commands.
- 2. It will take a second or two to boot up, as it needs to reset the track to a known state.
- 3. Once running, the clock can be found in the top right, the switch positions in the center area, and the recently triggered sensors below.
 - (a) The most recently triggered sensor is on the left, less recent sensors on the right.
- 4. It also displays the train's known position in the top left.
- 5. The command area is at the bottom of the screen.
 - (a) It accepts the 4 required commands.
 - (b) It is picky about syntax. Trailing characters are an error, and so is more than one space as a separator. Commands execute when the enter key is typed.
 - (c) To help diagnose typing errors, if the parser notices that invalid input is being entered, it will highlight it in red, starting at the position the command became invalid. It validates ranges for numeric arguments.

- (d) All numbers are in decimal, including switch addresses (Making the center switches 153-156 inclusively).
- (e) Syntax is case insensitive, all letters are lowercased coming in.
- (f) Starting an engineer to manage a specific train is e #train.
- (g) Once the engineer has acquired a location, routing it to a given spot is z #train landmark.

2.1 Troubleshooting

Hopefully this is unneeded, but if necessary, rebooting the train controller, clearing the screen and resetting the arm controller before starting the program again tends to work nicely.

3 Source Code

The source code submitted for this assignment can be found under /u/cbiocca/cs452/project/src/. Here are the checksums for the files:

```
83185\,b7100\,ee\,5f0\,d2\,5ac3\,4c0\,d3\,d5\,8\,4\,d3
                                                           ./.gitignore
                                                           ./Makefile
4 \operatorname{ca} 0 \operatorname{d} 8 \operatorname{d} 7 \operatorname{e} 0 0 8 4 8 2 f 8 4 6 3 \operatorname{d} 8 1 1 3 1 \operatorname{d} 3 1 5 2 3
                                                           ./README.md
436 a fe 7fc 1440 8e e 2f8 b e 159 b 83 c 4a c 1
                                                           . / data/BEST_HASH
0\,d7272fd2615423d956ebf7b375929d8
19380\,c\,b\,b\,a\,b\,26\,b\,a\,f\,2\,4\,0\,3\,7\,8\,3\,2\,a\,0\,8\,d\,a\,5\,c\,3\,6
                                                           ./data/parse_track.py
                                                           ./data/parseoutput.rb
ba66a1256e252e3ee3f38ad235e61b15\\
2 \, \text{f} \, 6 \, 3 \, 9 \, 5 \, \text{e} \, 2 \, \text{f} \, 4 \, \text{c} \, 6 \, 8 \, 0 \, \text{f} \, 7 \, 9 \, \text{e} \, 3 \, 5 \, 8 \, 0 \, 4 \, \text{b} \, 9 \, 6 \, 9 \, \text{c} \, 4 \, \text{f} \, 5 \, 4
                                                           ./data/tracka
445\,\mathrm{fe}\,48\,\mathrm{f}\,0\,18\,10\,3\,8\,\mathrm{f}\,3\,2\,\mathrm{c}\,3\,\mathrm{b}\,\mathrm{d}\,58\,\mathrm{e}\,4\,4\,7\,2\,\mathrm{a}\,3\,\mathrm{c}
                                                           ./data/trackb
                                                           . / doc /CALIBRATION
943217198 c 8 f 4 9 c 0 b a 9 f e a 5 d 4 0 f 6 b a f 8
                                                           . / doc/WORKFLOW
d761266f3eb599d0dfc6dd88d52619c9
5\,fb\,0\,8\,0\,ea\,e\,0\,d\,5\,a\,f6\,6\,c\,8\,0\,b\,2\,7\,5\,8\,3\,7\,d\,8\,2\,9\,c\,6
                                                           ./doc/k1.lyx
a\,f\,5\,2\,6\,e\,e\,1\,c\,1\,8\,6\,9\,d\,0\,2\,1\,c\,6\,2\,0\,b\,3\,2\,2\,b\,9\,f\,7\,e\,2\,5
                                                           ./doc/k2.lyx
691 be 4 bc 843 a c 1 b 54 e 2 e 701 a 98 b 4 f d 81
                                                           ./doc/k4.lyx
acd7d9afde797dda0c3d097569d301af
                                                           ./doc/tc1-hack.lyx
1\,b\,58\,9\,0\,fb\,f8\,d\,8\,2\,7\,4\,f\,9\,e\,3\,d\,c\,d\,d\,2\,e\,b\,fb\,1\,2\,e\,a
                                                           ./doc/tc1.lyx
                                                           ./include/bwio.h
3fb369afd0a490365655a5bfa157adf8
                                                           ./include/cpsr.h
c13942a5833307674dba2f254809595d
994cb3d4c9fdf6d59f7d2e8fd5190a55
                                                           ./include/debug.h
                                                           ./include/kernel/interrupts.h
91ee8f0b6de1f3e23857502e3732c852
                                                           ./include/kernel/ipc.h
f48ea9097d647cf0e299eea0cb838117
                                                           ./include/kernel/task.h
04095eebff3a2559d09e16cd4e998617
9\,9\,76\,c\,1a\,b\,7a\,d\,6\,4\,4\,6\,8\,2\,d\,a\,fa\,e\,5\,1\,1\,7\,2\,e\,f\,6\,1\,9
                                                           ./include/kernel/trampoline.h
                                                           ./include/lib.h
aeb47082076d178c1784cc5dcd786a2b
7 \operatorname{cc} f 9 26 19 52 25 \operatorname{fc} a b 0 16 18 6 c 8 c 5 18 5 \operatorname{fe}
                                                           ./include/stdbool.h
                                                           ./include/ts7200.h
a\,b\,50\,e\,9\,6\,7\,4\,e\,5\,e\,b\,7\,a\,a\,2\,3\,b\,9\,7\,d\,3\,5\,0\,2\,8\,3\,2\,0\,f\,a
                                                           ./include/user/clock.h
1a026162a95f84eb28bfae5e8c50c429
b11121fa5217f977a0441d8721f0520d
                                                           ./include/user/clock drawer.h
```

f6fb657b597079f5a7821c54e8b24962 dbdf4d53d41168a98bd51fd9ea4997f3ebed7e5cb0e74134077adcbe34b95141 13 fa 76 2 2 b 7 c 4 2 f 76 1 c 5 d b e f 8 8 0 3 3 b 6 4 3 e9a7384b7de24bf6eca953fb6555e0c0c43f99b170424b893e6769414cca0292 $028 \, a \, 9 \, 25756 \, 21233 \, b \, c \, 0689523896 \, a \, 6 \, c \, 71$ caf77700473316bef55fe272ecbcfc32 af578ce26dc1a2dd92828b10bfe1175e $1 \, f0 \, 8 \, 3 \, cc \, 3 \, eb \, 8 \, d2 \, b6 \, cc \, 7 \, fe \, 0 \, 46 \, 7b \, 7b \, d8 \, 71b$ $1133\,e3\,d3\,b6\,8d8\,ddfc6\,8f48\,b6\,de\,2a\,0\,11\,2$ d5b21bea98ff50825398fff55b01601c4c0bdd4751ecb8ed3be25dd71d7c86be65 dc 14 ad 86 590 6588 c2 f844 ef376111616 fa 29 76 ca 7 fa 79 5 f8 70 4 1 2 a 5 e 5 c 2 d d 4 $403814223\,e\,b\,206\,d\,e\,c\,10\,d\,d\,4\,7\,9\,6\,c\,9\,3\,0\,b\,4\,2$ $6948\,a\,4769\,1\,e\,c\,5\,fe\,2\,1\,fe\,9\,1\,d\,8\,0\,2\,6\,a\,0\,0\,fa\,3$ 936 d372675c83ab6ace0c553594580cf 08 a e 8 d 5 19 a 47 d f 09 d 89 140 2 e f 448 0 e 6425757 ddee 6 d82 a 16 be 926075 e 04 b 4972 $3880\,\mathrm{b}\,\mathrm{d}8\,\mathrm{c}\,7594\,\mathrm{d}3\,\mathrm{b}0\,\mathrm{b}5\mathrm{f}11\mathrm{e}\,55\mathrm{a}\,7\mathrm{c}0\,\mathrm{f}41\mathrm{d}$ 3 e b d 6 f 0 e 170 4 5 2 e d a 40 9 4 e 49 b 2 b c 7 5 0 7 6 a d c d 1 4 6 c c 1 4 5 f 4 1 e 7 5 2 2 2 c 4 6 4 d 6 b a 1 8 b6927156163 cab78 bc599 a 7 cc1 e 76949 $a\,55\,51\,c\,b\,200\,4\,e\,0\,4\,6\,f\,3\,8\,e\,b\,d\,8\,3\,a\,6\,1\,8\,d\,b\,8\,1\,0$ ee3f874c894c039f95edbbf2e1350e05ba2bbda6474d8eb0c742de1778c353d185a06c179e618d41cfa86f5a51346f43 ee9847d34ef3f14582e58f2bbdb3c2daae9d089d2f5260d6f2ecaea987e19b67537 ca 13 a 501 e 93 cd 4 e 7 b a 5 de a fd 8 0 0 f 5 $5\,d\,b\,2\,2\,8\,d\,5\,d\,3\,2\,8\,8\,c\,1\,1\,0\,d\,8\,6\,6\,f\,0\,4\,9\,d\,6\,f\,6\,f\,9\,4$ d8ae55d544e7b5293e0f7c5ae79fa235 e3810e414941275f21c099fe160815d6 $7 \, ceb \, 083605959f58f2c36785210a2399$ e 2af7655b1c5b954c3e9c4e964bf27d2 a35284dd4646ca6c3d0784651e5a4bf210 d8 149 a b d a 2 c 2 a 5 2 4 7 c a a d b 4 1 1 8 4 8 4 d $1056255\,ba9e\,e68987414\,d82da357f47e$ 7104 c 426418 a d a 62001 c 961 f 6 a a b 1 d 59 $825\,e\,8\,5\,b\,c\,28\,c\,7\,e\,8\,06\,a\,2\,4\,e\,b\,8\,4\,f\,a\,b\,9\,7\,3\,2\,0\,7$ bedac67e6bda1fd538f0ae710a9e1bdf 33 da 1c 00 24 21 5 d6 c 446 9 d60 50 e 90 b 91 adabfe 5cebd 59 55c1 f8 39 def 8cf 457 d4b68 dda f08a 986 158a 67985a f6b 6729086

```
./include/user/controller.h
./include/user/courier.h
./include/user/engineer.h
./include/user/freight.h
./include/user/heap.h
./include/user/init.h
./include/user/kinematics.h
./include/user/log.h
./include/user/mio.h
./include/user/nameserver.h
./include/user/parser.h
./include/user/pathfinding.h
./include/user/priorities.h
./include/user/sensor.h
./include/user/string.h
./include/user/syscall.h
./include/user/tio.h
./include/user/turnout.h
./include/user/vt100.h
./kernel/bwio.c
./kernel/interrupts.c
./kernel/ipc.c
./kernel/kernel.c
./kernel/lib.c
./kernel/task.c
./kernel/task internal.h
./kernel/trampoline.c
./linker.ld
./user/clock.c
./user/clock drawer.c
./user/controller.c
./user/courier.c
./user/engineer.c
./user/freight.c
./user/init.c
./user/kinematics.c
./user/log.c
./user/mio.c
./user/nameserver.c
./user/parser.c
./user/pathfinding.c
./user/sensor.c
./user/syscall.c
./user/tio.c
./user/turnout.c
```

Part II

Track/Train Control

4 Train Controller

The train controller manages three pieces of data:

- 1. A circular buffer of trains trying to find their position.
- 2. An array with engineer task id and last recorded position for each train.
- 3. A two-dimensional array mapping sensor and sensor number to engineer task id, (keeping track of engineer expectations).

It essentially runs in an event loop, receiving engineer requests (through their respective couriers) to set sensor expectations and switching turn-outs,

sending messages to them based on new sensor readings. It also updates the screen as needed.

5 Train Engineer

The engineer first spawns 2 helper processes:

- A timeout worker, which waits until a given time, then replies to the engineer, ad nauseam.
- A courier, used in messaging the train controller.

Then it drives until it hits a sensor. This sets its starting position. When it receives a sensor update, or a timeout, it recalculates its current position. It also sets expectations as to which sensor will next be hit, and when. As it moves it keeps a model of its current acceleration and speed, periodically corrected by the sensor updates coming in from the train controller.

5.1 Pathing

When sent an instruction to navigate to another position, the engineer first calculate an optimal path using Djikstra's algorithm (technically A* with a heuristic of 0), using a closed list to avoid revisiting the same nodes over and over. After planning a path, the engineer starts moving forward. As it does it continuously sets the switches that are at most its stopping distance along its path. The stopping distance is derived from the current calculated speed. Finally when it notes that the final position is exactly the stopping distance ahead of it, it starts decelerating.

5.2 Engineer Modes

An engineer can be in several modes, which can be set from the command line:

User

When a user explicitly specifies a destination for the train.

Circle 1

A circle composed of the sensor destination sequence {E14, D6, D1}.

Circle 2

A circle composed of the sensor destination sequence {C10, E2, A4}.

Random

A random destination is picked for the train.

Freight

Described by Section 5.2.1.

With the exception of user mode, modes are comprised of a sequence of destinations. The next destination is only generated when the train has reached its current one and so a mode change only affects the train if it doesn't already have a destination. However, user mode overrides any previous destination by design, as a sort of manual override.

5.2.1 Freight Mode

A freight is defined as a package that exists at a certain location which must be picked up and delivered to another location. The freight server is responsible for randomly generating freights (or using user specified freights specified from the command line) and making them accessible in a globally readable array.

Since only a single train may carry a freight to its destination, some synchronization needs to be implemented. We did this by making sure that writes to the available freights are only done by the freight server, which avoids an entire class of race conditions. Furthermore, when a train arrives at the source location of a freight, it must claim the freight by contacting the freight server with the unique freight ID. The server will then respond with an integer 1 or 0, corresponding to the success or failure of claiming the freight respectively. It claiming a freight fails, it is because another train has already claimed it and the train must find another freight to claim.

Once a train has successfully claimed a freight, the freight server generates a new freight in its place. If a user has specified a freight from the command line, this is the freight generated, otherwise a random one is generated.

Trains in freight mode query the available freights, find the one closest to them, and navigate towards the source of the chosen freight. This process repeats until a freight is successfully claimed, at which point the train navigates to the freights destination to successfully deliver the freight. After that, it goes back into finding freights.

6 Reservations

Reservations are implemented as 2 independent halves by the train engineer task: respecting reservations on the track and making and releasing reservations as the engineer needs them. In the reservation system implemented, the granularity supported is limited to track edges only. For the purposes of the reservation system, a track edge and its reverse are treated equivalently.

6.1 Respecting Reservations

All the track edges are available as part of the global track graph, and is thus readable by all engineers. An extra field called **reserved** was added to the track edge data type, which stores the train ID that the edge is allocated to or -1 otherwise. Thus, the invariant that all engineers must maintain is simple: one must never traverse an edge that they do not own. Since the state of reservations is available globally, checking reservations is cheap and doesn't even require a Send()/Receive() cycle.

When an engineer approaches a piece of track they do not own, they slow down and check the status later. An engineer must always ensure that they are capable of stopping without violating reservation invariants. If an engineer comes to a complete stop, they set a timeout which has both a static component (8 seconds) plus an additional random component based on the train ID (train 0 adds 0 seconds, train 80 would add 22.5 seconds). If this timeout is hit and the train is still unable to get the reservations it needs to follow the planned path, it replans its route. Route planning takes into account the current state of reservations and plans around it.

The random component based on train ID allows for fairly intelligent emergent behaviour from the trains. Since there is a large variance between the timeouts, in a deadlock situation, it is very likely that a single train would choose to replan before the other train. This first train would then follow its alternate route, giving up reservations which can then be used by the other train.

6.2 Making and Releasing Reservations

All updates to the reservation information in the track graph are made by the train controller, which is a single task. This synchronizes access to the graph and avoids an entire class of concurrency issues.

The engineer keeps track of reservations in three different classes. First, a function computes the set of edges that it thinks it needs. It does so by adding the edge that it is currently on, as well as all the edges its tail might be on if its near the ends of the edge. Finally, it projects forward by the current stopping distance, adding all traversed edges to the set, as well as edges on both sides of traversed branches.

Then a function compares what is in the set that is needed and the information presented in the track graph. If it detects that certain edges that it needs are are assigned to it, it moves said edges to a new set of granted reservations.

Finally, a function computes edges that are in the granted set but are no longer in the needed set. These edges are released back to the controller.

All the while, a courier is used as a communication bridge between the train controller and the engineer.

6.3 Interaction with Track Features

The train makes primary and secondary expectations (sensors that when triggered should be passed along to this specific engineer) with the train controller. Two engineers expecting a sensor is strictly an error and manifests itself as an assertion failure. The idea behind setting primary and secondary expectations is allow for track failures. For malfunctioning switches, the notion of an alternate sensor is also supported, and the switch would be marked as disfunctional should an alternate sensor be triggered enough times. However, at the time of writing, these systems are only partially implemented and no modifications to track topology are actually made.

To synchronize control of various track features, an engineer is only allowed to set an expectation on a sensor if it owns reservations on the edges on both sides of said sensor. Likewise, an engineer may only switch a branch turnout if it owns all 3 edges. This conservative system prevents quite a few accidents since expecting a sensor expressed the notion that a train rightfully intends to pass over it and thus can only do so if it has the reservations to back it up. Similarly, a turnout should not be switched unless the train owns all edges leading to/from it.