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Also, in the solutions of Examples 3.9 and 3.10 and in Fig. 3.12(b). In Fig. 3.26, change V to out. In Fig. 3.30(b), t 2 should denote the time at which V in and V out cross. In Fig. 3.41, change R out to L. In Fig. 3.67, place a resistor in series with D 1 and call it R 2. In Fig. 3.68, place a resistor in series with V B and call it R 2.

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A. Manian and B. Razavi, "A 32-Gb/s 9.3-mW CMOS Equalizer with 0.73-V Supply," Proc. IEEE Custom Integrated Circuits Conference, Sept. 2014. B. Razavi, "The Role of Translational Circuits in RF Receiver Design," Proc. IEEE Custom Integrated Circuits Conference, Sept. 2014. S. Hwu and B. Razavi, "A Receiver Architecture for Intra-Band Carrier Aggregation," Symposium on VLSI Circuits ...

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1 RF Microelectronics, Second Edition Errata Behzad Razavi Example 2.11, change Eq. (2.53) to 3.16 V p and Eq. (2.54) to + 20 dBm. On page 40, last paragraph, the noise current of a resistor should read

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