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Solutions for Homework ** Accounting 311 Cost ** Winter 2009

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CHAPTER 2 SOLUTIONS 5 (b) (c) vT = kT/q = 34 mV; note, however, that the total leakage will normally be higher for both threshold voltages at high temperature. 2.11 The nMOS will be off and will see Vds = VDD, so its leakage is 2.13 Assume VDD = 1.8 V. For a single transistor with n = 1.4, For two transistors in series, the intermediate voltage x and leakage current are

solutions - cmosvlsi.com

SUBCHAPTER I—FEDERAL INFORMATION POLICY Amendments. 2000— Pub. L. 106-398, §1 [[div. A], title X, §1064(a)(2)], Oct. 30, 2000, 114 Stat. 1654, 1654A-275 ...

[USC07] 44 USC CHAPTER 35, SUBCHAPTER I: FEDERAL ...

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