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Abstract. Linear time-invariant discrete digital plant, with no restrictions on the form of the state equation and the number of controls, is considered.

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Discrete time control systems. Operational amplifier 1 is an input amplifier with a high input impedance. Operational amplifier 2 is the output amplifier; it buffers the voltage on the hold capacitor. There are two modes of operation for a sample-and-hold circuit: the tracking mode and the hold mode.

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