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In reality, this course is a snapshot of a more detailed, 40 hour course on CMOS analog design found elsewhere. The target audience for this course should have some familiarity with analog circuits and integrated circuit technology. The terminology used is that found in both academia and industry.

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EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged. SM 2 EECE 488 – Set 1: Introduction and Background Marking

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Chapter 4 Analog CMOS Subcircuits From the viewpoint of Table 1.1-2, the previous two chapters have provided the background for understanding the technology and modeling of CMOS devices

and components compatible with the CMOS process. The next step toward our objective—methodically developing the subject of CMOS analog-circuit design—is to ...

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