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SM 1 SM 1 EECE 488 – Set 1: Introduction and Background. EECE488: Analog CMOS Integrated Circuit Design Introduction and Background. Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca. Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged.

### **EECE488: Analog CMOS Integrated Circuit Design ...**

Behzad Razavi. IEEE Donald O. Pederson Award "For his pioneering contributions to the design of high speed CMOS communication circuits".

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Design of Analog CMOS Integrated Circuits. Behzad Razavi. Errata. p. 6, Section 1.3, first sentence should read: The idea of metal-oxide-semiconductor ... p. 26, In Eq.

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