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This site is dedicated to providing resources for students and professionals in the area of analog integrated circuit design using CMOS and BiCMOS technologies. The resources are designed to help understand the principles, concepts, and techniques of analog integrated circuit design.

### **Phillip E. Allen - aicdesign.org**

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I.1 - INTRODUCTION. GLOBAL OBJECTIVES • Teach the analysis, modeling, simulation, and design of analog circuits implemented in CMOS technology. • Emphasis will be on the design methodology and a hierarchical approach to the subject. SPECIFIC OBJECTIVES. 1. Present an overall, uniform viewpoint of CMOS analog circuit design. 2. Achieve an understanding of analog circuit design.

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Allen and Holberg - CMOS Analog Circuit Design Page III.4-4. Typical Model Parameters Suitable for SPICE Simulations Using Level-3 Model (Extended Model). These Values Are Based upon a 0.8  $\mu$  m Si-Gate Bulk CMOS n-

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In reality, this course is a snapshot of a more detailed, 40 hour course on CMOS analog design found elsewhere. The target audience for this course should have some familiarity with analog circuits and integrated circuit technology. The terminology used is that found in both academia and industry.

### **CMOS Analog Circuit Design | Udemy**

EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi  
Department of Electrical and Computer Engineering University of British Columbia  
shahriar@ece.ubc.ca Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged. SM 2 EECE 488 – Set 1: Introduction and Background Marking

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Chapter 4 Analog CMOS Subcircuits From the viewpoint of Table 1.1-2, the previous two chapters have provided the background for understanding the technology and modeling of CMOS devices

and components compatible with the CMOS process. The next step toward our objective—methodically developing the subject of CMOS analog-circuit design—is to ...

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