

[illegible]

The image displays three timing diagrams for RFM95W modules, labeled U6, U7, and U9. Each diagram shows the relationship between various signals over 14 time slots (1 to 14). The signals are color-coded: VccGND (green), Vin (blue), GND (red), EN (green), G0 (blue), G0.1 (red), SCK (green), MISO (blue), MOSI (red), CS (green), and RST (blue). The diagrams illustrate the sequence of operations for each module, including the timing of the RST signal relative to the other signals.

**U6 RFM95W**

**U7 RFM95W**

**U9 RFM95W**

The diagram illustrates a CAN Transceiver circuit. A microcontroller (CNF2) is connected to a CANH/CANL transceiver (U5). The microcontroller's CTX pin is connected to pin 1 of the transceiver, and its CRX pin is connected to pin 4. The microcontroller's D pin is connected to pin 7, and its R pin is connected to pin 6. The microcontroller's Vref pin is connected to pin 2, and its Rs pin is connected to pin 3. The microcontroller is also connected to a 10k resistor (R3) to ground. The transceiver (U5) is connected to a 3.3V supply and ground. It has two diodes (D1, D2) connected to its CANH and CANL pins. The transceiver is also connected to a 120 ohm resistor (R4) to ground. The transceiver's CANH pin is connected to pin 1 of the CAN bus (J2), and its CANL pin is connected to pin 2. The CAN bus is connected to a 3.3V supply and ground. The CAN bus pins are labeled 1, 2, 3, and 4. The microcontroller is labeled SN65HVD230.

ECS-120-10-36Q-AES-TR  
 $Cl_{load} = 2 * (CL - C_{stray})$

Conn\_ARM\_SWD\_TagConnect\_TC2030 J4

Pin	Signal	Connection
1	VCC	+3.3V
2	RESET	NRST
3	SWCLK	WCLK
4	SWDIO	WDIO
5	GND	GND
6	SWO	SWO

H1 M3 H2 M3 H3 M3 H4 M3

H5 M3 H6 M3 H7 M3 H8 M3