

Contact

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Chennai
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Top Skills

RTL Development

Field-Programmable Gate Arrays
(FPGA)

Verilog

Rishabh C S

Electrical Engineering | IIT Madras | Xilinx | Aspiring Comic
Chennai

Experience

Xilinx

Intellectual Property Engineer
May 2020 - July 2020 (3 months)
Hyderabad, Telangana, India

Xilinx

Summer Intern
May 2019 - July 2019 (3 months)
Hyderabad Area, India

Created a Hardware Accelerator for Data Server Disk Storage Systems using
RTL and Xilinx Vivado tools

Melvano Education Private Limited

Content Developer
September 2018 - November 2018 (3 months)
Chennai Area, India

Prepared question papers along with solutions in Math, Chemistry and Physics
for class 11 and class 12 students for a Mobile Application

Education

Indian Institute of Technology, Madras

Bachelor of Technology, Electrical and Electronics Engineering · (2017 - 2021)