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SHA	THEED BHAGAT SINGH STATE TECHNICAL CAMPUS, FEROZEPUR	8	
ROI	L No: Total number of pages:[2  Total number of questions:06	1 1	
	B.C.A. 4 <sup>th</sup> Sem		
Microprocessor and microcontroller			
	Subject Code: BSBC402		
Paper ID:			
Time allowed: 3 Hrs Max Marks:60			
lmpo	rtant Instructions:		
•	All questions are compulsory		
•	Assume any missing data		
۰	Additional instructions, if any		
	PART A (2×10)		
Q. 1.	(a) Explain ALE	II COs	
	(b) Synchronous serial communication vs. Asynchronous serial communication.		
	(c) Branching operations (d) Internal and external memories.		
	(e) Explain Instruction set.		
	(f)Explain instruction cycle.		
	(g)Explain UART (h)What is program counter?		
	(i) What is system bus?		
	(j)Explain interrupt.		
	PART B (8×5)		
Q. 2.	Explain architecture of 8085 with diagram.	CO1	
Q. 2.	OR	CO1	
	Explain Instruction cycle and timing diagram of memory read/write	COI	
0.3	operations. Explain the architecture of 8051 with pin diagram.	CO2	
Q. 3.	, OK	002	
	Explain the use of DMA controller with diagram.	CO2 CO3	
Q. 4.	Explain the architecture of interrupt controller 82.39.		
	Explain and differentiate between Data movement instruction and Arithmetic	CO3	
	instructions	CO4	
Q. 5.	Explain the architecture of clock generator 8082A.  OR		

With help of diagram explain memory organization of microcontroller.

Q. 6.

What are addressing modes? Why we have number of addressing modes. CO5

Explain the difference between immediate, relative and index addressing

CO4

modes.

OR

Write short notes:

- (a) Explain counters and timers.
- (b) Explain the various modes of DMA.

CO<sub>4</sub>