

ROLL No:

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Total number of pages:[1]  
Total number of questions: 06

**B.Tech. || EE || 5<sup>th</sup> Sem**  
**Microprocessors**  
**Subject Code: BTEE-503**  
**Paper ID:**

Time allowed: 3 Hrs

Important Instructions:

Max Marks: 60

All questions are **Compulsory**. Assume any missing data.

**Section-A (2\*10=20)**

Q1. Write briefly:

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|---|-----|
| (a) State the function of READY pin in 8085.                    | CO2 |
| (b) Differentiate between memory mapped I/O and I/O mapped I/O. | CO2 |
| (c) What do you mean by stack and subroutine?                   | CO2 |
| (d) Differentiate between PUSH and POP instruction.             | CO3 |
| (e) What is the conversion time in A/D converter?               | CO4 |
| (f) Differentiate between MOV and MVI instruction.              | CO3 |
| (g) Define Microprocessor.                                      | CO1 |
| (h) What is Programmable peripheral interface?                  | CO4 |
| (i) How many memory locations can be addressed by 8085?         | CO1 |
| (j) What do you mean by Macros?                                 | CO3 |

**Section-B (5\*4=20)**

Q2. Describe the functioning of 8086 in maximum mode. CO2

**OR**

With the help of neat diagram, explain the memory segmentation and its advantages in 8086. CO2

Q3. Draw the pin diagram of 8085 microprocessor and discuss function of each pin. CO2

**OR**

Explain the function of each block of internal architecture of 8085 with the help of neat diagram. CO2

Q4. Write a program to subtract two 8-bit numbers 48H and 20H stored at D001H and D002H memory locations respectively. CO3

**OR**

How would you categorize various addressing modes supported by 8086 microprocessor giving one example of each? CO3

Q5. Differentiate 32 bit Microprocessor with 64 microprocessors. CO1

**OR**

Discuss the evolution of computers. CO1

Q6. Discuss the various software and hardware interrupts of 8086. CO4

**OR**

Describe the interfacing of 8086 with keyboard with the help of program and diagram. CO4

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