SHAHEED BHAGAT SINGH STATE TECHNICAL CAMPUS, FEROZEPUR ROLL No: Total number of pages:[] Total number of questions:06 B.C.A. 3rd Sem Introduction to Microprocessor Subject Code: BCAP1-356 Paper ID: Time allowed: 3 Hrs Max Marks:100 Important Instructions: All questions are compulsory PART A (2×10) Q. 1. Short-Answer Questions: (a) Define opcode and operand. (b) What is the need of demultiplexing the bus? (c) What is accumulator? Explain its role in 8085 microprocessor? (d) What is the difference between 8085 and 8086? (e) What are the essential elements of CPU? (f)Differentiate between memory mapped and program controlled I/O. (g) What are nibble, byte and word? (h) Define T states? (i) What are the different types of data transfer operations possible? (j) What are the various types of memories used in microcontroller? **PART B (16×5)** a) What are the various groups of instruction in 8085? Discuss briefly each 12 0.2. group by giving two examples of each group? b) What is the operation performed by the following instructions? 04 a) CMP M b)XCHG c) PUSH PSW d) SHLD 5000H OR a) Write an assembly language program using 8085 microprocessor instruction set 10 to arrange N numbers in ascending order? 06 b) Discuss any five branch instructions in 8085 microprocessor?

Q. 3. a) What is microprocessor? Explain in detail the evolution of microprocessor? Discuss various applications of microprocessor? b) Differentiate between microprocessor?	89
b) Differentiate between microprocessor? b) Differentiate between microprocessor and microcontroller.	12
	04
OR	
a) What do you mean by bus? Explain the bus structure of a microprocessor b) Draw and discuss the pin description of 8085?	08
and a) What is the in the control of	08
Q. 4. a) What is timing diagram? Discuss in detail the timing diagram of memory readb) Explain instruction cycle, machine cycle and state.	10
	06
OR	
a) Discuss fetch and execution and a second	
a) Discuss fetch and execution cycles of 8085 by considering any instruction? b) Explain the concept of Machine cycle, instruction cycle and T-state.	08
Q. 5. a)Draw the block diagram showing the memory interfacing with a microprocessor and mention the purpose of each and every signal used therein for interfacing b) What are various types of POMs 3 Diagram is a little of the purpose of the purpose of POMs 3 Diagram is a little of the purpose of the purpos	08
b) What are various types of ROMs? Discuss in detail	08
OR	
a)Differentiate between following	
i) RAM and ROM ii) EPROM and E ² PROM	06
b) Discuss in detail microprocessor operations?	10
a) What are the various schemes of I/O data transfer? Diames in 1, 1	
a) What are the various schemes of I/O data transfer? Discuss in detailb) Discuss two types of address schemes provided by 8085 for I/O ports?	10 06
OR	
a) Explain in detail with circuit diagram the interfacing of microprocessor with any	7 10
device.	06
b) Write short note on 8086,80386 and 80486	