SHAF	IEED BHAGAT SINGH ST	ATE TECHNICAL CAMPUS, FEROZEPU	JR -
ROLL Total o	No: Use I will be a substitute of questions:06	Total number of page	s:[2]
	B.Te	ech. EE 5 ^{thSem}	
	POWE	RELECTRONICS (PP)	
100			
	Subjec	t Code: BTEE-504	
		Paper ID: M)18	
	allowed: 3 Hrs tant Instructions:	(2011-2014 both) Max Marks	: 60
	All questions are compulsory		
	Assume any missing data		
		PART A (2×10)	
Q. 1.	Short-Answer Questions:	All	COs
	(a) Define latching and holding		
	(b) What are the types of cyclo		
	(c)Draw the circuit symbol and (d)Write the applications of G		
	(e) What are A.C links choppe		
	(f)What is main drawback of I	half bridge inverters and how can overcome from i	t?
	(g)Discuss the conditions whi	ch must be satisfied for turning on SCR with a gate	2
	Signal.	ous of whose controlled rectifier?	
		ons of phase controlled rectifier? a conducting thyristor turned off in load condition	is?
		in which current source inverter can use?	
		PART B (5×8)	
Q. 2.	What is pulse-width modulati	on? Explain the various PWM technique? OR	COc
	Explain voltage controlled co	mmutation technique?	
Q. 3.	Describe the basic principle the output voltage?	of chopper and explain the strategies for control	COc
	Describe class C type of com	OR	
	current and voltage waveform	mutation used for thyristor with appropriate n?	
Q. 4.	a) Explain LASCRGive its b) Compare SITH with a GT	O?	COa
	Explain various techniques f	OR or the thyristor protection?	
	Dapiani various teeninques i	or the infinited protection:	
0.5	Describe the basic circuit and	d principle of operation of dual converter?	co-