

SHAHEED BHAGAT SINGH STATE TECHNICAL CAMPUS, FEROZEPUR

ROLL No:

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Total number of pages:[1]

Total number of questions:06

B.C.A. 1st Sem

Digital Electronics

(RP)

Subject Code: BCAP1-103

Paper ID: M/18 (for office use)

(2016 batch onwards)

Time allowed: 3 Hrs

Max Marks: 60

To be specified by paper setter

Important Instructions:

- All questions are compulsory
- Assume any missing data

PART A (2×10)

All COs

Q. 1. Short-Answer Questions:

- What is Number System?
- What are Universal Gates?
- Discuss Overflow and Underflow conditions?
- Differentiate between ROM and RAM?
- What do you mean by integer and floating point representation?
- Explain Grey code?
- Discuss Boolean Algebra?
- Explain Truth Table?
- What do you mean by Flip Flop?
- What do you mean by Binary Adder?

PART B (8×5)

Q. 2. What do you mean by Number System? Explain?

COa

OR

Differentiate between 1's Complement and 2's Complement methods?

COa

Q. 3. Differentiate between SOP and POS?

COb

OR

What do you mean by Karnaugh Maps? Discuss

COb

Q. 4. Explain Multiplexer and Demultiplexer in details?

COc

OR

Discuss encoder and Decoder?

COc

Q. 5. What do you mean by Flip-Flop? Explain J-K flip flop?

COd

OR

What do you mean by Register? Explain its types?

COd

Q. 6. Differentiate between Static and Dynamic memory?

COe

OR

Explain Read and Write Control Timing Diagrams for ICs?

COe