## SHAHEED BHAGAT SINGH STATE TECHNICAL CAMPUS, FEROZEPUR ROLL No: Total number of pages:[2] B.Tech. || EE || 3<sup>rd</sup> SEM. **Electronic Devices & Circuits** Subject Code:BTEE-304 Paper ID: Time allowed: 3 Hrs Max Marks: 60 Important Instructions: All questions are compulsory Assume any missing data Additional instructions, if any PART A (10x 2marks) Q. 1. Short-Answer Questions: a) What do you mean by biasing? b) Give any two advantages of FETs over BJTs. c) How the Tunnel Diode is different from an ordinary diode? d) What do you mean by electronic clipper? e) Why base of transistor is lightly doped? What do you mean by IC? f) g) Give applications of OPAMP. h) Why positive feedback is used in oscillators? What do you mean by active filter? Why transistor is called a current controlled device? PART B (5×8marks)

Discuss the working of bridge type full wave rectifier.	CO1
Draw and discuss the transfer characteristics and drain characteristics of JFET.	CO3
OR	
How OPAMP can be used as integrating amplifier explain with suitable circuit	
diagram.	
Discuss the voltage divider bias circuit for transistor biasing.	CO2
	Draw and discuss the transfer characteristics and drain characteristics of JFET.  Discuss Schmitt trigger circuit using OPAMP.  OR  How OPAMP can be used as integrating amplifier explain with suitable circuit