SHAHEED BHAGAT SINGH STATE TECHNICA	AL CAMPUS, FEROZZII
ROLL No:	Total number of pages: [2] Total number of Questions: 06
B.Tech.    ECE    3 <sup>rd</sup> Sen	nester
Digital Circuit & Logi	c Design
Subject Code: BTEC	-302
Paper ID:	Max Marks: 60
Time Allowed: 3 Hrs Important Instructions:	Max Marks.
<ul> <li>All questions are compulsory</li> <li>Assume any missing data</li> </ul>	
Q. 1. Short-Answer Questions: PART A (2×10)	All COs
(a) State De-Morgan's Theorem?	
(b) What do you know about signed and unsigned b	oinary numbers?
(c) What is difference between PAL and PLA?	
(d) What are the limitations of Asynchronous Coun	iters?
(e) What is a flip-flop? Why D Flip-flop is so calle	ed?
(f) What is a shift register? Give its types?	
(g) What is need of ADC and DAC?	
(h) What are the advantages of CMOS over DTL?	
(i) What is Fan in and Fan out?	
(j) Compute the result of (DF)16 + (AC)16 = (	)16?
PART B (8×5)	
2. i) What are different binary codes? Discuss above examples? ii) If X=(1101.1010)2 and Y= (100.1011)2; evaluation	
X+ Y= and X-Y=	

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i) Subtrac	OR et two numbers (75)10- (15)10 using 2's complement method	CO1
of culati	raction? t (A6F.CD) 16 and (36.125)10 into octal numbers?	
Q. 3. Minimize the follologic circuit:	owing using K-Map technique and also implement it into a	CO2
Y(A,B,0	$C,D,) = \sum_{i=1}^{n} m(1,2,9,10,11,14,15)$	
Explain why NAN you justify your ans	OR D and NOR gates are called Universal gates? How would swer?	CO2
Q. 4. What is a Multiplex	er? Design a 4:1 Multiplexer using only NAND gates? OR	CO3
What is a Code Con	verter? Design a binary to Gray Code Converter?	CO3
	n classification of semiconductor memories?  OR	CO4
What are different ty using T Flip-flops?	ypes of counters? Design an asynchronous decade counter	CO4
operation of Successi	OR OR	CO4
What are the differen	t Logic families? Explain CMOS and its various types?	CO4

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