## 5.5B Configurations for DC

For an NR DC configuration specified in Table 5.5B-1, the bandwidth combination sets for the corresponding NR CA configuration in 5.5A.3, i.e., dual uplink inter-band carrier aggregation with uplink assigned to two NR bands, are applicable to Dual Connectivity.

Table 5.5B-1: Inter-band NR DC configurations (two bands)

| NR DC  configuration | Uplink NR DC  configuration |
| --- | --- |
| DC\_n1A-n3A | DC\_n1A-n3A |
| DC\_n1A-n7A | DC\_n1A-n7A |
| DC\_n1A-n20A | DC\_n1A-n20A |
| DC\_n1A-n28A | DC\_n1A-n28A |
| DC\_n1A-n41A | DC\_n1A-n41A |
| DC\_n1A-n46A  DC\_n1A-n46C  DC\_n1A-n46D | DC\_n1A-n46A |
| DC\_n1A-n46(2A) | DC\_n1A-n46A |
| DC\_n1A-n77A2 | DC\_n1A-n77A |
| DC\_n1A-n78A | DC\_n1A-n78A |
| DC\_n1A-n78(2A) | DC\_n1A-n78A |
| DC\_n1A-n79A2 | DC\_n1A-n79A |
| DC\_n1A-n102A  DC\_n1A-n102B  DC\_n1A-n102C  DC\_n1A-n102D  DC\_n1A-n102E | DC\_n1A-n102A  DC\_n1A-n102B  DC\_n1A-n102C |
| DC\_n1A-n102(2A) | DC\_n1A-n102A |
| DC\_n2A-n5A  DC\_n2A-n5B | DC\_n2A-n5A |
| DC\_n2A-n48A  DC\_n2A-n48B  DC\_n2A-n48C | DC\_n2A-n48A |
| DC\_n2A-n48(2A)  DC\_n2A-n48(A-B) | DC\_n2A-n48A |
| DC\_n2A-n66A  DC\_n2A-n66B | DC\_n2A-n66A |
| DC\_n2A-n77A  DC\_n2A-n77B  DC\_n2A-n77C | DC\_n2A-n77A |
| DC\_n2A-n77(2A)  DC\_n2A-n77(3A)  DC\_n2(2A)-n77A  DC\_n2(2A)-n77B  DC\_n2(2A)-n77(2A)  DC\_n2(2A)-n77C | DC\_n2A-n77A |
| DC\_n3A-n7A | DC\_n3A-n7A |
| DC\_n3A-n20A | DC\_n3A-n20A |
| DC\_n3A-n28A | DC\_n3A-n28A |
| DC\_n3A-n41A | DC\_n3A-n41A |
| DC\_n3A-n77A2 | DC\_n3A-n77A |
| DC\_n3A-n77(2A) 2 | DC\_n3A-n77A |
| DC\_n3A-n78A2 | DC\_n3A-n78A |
| DC\_n3A-n78(2A)2 | DC\_n3A-n78A |
| DC\_n3A-n79A | DC\_n3A-n79A |
| DC\_n3A-n102A  DC\_n3A-n102B  DC\_n3A-n102C  DC\_n3A-n102D  DC\_n3A-n102E | DC\_n3A-n102A  DC\_n3A-n102B  DC\_n3A-n102C |
| DC\_n3A-n102(2A) | DC\_n3A-n102A |
| DC\_n5A-n13A  DC\_n5B-n13A | DC\_n5A-n13A |
| DC\_n5A-n48A  DC\_n5A-n48B  DC\_n5A-n48C | DC\_n5A-n48A |
| DC\_n5A-n48(2A) | DC\_n5A-n48A |
| DC\_n5A-n66A  DC\_n5A-n66B  DC\_n5B-n66A  DC\_n5B-n66B | DC\_n5A-n66A |
| DC\_n5A-n66(2A)  DC\_n5A-n66(3A)  DC\_n5B-n66(2A) | DC\_n5A-n66A |
| DC\_n5A-n77A  DC\_n5A-n77B  DC\_n5A-n77C  DC\_n5B-n77A  DC\_n5B-n77C | DC\_n5A-n77A |
| DC\_n5A-n77(2A)  DC\_n5A-n77(3A)  DC\_n5(2A)-n77A  DC\_n5(2A)-n77C | DC\_n5A-n77A |
| DC\_n7A-n20A | DC\_n7A-n20A |
| DC\_n7A-n28A | DC\_n7A-n28A |
| DC\_n7A-n46A  DC\_n7A-n46C  DC\_n7A-n46D | DC\_n7A-n46A |
| DC\_n7A-n46(2A) | DC\_n7A-n46A |
| DC\_n7A-n78A | DC\_n7A-n78A |
| DC\_n7A-n78(2A) | DC\_n7A-n78A |
| DC\_n7A-n102A  DC\_n7A-n102B  DC\_n7A-n102C  DC\_n7A-n102D  DC\_n7A-n102E | DC\_n7A-n102A  DC\_n7A-n102B  DC\_n7A-n102C |
| DC\_n7A-n102(2A) | DC\_n7A-n102A |
| DC\_n8A-n78A2 | DC\_n8A-n78A |
| DC\_n12A-n77A | DC\_n12A-n77A |
| DC\_n12A-n77(2A) | DC\_n12A-n77A |
| DC\_n13A-n66A  DC\_n13A-n66B | DC\_n13A-n66A |
| DC\_n13A-n66(2A) | DC\_n13A-n66A |
| DC\_n13A-n77A  DC\_n13A-n77C | DC\_n13A-n77A |
| DC\_n20A-n78A | DC\_n20A-n78A |
| DC\_n20A-n78(2A) | DC\_n20A-n78A |
| DC\_n25A-n41A | DC\_n25A-n41A |
| DC\_n25A-n66A | DC\_n25A-n66A |
| DC\_n25A-n71A | DC\_n25A-n71A |
| DC\_n25A-n77A | DC\_n25A-n77A |
| DC\_n25A-n77(2A) | DC\_n25A-n77A |
| DC\_n28A-n41A | DC\_n28A-n41A |
| DC\_n28A-n46A  DC\_n28A-n46C  DC\_n28A-n46D | DC\_n28A-n46A |
| DC\_n28A-n46(2A) | DC\_n28A-n46A |
| DC\_n28A-n77A2 | DC\_n28A-n77A |
| DC\_n28A-n77(2A) | DC\_n28A-n77A |
| DC\_n28A-n78A2 | DC\_n28A-n78A |
| DC\_n28A-n78(2A)2 | DC\_n28A-n78A |
| DC\_n28A-n79A | DC\_n28A-n79A |
| DC\_n28A-n102A  DC\_n28A-n102B  DC\_n28A-n102C  DC\_n28A-n102D  DC\_n28A-n102E | DC\_n28A-n102A  DC\_n28A-n102B  DC\_n28A-n102C |
| DC\_n28A-n102(2A) | DC\_n28A-n102A |
| DC\_n41A-n66A | DC\_n41A-n66A |
| DC\_n41A-n71A | DC\_n41A-n71A |
| DC\_n41A-n77A | DC\_n41A-n77A |
| DC\_n41A-n78A | DC\_n41A-n78A |
| DC\_n41A-n79A2, 3 | DC\_n41A-n79A |
| DC\_n46A-n48A  DC\_n46A-n48B  DC\_n46A-n48C  DC\_n46B-n48A  DC\_n46B-n48B  DC\_n46B-n48C  DC\_n46C-n48A  DC\_n46C-n48B  DC\_n46C-n48C  DC\_n46D-n48A  DC\_n46D-n48B  DC\_n46D-n48C  DC\_n46N-n48A  DC\_n46N-n48B  DC\_n46N-n48C | DC\_n46A-n48A  DC\_n46A-n48B |
| DC\_n46A-n77A  DC\_n46C-n77A  DC\_n46D-n77A | DC\_n46A-n77A |
| DC\_n46A-n77(2A)  DC\_n46C-n77(2A)  DC\_n46D-n77(2A)  DC\_n46(2A)-n77A  DC\_n46(2A)-n77(2A) | DC\_n46A-n77A |
| DC\_n46A-n78A  DC\_n46C-n78A  DC\_n46D-n78A | DC\_n46A-n78A |
| DC\_n46A-n78(2A)  DC\_n46C-n78(2A)  DC\_n46D-n78(2A)  DC\_n46(2A)-n78A  DC\_n46(2A)-n78(2A) | DC\_n46A-n78A |
| DC\_n48A-n66A  DC\_n48B-n66A  DC\_n48B-n66B  DC\_n48C-n66A  DC\_n48C-n66B | DC\_n48A-n66A |
| DC\_n48A-n66(2A)  DC\_n48B-n66(2A)  DC\_n48(2A)-n66A  DC\_n48(2A)-n66(2A) | DC\_n48A-n66A |
| DC\_n48A-n70A  DC\_n48B-n70A | DC\_n48A-n70A |
| DC\_n48(2A)-n70A | DC\_n48A-n70A |
| DC\_n48A-n71A  DC\_n48B-n71A  DC\_n48C-n71A | DC\_n48A-n71A |
| DC\_n48A-n71(2A)  DC\_n48(2A)-n71A  DC\_n48(2A)-n71(2A)  DC\_n48(3A)-n71A  DC\_n48(4A)-n71A  DC\_n48B-n71(2A) | DC\_n48A-n71A |
| DC\_n48A-n96A  DC\_n48B-n96A  DC\_n48C-n96A  DC\_n48A-n96B  DC\_n48B-n96B  DC\_n48C-n96B  DC\_n48A-n96C  DC\_n48B-n96C  DC\_n48C-n96C  DC\_n48A-n96D  DC\_n48B-n96D  DC\_n48C-n96D  DC\_n48A-n96E  DC\_n48B-n96E  DC\_n48C-n96E | DC\_n48A-n96A DC\_n48B-n96A |
| DC\_n66A-n71A | DC\_n66A-n71A |
| DC\_n66A-n77A  DC\_n66A-n77B  DC\_n66A-n77C  DC\_n66B-n77A  DC\_n66B-n77C | DC\_n66A-n77A |
| DC\_n66A-n77(2A)  DC\_n66A-n77(3A)  DC\_n66(2A)-n77(2A)  DC\_n66(2A)-n77(3A)  DC\_n66(3A)-n77(2A)  DC\_n66(2A)-n77A  DC\_n66(2A)-n77B  DC\_n66(2A)-n77C  DC\_n66(3A)-n77A  DC\_n66(3A)-n77C | DC\_n66A-n77A |
| DC\_n71A-n77A | DC\_n71A-n77A |
| DC\_n71A-n77(2A) | DC\_n71A-n77A |
| DC\_n77A-n79A1 | DC\_n77A-n79A |
| DC\_n77(2A)-n79A1 | DC\_n77A-n79A |
| DC\_n78A-n79A | DC\_n78A-n79A |
| DC\_n78(2A)-n79A | DC\_n78A-n79A |
| DC\_n77A-n102A  DC\_n77A-n102B  DC\_n77A-n102C  DC\_n77A-n102D  DC\_n77A-n102E | DC\_n77A-n102A  DC\_n77A-n102B  DC\_n77A-n102C |
| DC\_n77A-n102(2A)  DC\_n77(2A)-n102A  DC\_n77(2A)-n102B  DC\_n77(2A)-n102C  DC\_n77(2A)-n102D  DC\_n77(2A)-n102E  DC\_n77(2A)-n102(2A) | DC\_n77A-n102A  DC\_n77A-n102B  DC\_n77A-n102C |
| DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C  DC\_n78A-n102D  DC\_n78A-n102E | DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| DC\_n78A-n102(2A)  DC\_n78(2A)-n102A  DC\_n78(2A)-n102B  DC\_n78(2A)-n102C  DC\_n78(2A)-n102D  DC\_n78(2A)-n102E  DC\_n78(2A)-n102(2A) | DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| NOTE 1: The minimum requirements apply only when there is non-simultaneous Rx/Tx operation between n77-n79 NR carriers. This restriction applies also for these carriers when applicable NR DC configuration is part of a higher order configuration.  NOTE 2: Applicable for UE supporting inter-band NR DC with mandatory simultaneous Rx/Tx capability.  NOTE 3: The frequency range below 2506 MHz for Band n41 is not used in this combination. | |

Table 5.5B-2: Inter-band NR DC configurations (three bands)

| NR DC  configuration | Uplink NR DC  configuration |
| --- | --- |
| DC\_n1A-n3A-n7A | DC\_n1A-n3A  DC\_n1A-n7A  DC\_n3A-n7A |
| DC\_n1A-n3A-n20A | DC\_n1A-n3A  DC\_n1A-n20A  DC\_n3A-n20A |
| DC\_n1A-n3A-n28A | DC\_n1A-n3A  DC\_n1A-n28A  DC\_n3A-n28A |
| DC\_n1A-n3A-n41A | DC\_n1A-n3A  DC\_n1A-n41A  DC\_n3A-n41A |
| DC\_n1A-n3A-n67A | DC\_n1A-n3A |
| DC\_n1A-n3A-n77A | DC\_n1A-n3A  DC\_n3A-n77A  DC\_n1A-n77A |
| DC\_n1A-n3A-n78A | DC\_n1A-n3A  DC\_n3A-n78A  DC\_n1A-n78A |
| DC\_n1A-n3A-n78(2A) | DC\_n1A-n3A  DC\_n3A-n78A  DC\_n1A-n78A |
| DC\_n1A-n3A-n79A | DC\_n1A-n3A  DC\_n3A-n79A  DC\_n1A-n79A |
| DC\_n1A-n7A-n28A | DC\_n1A-n7A  DC\_n7A-n28A  DC\_n1A-n28A |
| DC\_n1A-n7A-n67A | DC\_n1A-n7A |
| DC\_n1A-n7A-n78A | DC\_n1A-n7A  DC\_n7A-n78A  DC\_n1A-n78A |
| DC\_n1A-n7A-n78(2A) | DC\_n1A-n7A  DC\_n7A-n78A  DC\_n1A-n78A |
| DC\_n1A-n20A-n67A | DC\_n1A-n20A |
| DC\_n1A-n28A-n41A | DC\_n1A-n28A  DC\_n1A-n41A  DC\_n28A-n41A |
| DC\_n1A-n28A-n46A  DC\_n1A-n28A-n46C  DC\_n1A-n28A-n46D | DC\_n1A-n46A  DC\_n28A-n46A |
| DC\_n1A-n28A-n46(2A) | DC\_n1A-n46A  DC\_n28A-n46A |
| DC\_n1A-n28A-n77A | DC\_n1A-n28A  DC\_n1A-n77A  DC\_n28A-n77A |
| DC\_n1A-n28A-n78A | DC\_n1A-n28A  DC\_n1A-n78A  DC\_n28A-n78A |
| DC\_n1A-n28A-n78(2A) | DC\_n1A-n28A  DC\_n1A-n78A  DC\_n28A-n78A |
| DC\_n1A-n28A-n79A | DC\_n1A-n28A  DC\_n1A-n79A  DC\_n28A-n79A |
| DC\_n1A-n28A-n102A  DC\_n1A-n28A-n102B  DC\_n1A-n28A-n102C  DC\_n1A-n28A-n102D  DC\_n1A-n28A-n102E | DC\_n1A-n102A  DC\_n1A-n102B  DC\_n1A-n102C  DC\_n28A-n102A  DC\_n28A-n102B  DC\_n28A-n102C |
| DC\_n1A-n28A-n102(2A) | DC\_n1A-n102A  DC\_n28A-n102A |
| DC\_n1A-n41A-n77A | DC\_n1A-n41A  DC\_n1A-n77A  DC\_n41A-n77A |
| DC\_n1A-n41A-n79A | DC\_n1A-n41A  DC\_n1A-n79A  DC\_n41A-n79A |
| DC\_n1A-n46A-n78A  DC\_n1A-n46C-n78A  DC\_n1A-n46D-n78A | DC\_n1A-n46A  DC\_n1A-n78A  DC\_n46A-n78A |
| DC\_n1A-n46A-n78(2A)  DC\_n1A-n46C-n78(2A)  DC\_n1A-n46D-n78(2A)  DC\_n1A-n46(2A)-n78A  DC\_n1A-n46(2A)-n78(2A) | DC\_n1A-n46A  DC\_n1A-n78A  DC\_n46A-n78A |
| DC\_n1A-n67A-n78A | DC\_n1A-n78A |
| DC\_n1A-n67A-n78(2A) | DC\_n1A-n78A |
| DC\_n1A-n77A-n79A | DC\_n1A-n77A  DC\_n1A-n79A  DC\_n77A-n79A |
| DC\_n1A-n78A-n102A  DC\_n1A-n78A-n102B  DC\_n1A-n78A-n102C  DC\_n1A-n78A-n102D  DC\_n1A-n78A-n102E | DC\_n1A-n78A  DC\_n1A-n102A  DC\_n1A-n102B  DC\_n1A-n102C  DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| DC\_n1A-n78(2A)-n102A  DC\_n1A-n78(2A)-n102B  DC\_n1A-n78(2A)-n102C  DC\_n1A-n78(2A)-n102D  DC\_n1A-n78(2A)-n102E  DC\_n1A-n78A-n102(2A)  DC\_n1A-n78(2A)-n102(2A) | DC\_n1A-n78A  DC\_n1A-n102A  DC\_n1A-n102B  DC\_n1A-n102C  DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| DC\_n2A-n5A-n48A  DC\_n2A-n5A-n48B | DC\_n2A-n5A  DC\_n2A-n48A  DC\_n5A-n48A |
| DC\_n2A-n5A-n48(2A)  DC\_n2A-n5A-n48(A-B) | DC\_n2A-n5A  DC\_n2A-n48A  DC\_n5A-n48A |
| DC\_n2A-n5A-n66A | DC\_n2A-n5A  DC\_n2A-n66A  DC\_n5A-n66A |
| DC\_n2A-n5A-n66(2A)  DC\_n2A-n5A-n66(3A)  DC\_n2(2A)-n5A-n66A  DC\_n2(2A)-n5A-n66(2A) | DC\_n2A-n5A  DC\_n2A-n66A  DC\_n5A-n66A |
| DC\_n2A-n5A-n77A  DC\_n2A-n5A-n77C | DC\_n2A-n5A  DC\_n2A-n77A  DC\_n5A-n77A |
| DC\_n2A-n5A-n77(2A)  DC\_n2(2A)-n5A-n77A  DC\_n2(2A)-n5A-n77(2A) | DC\_n2A-n5A  DC\_n2A-n77A  DC\_n5A-n77A |
| DC\_n2A-n48A-n66A  DC\_n2A-n48B-n66A | DC\_n2A-n48A  DC\_n2A-n66A  DC\_n48A-n66A |
| DC\_n2A-n48(2A)-n66A  DC\_n2A-n48(A-B)-n66A | DC\_n2A-n48A  DC\_n2A-n66A  DC\_n48A-n66A |
| DC\_n2A-n48A-n77A  DC\_n2A-n48A-n77C  DC\_n2A-n48B-n77A  DC\_n2A-n48B-n77C | DC\_n2A-n48A  DC\_n2A-n77A |
| DC\_n2A-n48(2A)-n77A  DC\_n2A-n48(2A)-n77C | DC\_n2A-n48A  DC\_n2A-n77A |
| DC\_n2A-n66A-n77A  DC\_n2A-n66A-n77C | DA\_n2A-n66A  DA\_n2A-n77A  DA\_n66A-n77A |
| DC\_n2(2A)-n66A-n77A  DC\_n2A-n66(2A)-n77A  DC\_n2A-n66A-n77(2A)  DC\_n2(2A)-n66A-n77(2A)  DC\_n2A-n66(2A)-n77(2A)  DC\_n2(2A)-n66(2A)-n77A  DC\_n2(2A)-n66(2A)-n77(2A)  DC\_n2A-n66(3A)-n77A  DC\_n2A-n66(3A)-n77(2A) | DC\_n2A-n66A  DC\_n2A-n77A  DC\_n66A-n77A |
| DC\_n3A-n7A-n20A | DC\_n3A-n7A  DC\_n3A-n20A  DC\_n7A-n20A |
| DC\_n3A-n7A-n28A | DC\_n3A-n7A DC\_n3A-n28A DC\_n7A-n28A |
| DC\_n3A-n7A-n67A | DC\_n3A-n7A |
| DC\_n3A-n7A-n78A | DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n3A-n7A-n78(2A) | DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n3A-n20A-n67A | DC\_n3A-n20A |
| DC\_n3A-n20A-n78A | DC\_n3A-n20A  DC\_n3A-n78A  DC\_n20A-n78A |
| DC\_n3A-n20A-n78(2A) | DC\_n3A-n20A  DC\_n3A-n78A  DC\_n20A-n78A |
| DC\_n3A-n28A-n41A | DC\_n3A-n28A  DC\_n3A-n41A  DC\_n28A-n41A |
| DC\_n3A-n28A-n77A | DC\_n3A-n28A  DC\_n3A-n77A  DC\_n28A-n77A |
| DC\_n3A-n28A-n77(2A) | DC\_n3A-n28A  DC\_n3A-n77A  DC\_n28A-n77A |
| DC\_n3A-n28A-n78A | DC\_n3A-n28A  DC\_n3A-n78A  DC\_n28A-n78A |
| DC\_n3A-n28A-n78(2A) | DC\_n3A-n28A  DC\_n3A-n78A  DC\_n28A-n78A |
| DC\_n3A-n28A-n79A | DC\_n3A-n28A  DC\_n3A-n79A  DC\_n28A-n79A |
| DC\_n3A-n41A-n77A | DC\_n3A-n41A  DC\_n3A-n77A  DC\_n41A-n77A |
| DC\_n3A-n41A-n79A | DC\_n3A-n41A  DC\_n3A-n79A  DC\_n41A-n79A |
| DC\_n3A-n67A-n78A | DC\_n3A-n78A |
| DC\_n3A-n67A-n78(2A) | DC\_n3A-n78A |
| DC\_n3A-n77A-n79A | DC\_n3A-n77A  DC\_n3A-n79A  DC\_n77A-n79A |
| DC\_n3A-n77(2A)-n79A | DC\_n3A-n77A  DC\_n3A-n79A  DC\_n77A-n79A |
| DC\_n5A-n48A-n66A  DC\_n5A-n48B-n66A | DC\_n5A-n48A  DC\_n5A-n66A  DC\_n48A-n66A |
| DC\_n5A-n48(2A)-n66A  DC\_n5A-n48(A-B)-n66A | DC\_n5A-n48A  DC\_n5A-n66A  DC\_n48A-n66A |
| DC\_n5A-n48A-n77A  DC\_n5A-n48A-n77C  DC\_n5A-n48B-n77A  DC\_n5A-n48B-n77C | DC\_n5A-n48A  DC\_n5A-n77A |
| DC\_n5A-n48(2A)-n77A  DC\_n5A-n48(2A)-n77C | DC\_n5A-n48A  DC\_n5A-n77A |
| DC\_n5A-n66A-n77A  DC\_n5A-n66A-n77C | DC\_n5A-n66A  DC\_n5A-n77A  DC\_n66A-n77A |
| DC\_n5A-n66(2A)-n77A  DC\_n5A-n66A-n77(2A)  DC\_n5A-n66(2A)-n77(2A)  DC\_n5A-n66(3A)-n77A  DC\_n5A-n66(3A)-n77(2A)  DC\_n5A-n66A-n77(3A) | DC\_n5A-n66A  DC\_n5A-n77A  DC\_n66A-n77A |
| DC\_n7A-n20A-n67A | DC\_n7A-n20A |
| DC\_n7A-n20A-n78A | DC\_n7A-n20A DC\_n7A-n78A DC\_n20A-n78A |
| DC\_n7A-n20A-n78(2A) | DC\_n7A-n20A DC\_n7A-n78A DC\_n20A-n78A |
| DC\_n7A-n28A-n78A | DC\_n7A-n28A  DC\_n7A-n78A  DC\_n28A-n78A |
| DC\_n7A-n28A-n78(2A) | DC\_n7A-n28A  DC\_n7A-n78A  DC\_n28A-n78A |
| DC\_n7A-n46A-n78A  DC\_n7A-n46C-n78A  DC\_n7A-n46D-n78A | DC\_n7A-n46A  DC\_n7A-n78A  DC\_n46A-n78A |
| DC\_n7A-n46(2A)-n78A  DC\_n7A-n46(2A)-n78(2A)  DC\_n7A-n46A-n78(2A)  DC\_n7A-n46C-n78(2A)  DC\_n7A-n46D-n78(2A) | DC\_n7A-n46A  DC\_n7A-n78A  DC\_n46A-n78A |
| DC\_n7A-n67A-n78A | DC\_n7A-n78A |
| DC\_n7A-n67A-n78(2A) | DC\_n7A-n78A |
| DC\_n7A-n78A-n102A  DC\_n7A-n78A-n102B  DC\_n7A-n78A-n102C  DC\_n7A-n78A-n102D  DC\_n7A-n78A-n102E | DC\_n7A-n78A  DC\_n7A-n102A  DC\_n7A-n102B  DC\_n7A-n102C  DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| DC\_n7A-n78(2A)-n102A  DC\_n7A-n78(2A)-n102B  DC\_n7A-n78(2A)-n102C  DC\_n7A-n78(2A)-n102D  DC\_n7A-n78(2A)-n102E  DC\_n7A-n78A-n102(2A)  DC\_n7A-n78(2A)-n102(2A) | DC\_n7A-n78A  DC\_n7A-n102A  DC\_n7A-n102B  DC\_n7A-n102C  DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| DC\_n13A-n66A-n77A | DC\_n13A-n66A  DC\_n13A-n77A  DC\_n66A-n77A |
| DC\_n13A-n66A-n77(2A) | DC\_n13A-n66A  DC\_n13A-n77A  DC\_n66A-n77A |
| DC\_n20A-n67A-n78A | DC\_n20A-n78A |
| DC\_n20A-n67A-n78(2A) | DC\_n20A-n78A |
| DC\_n28A-n41A-n77A | DC\_n28A-n41A  DC\_n28A-n77A  DC\_n41A-n77A |
| DC\_n28A-n41A-n79A | DC\_n28A-n41A  DC\_n28A-n79A  DC\_n41A-n79A |
| DC\_n28A-n46A-n78A  DC\_n28A-n46C-n78A  DC\_n28A-n46D-n78A | DC\_n28A-n46A  DC\_n28A-n78A  DC\_n46A-n78A |
| DC\_n28A-n46(2A)-n78A  DC\_n28A-n46(2A)-n78(2A)  DC\_n28A-n46A-n78(2A)  DC\_n28A-n46C-n78(2A)  DC\_n28A-n46D-n78(2A) | DC\_n28A-n46A  DC\_n28A-n78A  DC\_n46A-n78A |
| DC\_n28A-n77A-n79A | DC\_n28A-n77A  DC\_n28A-n79A  DC\_n77A-n79A |
| DC\_n28A-n77(2A)-n79A | DC\_n28A-n77A  DC\_n28A-n79A  DC\_n77A-n79A |
| DC\_n28A-n78A-n102A  DC\_n28A-n78A-n102B  DC\_n28A-n78A-n102C  DC\_n28A-n78A-n102D  DC\_n28A-n78A-n102E | DC\_n28A-n78A  DC\_n28A-n102A  DC\_n28A-n102B  DC\_n28A-n102C  DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| DC\_n28A-n78(2A)-n102A  DC\_n28A-n78(2A)-n102B  DC\_n28A-n78(2A)-n102C  DC\_n28A-n78(2A)-n102D  DC\_n28A-n78(2A)-n102E  DC\_n28A-n78A-n102(2A)  DC\_n28A-n78(2A)-n102(2A) | DC\_n28A-n78A  DC\_n28A-n102A  DC\_n28A-n102B  DC\_n28A-n102C  DC\_n78A-n102A  DC\_n78A-n102B  DC\_n78A-n102C |
| DC\_n41A-n77A-n79A | DC\_n41A-n77A  DC\_n41A-n79A  DC\_n77A-n79A |
| DC\_n46A-n48A-n96A  DC\_n46B-n48A-n96A  DC\_n46C-n48A-n96A  DC\_n46D-n48A-n96A  DC\_n46M-n48A-n96A  DC\_n46N-n48A-n96A  DC\_n46A-n48B-n96A  DC\_n46B-n48B-n96A  DC\_n46C-n48B-n96A  DC\_n46D-n48B-n96A  DC\_n46M-n48B-n96A  DC\_n46N-n48B-n96A  DC\_n46A-n48C-n96A  DC\_n46B-n48C-n96A  DC\_n46C-n48C-n96A  DC\_n46D-n48C-n96A  DC\_n46M-n48C-n96A  DC\_n46N-n48C-n96A  DC\_n46A-n48A-n96B  DC\_n46B-n48A-n96B  DC\_n46C-n48A-n96B  DC\_n46D-n48A-n96B  DC\_n46M-n48A-n96B  DC\_n46N-n48A-n96B | DC\_n46A-n48A  DC\_n46A-n48B  DC\_n48A-n96A  DC\_n48B-n96A |
| DC\_n48A-n66A-n77A  DC\_n48A-n66A-n77C  DC\_n48B-n66A-n77A  DC\_n48B-n66A-n77C | DC\_n48A-n66A  DC\_n66A-n77A |
| DC\_n48(2A)-n66A-n77A  DC\_n48(2A)-n66A-n77C DC\_n48A-n66(2A)-n77A | DC\_n48A-n66A  DC\_n66A-n77A |

Table 5.5B-3: Inter-band NR DC configurations (four bands)

| NR DC  configuration | Uplink NR DC  configuration |
| --- | --- |
| DC\_n1A-n3A-n7A-n67A | DC\_n1A-n3A  DC\_n1A-n7A  DC\_n3A-n7A |
| DC\_n1A-n3A-n7A-n78A | DC\_n1A-n3A  DC\_n1A-n7A  DC\_n1A-n78A  DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n1A-n3A-n7A-n78(2A) | DC\_n1A-n3A  DC\_n1A-n7A  DC\_n1A-n78A  DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n1A-n3A-n20A-n67A | DC\_n1A-n3A  DC\_n1A-n20A  DC\_n3A-n20A |
| DC\_n1A-n3A-n28A-n41A | DC\_n1A-n3A  DC\_n1A-n28A  DC\_n1A-n41A  DC\_n3A-n28A  DC\_n3A-n41A  DC\_n28A-n41A |
| DC\_n1A-n3A-n28A-n77A | DC\_n1A-n3A  DC\_n1A-n28A  DC\_n1A-n77A  DC\_n3A-n28A  DC\_n3A-n77A  DC\_n28A-n77A |
| DC\_n1A-n3A-n28A-n79A | DC\_n1A-n3A  DC\_n1A-n28A  DC\_n1A-n79A  DC\_n3A-n28A  DC\_n3A-n79A  DC\_n28A-n79A |
| DC\_n1A-n3A-n41A-n77A | DC\_n1A-n3A  DC\_n1A-n41A  DC\_n1A-n77A  DC\_n3A-n41A  DC\_n3A-n77A  DC\_n41A-n77A |
| DC\_n1A-n3A-n41A-n79A | DC\_n1A-n3A  DC\_n1A-n41A  DC\_n1A-n79A  DC\_n3A-n41A  DC\_n3A-n79A  DC\_n41A-n79A |
| DC\_n1A-n3A-n67A-n78A | DC\_n1A-n3A  DC\_n1A-n78A  DC\_n3A-n78A |
| DC\_n1A-n3A-n67A-n78(2A) | DC\_n1A-n3A  DC\_n1A-n78A  DC\_n3A-n78A |
| DC\_n1A-n3A-n77A-n79A | DC\_n1A-n3A  DC\_n1A-n77A  DC\_n1A-n79A  DC\_n3A-n77A  DC\_n3A-n79A  DC\_n77A-n79A |
| DC\_n1A-n7A-n28A-n78A | DC\_n1A-n7A DC\_n1A-n28A DC\_n1A-n78A DC\_n7A-n28A DC\_n7A-n78A DC\_n28A-n78A |
| DC\_n1A-n7A-n28A-n78(2A) | DC\_n1A-n7A DC\_n1A-n28A DC\_n1A-n78A DC\_n7A-n28A DC\_n7A-n78A DC\_n28A-n78A |
| DC\_n1A-n7A-n67A-n78A | DC\_n1A-n7A  DC\_n1A-n78A  DC\_n7A-n78A |
| DC\_n1A-n7A-n67A-n78(2A) | DC\_n1A-n7A  DC\_n1A-n78A  DC\_n7A-n78A |
| DC\_n1A-n28A-n41A-n77A | DC\_n1A-n28A  DC\_n1A-n41A  DC\_n1A-n77A  DC\_n28A-n41A  DC\_n28A-n77A  DC\_n41A-n77A |
| DC\_n1A-n28A-n41A-n79A | DC\_n1A-n28A  DC\_n1A-n41A  DC\_n1A-n79A  DC\_n28A-n41A  DC\_n28A-n79A  DC\_n41A-n79A |
| DC\_n1A-n28A-n77A-n79A | DC\_n1A-n28A  DC\_n1A-n77A  DC\_n1A-n79A  DC\_n28A-n77A  DC\_n28A-n79A  DC\_n77A-n79A |
| DC\_n1A-n41A-n77A-n79A | DC\_n1A-n41A  DC\_n1A-n77A  DC\_n1A-n79A  DC\_n41A-n77A  DC\_n41A-n79A  DC\_n77A-n79A |
| DC\_n3A-n7A-n20A-n67A | DC\_n3A-n7A  DC\_n3A-n20A  DC\_n7A-n20A |
| DC\_n3A-n7A-n20A-n78A | DC\_n3A-n7A  DC\_n3A-n20A  DC\_n3A-n78A  DC\_n7A-n20A  DC\_n7A-n78A  DC\_n20A-n78A |
| DC\_n3A-n7A-n20A-n78(2A) | DC\_n3A-n7A  DC\_n3A-n20A  DC\_n3A-n78A  DC\_n7A-n20A  DC\_n7A-n78A  DC\_n20A-n78A |
| DC\_n3A-n7A-n28A-n78A | DC\_n3A-n7A  DC\_n3A-n28A  DC\_n3A-n78A  DC\_n7A-n28A  DC\_n7A-n78A  DC\_n28A-n78A |
| DC\_n3A-n7A-n28A-n78(2A) | DC\_n3A-n7A  DC\_n3A-n28A  DC\_n3A-n78A  DC\_n7A-n28A  DC\_n7A-n78A  DC\_n28A-n78A |
| DC\_n3A-n7A-n67A-n78A | DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n3A-n7A-n67A-n78(2A) | DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n3A-n20A-n67A-n78A | DC\_n3A-n20A  DC\_n3A-n78A  DC\_n20A-n78A |
| DC\_n3A-n20A-n67A-n78(2A) | DC\_n3A-n20A  DC\_n3A-n78A  DC\_n20A-n78A |
| DC\_n3A-n28A-n41A-n77A | DC\_n3A-n28A  DC\_n3A-n41A  DC\_n3A-n77A  DC\_n28A-n41A  DC\_n28A-n77A  DC\_n41A-n77A |
| DC\_n3A-n28A-n41A-n79A | DC\_n3A-n28A  DC\_n3A-n41A  DC\_n3A-n79A  DC\_n28A-n41A  DC\_n28A-n79A  DC\_n41A-n79A |
| DC\_n3A-n28A-n77A-n79A | DC\_n3A-n28A  DC\_n3A-n77A  DC\_n3A-n79A  DC\_n28A-n77A  DC\_n28A-n79A  DC\_n77A-n79A |
| DC\_n3A-n28A-n77(2A)-n79A | DC\_n3A-n28A  DC\_n3A-n77A  DC\_n3A-n79A  DC\_n28A-n77A  DC\_n28A-n79A  DC\_n77A-n79A |
| DC\_n3A-n41A-n77A-n79A | DC\_n3A-n41A  DC\_n3A-n77A  DC\_n3A-n79A  DC\_n41A-n77A  DC\_n41A-n79A  DC\_n77A-n79A |
| DC\_n7A-n20A-n67A-n78A | DC\_n7A-n20A  DC\_n7A-n78A  DC\_n20A-n78A |
| DC\_n7A-n20A-n67A-n78(2A) | DC\_n7A-n20A  DC\_n7A-n78A  DC\_n20A-n78A |
| DC\_n28A-n41A-n77A-n79A | DC\_n28A-n41A  DC\_n28A-n77A  DC\_n28A-n79A  DC\_n41A-n77A  DC\_n41A-n79A  DC\_n77A-n79A |

Table 5.5B-4: Inter-band NR DC configurations (five bands)

| NR DC  configuration | Uplink NR DC  configuration |
| --- | --- |
| DC\_n1A-n3A-n7A-n67A-n78A | DC\_n1A-n3A  DC\_n1A-n7A  DC\_n1A-n78A  DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n1A-n3A-n7A-n67A-n78(2A) | DC\_n1A-n3A  DC\_n1A-n7A  DC\_n1A-n78A  DC\_n3A-n7A  DC\_n3A-n78A  DC\_n7A-n78A |
| DC\_n1A-n3A-n28A-n41A-n77A | DC\_n1A-n3A  DC\_n1A-n28A  DC\_n1A-n41A  DC\_n1A-n77A  DC\_n3A-n28A  DC\_n3A-n41A  DC\_n3A-n77A  DC\_n28A-n41A  DC\_n28A-n77A  DC\_n41A-n77A |
| DC\_n1A-n3A-n28A-n41A-n79A | DC\_n1A-n3A  DC\_n1A-n28A  DC\_n1A-n41A  DC\_n1A-n79A  DC\_n3A-n28A  DC\_n3A-n41A  DC\_n3A-n79A  DC\_n28A-n41A  DC\_n28A-n79A  DC\_n41A-n79A |
| DC\_n1A-n3A-n28A-n77A-n79A | DC\_n1A-n3A  DC\_n1A-n28A  DC\_n1A-n77A  DC\_n1A-n79A  DC\_n3A-n28A  DC\_n3A-n77A  DC\_n3A-n79A  DC\_n28A-n77A  DC\_n28A-n79A  DC\_n77A-n79A |
| DC\_n1A-n3A-n41A-n77A-n79A | DC\_n1A-n3A  DC\_n1A-n41A  DC\_n1A-n77A  DC\_n1A-n79A  DC\_n3A-n41A  DC\_n3A-n77A  DC\_n3A-n79A  DC\_n41A-n77A  DC\_n41A-n79A  DC\_n77A-n79A |
| DC\_n1A-n28A-n41A-n77A-n79A | DC\_n1A-n28A  DC\_n1A-n41A  DC\_n1A-n77A  DC\_n1A-n79A  DC\_n28A-n41A  DC\_n28A-n77A  DC\_n28A-n79A  DC\_n41A-n77A  DC\_n41A-n79A  DC\_n77A-n79A |
| DC\_n3A-n7A-n20A-n67A-n78A | DC\_n3A-n7A  DC\_n3A-n20A  DC\_n3A-n78A  DC\_n7A-n20A  DC\_n7A-n78A  DC\_n20A-n78A |
| DC\_n3A-n7A-n20A-n67A-n78(2A) | DC\_n3A-n7A  DC\_n3A-n20A  DC\_n3A-n78A  DC\_n7A-n20A  DC\_n7A-n78A  DC\_n20A-n78A |
| DC\_n3A-n28A-n41A-n77A-n79A | DC\_n3A-n28A  DC\_n3A-n41A  DC\_n3A-n77A  DC\_n3A-n79A  DC\_n28A-n41A  DC\_n28A-n77A  DC\_n28A-n79A  DC\_n41A-n77A  DC\_n41A-n79A  DC\_n77A-n79A |

## 5.5C Configurations for SUL

The configuration tables for SUL describe Bandwidth Combination Sets. Bandwidth Combination Set 4 and 5 contains all possible defined channel bandwidths for each band in the combination. The fact that BCS4 and BCS5 contains all channel bandwidths for each band does not alter if a bandwidth is mandatory or optional for a given band. Bandwidths that are identified as optional in Table 5.3.5-1 for a given release are still optional for UEs that support BCS4 or BCS5. , where the bandwidths the UE supports for each band, the maximum bandwidth and/or minimum bandwidth for the band in the band combination are indicated in the UE capabilities. The minimum bandwidth per CC and aggregated FDD, TDD and total bandwidth per band combination may be indicated only for BCS5 as described in 38.306 [15] and BCS5 shall not be indicated together with BCS4 for a SUL configuration. For SUL band combinations including FR1 intra-band CA and with BCS4 or BCS5, the Bandwidth Combination Sets for the FR1 intra-band CA are BCS4 or BCS5.

For the NR SUL band configurations with inter-band CA in sub-clause 5.5C, when the capability *supportedBandPairListNR-r18* is present, three or four bands can be configured in the uplink with simultaneous uplink transmission on up to two bands, and the corresponding requirements for SUL band configurations with inter-band CA and with uplink assigned to one or two bands shall apply. For each uplink band pair in the NR SUL band configurations with inter-band CA, according to the capability *uplinkTxSwitchingOptionForBandPair*,

– if *switchedUL* is supported, uplink transmission on any one band of the band pair in the band combination shall be supported according to the scheduling commands, and the corresponding requirements for SUL band configuration with inter-band CA and with uplink assigned to one band on band X or band Y apply;

– if *dualUL* is supported, simultaneous uplink transmission on the two NR UL bands from the band pair for which *dualUL* is declared in the band combination shall be supported according to the scheduling commands, and the corresponding requirements for SUL band configuration with inter-band CA and with uplink CA between the two uplink bands apply.

For SUL band configuration with inter-band CA, band pair(s) of two non-SUL bands with *switchedUL* or *dualUL* by the parameter *uplinkTxSwitchingOption* is supported, and any other band pair(s) including SUL with *switchedUL* is supported, in release 18.

Table 5.5C-1: Supported channel bandwidths per SUL band combination

| SUL configuration | NR Band | Channel bandwidth (MHz) (NOTE 1) | Bandwidth combination set |
| --- | --- | --- | --- |
| SUL\_n1A-n80A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  | n80 | 5, 10, 15, 20, 25, 30 |  |
| SUL\_n1A-n81A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  | n81 | 5, 10, 15, 20 |  |
| SUL\_n1A-n89A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  | n89 | 5, 10, 15, 20 |  |
| SUL\_n3A-n84A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  | n84 | 5, 10, 15, 20 |  |
| SUL\_n5A-n84A | n5 | 5, 10, 15, 20 | 0 |
|  | n84 | 5, 10, 15, 20 |  |
| SUL\_n8A-n84A | n8 | 5, 10, 15, 20 | 0 |
|  | n84 | 5, 10, 15, 20 |  |
| SUL\_n24A-n99A | n24 | 5, 10 | 0 |
|  | n99 | 5, 10 |  |
| SUL\_n41A-n80A | n41 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n80 | 5, 10, 15, 20, 25, 30 |  |
|  | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 1 |
|  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| SUL\_n41A-n81A | n41 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n81 | 5, 10, 15, 20 |  |
| SUL\_n41A-n83A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  | n83 | 5, 10, 15, 20, 30 |  |
| SUL\_n41A-n95A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  | n95 | 5, 10, 15 |  |
| SUL\_n41A-n97A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  | n97 | 5, 10, 15, 20, 25, 30, 40, 50, 60, 80 |  |
|  | n41 | 10, 15, 20, 30, 40, 50, 60, 70, 80, 90, 100 | 1 |
|  | n97 | 5, 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
| SUL\_n41A-n98A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| SUL\_n41A-n99A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  | n99 | 5, 10 |  |
| SUL\_n48A-n99A | n48 | 5, 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n99 | 5, 10 |  |
| SUL\_n77A-n80A | n77 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n80 | 5, 10, 15, 20, 25, 30 |  |
| SUL\_n77A-n84A | n77 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n84 | 5, 10, 15, 20 |  |
| SUL\_n77A-n99A | n77 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n99 | 5, 10 |  |
| SUL\_n78A-n80A | n78 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n80 | 5, 10, 15, 20, 25, 30 |  |
|  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 1 |
|  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| SUL\_n78A-n81A | n78 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n81 | 5, 10, 15, 20 |  |
|  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 1 |
|  | n81 | 5, 10, 15, 20 |  |
| SUL\_n78A-n82A | n78 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n82 | 5, 10, 15, 20 |  |
| SUL\_n78A-n83A | n78 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n83 | 5, 10, 15, 20 |  |
|  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 1 |
|  | n83 | 5, 10, 15, 20, 30 |  |
| SUL\_n78A-n84A | n78 | 10, 15, 20, 40, 50, 60, 80, 90, 100 | 0 |
|  | n84 | 5, 10, 15, 20 |  |
|  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 1 |
|  | n84 | 5, 10, 15, 20, 25, 30, 40, 50 |  |
| SUL\_n78A-n86A | n78 | 10, 15, 20, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  | n86 | 5, 10, 15, 20 |  |
| SUL\_n78A-n89A | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  | n89 | 5, 10, 15, 20 |  |
| SUL\_n79A-n80A | n79 | 40, 50, 60, 80, 100 | 0 |
|  | n80 | 5, 10, 15, 20, 25, 30 |  |
|  | n79 | 40, 50, 60, 80, 100 | 1 |
|  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| SUL\_n79A-n81A | n79 | 40, 50, 60, 80, 100 | 0 |
|  | n81 | 5, 10, 15, 20 |  |
| SUL\_n79A-n83A | n79 | 40, 50, 60, 80, 100 | 0 |
|  | n83 | 5, 10, 15, 20, 30 |  |
|  | n79 | See n79 channel bandwidths in Table 5.3.5-1 for each carrier | 4 and 5 |
|  | n83 | See n83 channel bandwidths in Table 5.3.5-1 for each carrier |  |
| SUL\_n79A-n84A | n79 | 40, 50, 60, 80, 100 | 0 |
|  | n84 | 5, 10, 15, 20 |  |
| SUL\_n79A-n95A | n79 | 40, 50, 60, 80, 100 | 0 |
|  | n95 | 5, 10, 15 |  |
| SUL\_n79A-n97A | n79 | 40, 50, 60, 80, 100 | 0 |
|  | n97 | 5, 10, 15, 20, 25, 30, 40, 50, 60, 80 |  |
|  | n79 | 40, 50, 60, 80, 100 | 1 |
|  | n97 | 5, 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  | n79 | See n79 channel bandwidths in Table 5.3.5-1 for each carrier | 4 and 5 |
|  | n97 | See n97 channel bandwidths in Table 5.3.5-1 for each carrier |  |
| SUL\_n79A-n98A | n79 | 40, 50, 60, 80, 100 | 0 |
|  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| NOTE 1: The SCS of each channel bandwidth for NR band refers to Table 5.3.5-1. | | | |

Table 5.5C-2: Supported channel bandwidths per SUL band combination with intra-band non-contiguous CA

| SUL band combination with intra-band non-contiguous CA | SUL configuration | NR Band | Channel bandwidth (MHz) (NOTE 1) | Bandwidth combination set |
| --- | --- | --- | --- | --- |
| CA\_n41(2A)-n99A | SUL\_n41A-n99A | n41 | CA\_n41(2A)\_BCS0 | 0 |
|  |  | n99 | 5, 10 |  |
| CA\_n48(2A)-n99A | SUL\_n48A-n99A | n48 | CA\_n48(2A)\_BCS0 | 0 |
|  |  | n99 | 5, 10 |  |
| CA\_n77(2A)-n99A | SUL\_n77A-n99A | n77 | CA\_n77(2A)\_BCS0 | 0 |
|  |  | n99 | 5, 10 |  |
| CA\_n78(2A)-n86A | SUL\_n78A-n86A | n78 | CA\_n78(2A)\_BCS0 | 0 |
|  |  | n86 | 5, 10, 15, 20 |  |
| NOTE 1: The SCS of each channel bandwidth for NR band refers to Table 5.3.5-1. | | | | |

Table 5.5C-3: Supported channel bandwidths per SUL band combination  
with intra-band contiguous CA

| SUL band combination with CA | SUL configuration | NR Band | Channel bandwidth (MHz) (NOTE 1) | Bandwidth combination set |
| --- | --- | --- | --- | --- |
| CA\_n41C-n80A | SUL\_n41A-n80A  CA\_n41C-n80A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n41C-n83A | SUL\_n41A-n83A  CA\_n41C-n83A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n41C-n95A | SUL\_n41A-n95A  CA\_n41C-n95A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n41C-n98A | SUL\_n41A-n98A  CA\_n41C-n98A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n78C-n80A | SUL\_n78A-n80A  CA\_n78C-n80A | n78 | CA\_n78C\_BCS1 | 0 |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n78C-n81A | SUL\_n78A-n81A  CA\_n78C-n81A | n78 | CA\_n78C\_BCS1 | 0 |
|  |  | n81 | 5, 10, 15, 20 |  |
| CA\_n78C-n84A | SUL\_n78A-n84A  CA\_n78C-n84A | n78 | CA\_n78C\_BCS1 | 0 |
|  |  | n84 | 5, 10, 15, 20, 25, 30, 40, 50 |  |
| CA\_n78C-n89A | SUL\_n78A-n89A  CA\_n78C-n89A | n78 | CA\_n78C\_BCS1 | 0 |
|  |  | n89 | 5, 10, 15, 20 |  |
| CA\_n79C-n80A | SUL\_n79A-n80A  CA\_n79C-n80A | n79 | CA\_n79C\_BCS0 | 0 |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n79C-n83A | SUL\_n79A-n83A  CA\_n79C-n83A | n79 | CA\_n79C\_BCS0 | 0 |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n79C-n95A | SUL\_n79A-n95A  CA\_n79C-n95A | n79 | CA\_n79C\_BCS0 | 0 |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n79C-n98A | SUL\_n79A-n98A  CA\_n79C-n98A | n79 | CA\_n79C\_BCS0 | 0 |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| NOTE 1: The SCS of each channel bandwidth for NR band refers to Table 5.3.5-1. | | | | |

Table 5.5C-4: Supported channel bandwidths per SUL band combination with inter-band CA

| SUL band combination with CA | UL configuration | NR Band | Channel bandwidth (MHz) (NOTE 1) | Bandwidth combination set |
| --- | --- | --- | --- | --- |
| CA\_n1A\_n78A-n80A | SUL\_n78A-n80A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n1A\_n78C-n80A | SUL\_n78A-n80A  CA\_n78C  CA\_n1A-n78A  CA\_n78C-n80A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n1A\_n78A-n81A | SUL\_n78A-n81A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n81 | 5, 10, 15, 20 |  |
| CA\_n1A\_n78C-n81A | SUL\_n78A-n81A  CA\_n78C-n81A  CA\_n78C  CA\_n1A-n78A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n81 | 5, 10, 15, 20 |  |
| CA\_n1A\_n78A-n84A | SUL\_n78A-n84A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n84 | 5, 10, 15, 20, 25, 30, 40, 50 |  |
| CA\_n1A\_n78C-n84A | SUL\_n78A-n84A  CA\_n78C-n84A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n84 | 5, 10, 15, 20, 25, 30, 40, 50 |  |
| CA\_n1A\_n78A-n89A | SUL\_n78A-n89A  CA\_n1A-n78A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n89 | 5, 10, 15, 20 |  |
| CA\_n1A\_n78C-n89A | SUL\_n78A-n89A  CA\_n78C  CA\_n1A-n78A  CA\_n78C-n89A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | See CA\_n78C Bandwidth Combination Set 1 in Table 5.5A.1-1 |  |
|  |  | n89 | 5, 10, 15, 20 |  |
| CA\_n3A\_n41A-n80A | SUL\_n41A-n80A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n3A\_n41C-n80A | SUL\_n41A-n80A  CA\_n41C-n80A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n41 | CA\_n41C\_BCS1 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n3A\_n78A-n80A | SUL\_n78A-n80A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n3A\_n78C-n80A | SUL\_n78A-n80A  CA\_n78C-n80A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n3A\_n78A-n84A | SUL\_n78A-n84A  CA\_n3A-n78A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n3A\_n78C-n84A | SUL\_n78A-n84A  CA\_n3A-n78A  CA\_n78C  CA\_n78C-n84A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n78 | See CA\_n78C Bandwidth Combination Set 1 in Table 5.5A.1-1 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n3A\_n79A-n80A | SUL\_n79A-n80A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n3A\_n79C-n80A | SUL\_n79A-n80A  CA\_n79C-n80A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n5A\_n78A-n84A | SUL\_n78A-n84A  CA\_n5A-n78A | n5 | 5, 10, 15, 20 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n5A\_n78C-n84A | SUL\_n78A-n84A  CA\_n5A-n78A  CA\_n78C  CA\_n78C-n84A | n5 | 5, 10, 15, 20 | 0 |
|  |  | n78 | See CA\_n78C Bandwidth Combination Set 1 in Table 5.5A.1-1 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n8A\_n78A-n81A | SUL\_n78A-n81A | n8 | 5, 10, 15, 20 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n81 | 5, 10, 15, 20 |  |
| CA\_n8A\_n78A-n84A | SUL\_n78A-n84A  CA\_n8A-n78A | n8 | 5, 10, 15, 20 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n8A\_n78C-n84A | SUL\_n78A-n84A  CA\_n78C  CA\_n8A-n78A  CA\_n78C-n84A | n8 | 5, 10, 15, 20 | 0 |
|  |  | n78 | See CA\_n78C Bandwidth Combination Set 1 in Table 5.5A.1-1 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n28A\_n41A-n83A | SUL\_n41A-n83A | n28 | 5, 10, 15, 20, 30 | 0 |
|  |  | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n28A\_n41C-n83A | SUL\_n41A-n83A  CA\_n41C-n83A | n28 | 5, 10, 15, 20, 30 | 0 |
|  |  | n41 | CA\_n41C\_BCS1 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n28A\_n79A-n83A | SUL\_n79A-n83A | n28 | 5, 10, 15, 20, 30 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n28A\_n79C-n83A | SUL\_n79A-n83A  CA\_n79C-n83A | n28 | 5, 10, 15, 20, 30 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n41A\_n79A-n80A | SUL\_n79A-n80A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n41A\_n79A-n83A | SUL\_n79A-n83A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n41A\_n79C-n83A | SUL\_n79A-n83A  CA\_n79C-n83A  CA\_n41A-n79A  CA\_n79C | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n41C\_n79A-n83A | SUL\_n79A-n83A  CA\_n41C  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n41C\_n79C-n83A | CA\_n41C  CA\_n79C  SUL\_n79A-n83A  CA\_n79C-n83A  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n41A\_n79A-n95A | SUL\_n79A-n95A | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n41A\_n79C-n95A | SUL\_n79A-n95A  CA\_n79C-n95A  CA\_n41A-n79A  CA\_n79C | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n41C\_n79A-n95A | SUL\_n79A-n95A  CA\_n41C  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n41C\_n79C-n95A | CA\_n41C  CA\_n79C  SUL\_n79A-n95A  CA\_n79C-n95A  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n98 | 5, 10, 15 |  |
| CA\_n41A\_n79A-n97A | SUL\_n79A-n97A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n97 | 5, 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
| CA\_n41A\_n79A-n98A | SUL\_n79A-n98A | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n41A\_n79C-n98A | SUL\_n79A-n98A  CA\_n79C-n98A  CA\_n41A-n79A  CA\_n79C | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n41C\_n79A-n98A | SUL\_n79A-n98A  CA\_n41C  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n41C\_n79C-n98A | CA\_n41C  CA\_n79C  SUL\_n79A-n98A  CA\_n79C-n98A  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n78A\_n1A-n80A | SUL\_n1A-n80A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30 |  |
| CA\_n78C\_n1A-n80A | SUL\_n1A-n80A  CA\_n78C  CA\_n1A-n78A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30 |  |
| CA\_n78A\_n1A-n81A | SUL\_n1A-n81A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n81 | 5, 10, 15, 20 |  |
| CA\_n78C\_n1A-n81A | SUL\_n1A-n81A  CA\_n78C  CA\_n1-n78A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n81 | 5, 10, 15, 20 |  |
| CA\_n78A\_n1A-n89A | SUL\_n1A-n89A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n89 | 5, 10, 15, 20 |  |
| CA\_n78C\_n1A-n89A | SUL\_n1A-n89A  CA\_n78C  CA\_n1A-n78A | n1 | 5, 10, 15, 20, 25, 30, 40, 50 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n89 | 5, 10, 15, 20 |  |
| CA\_n78A\_n3A-n84A | SUL\_n3A-n84A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n78C\_n3A-n84A | SUL\_n3A-n84A  CA\_n78C  CA\_n3A-n78A | n3 | 5, 10, 15, 20, 25, 30, 40 | 0 |
|  |  | n78 | CA\_n78C\_BCS1 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n78A\_n5A-n84A | SUL\_n5A-n84A  CA\_n5A-n78A | n5 | 5, 10, 15, 20 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n78C\_n5A-n84A | SUL\_n5A-n84A  CA\_n78C  CA\_n5A-n78A | n5 | 5, 10, 15, 20 | 0 |
|  |  | n78 | See CA\_n78C Bandwidth Combination Set 1 in Table 5.5A.1-1 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n78A\_n8A-n84A | SUL\_n8A-n84A  CA\_n8A-n78A | n8 | 5, 10, 15, 20 | 0 |
|  |  | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n78C\_n8A-n84A | SUL\_n8A-n84A  CA\_n78C  CA\_n8A-n78A | n8 | 5, 10, 15, 20 | 0 |
|  |  | n78 | See CA\_n78C Bandwidth Combination Set 1 in Table 5.5A.1-1 |  |
|  |  | n84 | 5, 10, 15, 20 |  |
| CA\_n79A\_n41A-n80A | SUL\_n41A-n80A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n80 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n79A\_n41A-n83A | SUL\_n41A-n83A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n79A\_n41C-n83A | SUL\_n41A-n83A  CA\_n41C-n83A  CA\_n41A-n79A  CA\_n41C | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n79C\_n41A-n83A | SUL\_n41A-n83A  CA\_n41A-n79A  CA\_n79C | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n79C\_n41C-n83A | CA\_n41C  CA\_n79C  SUL\_n41A-n83A  CA\_n41C-n83A  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n79A\_n41A-n95A | SUL\_n41A-n95A | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n79C\_n41A-n95A | SUL\_n41A-n95A  CA\_n41A-n79A  CA\_n79C | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n79A\_n41C-n95A | SUL\_n41A-n95A  CA\_n41C-n95A  CA\_n41A-n79A  CA\_n41C | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n79C\_n41C-n95A | CA\_n41C  CA\_n79C  SUL\_n41A-n95A  CA\_n41C-n95A  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n95 | 5, 10, 15 |  |
| CA\_n79A\_n41A-n97A | SUL\_n41A-n97A | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 | 0 |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n97 | 5, 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 |  |
| CA\_n79A\_n41A-n98A | SUL\_n41A-n98A | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n79C\_n41A-n98A | SUL\_n41A-n98A  CA\_n41A-n79A  CA\_n79C | n41 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n79A\_n41C-n98A | SUL\_n41A-n98A  CA\_n41C-n98A  CA\_n41A-n79A  CA\_n41C | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | 10, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n79C\_n41C-n98A | CA\_n41C  CA\_n79C  SUL\_n41A-n98A  CA\_n41C-n98A  CA\_n41A-n79A | n41 | CA\_n41C\_BCS1 | 0 |
|  |  | n79 | CA\_n79C\_BCS0 |  |
|  |  | n98 | 5, 10, 15, 20, 25, 30, 40 |  |
| CA\_n28A-n79A\_n41A-n83A | SUL\_n41A-n83A | n28 | 5, 10, 15, 20, 30 | 0 |
|  |  | n41 | 10, 15, 20, 30, 40, 50, 60, 80, 90, 100 |  |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| CA\_n28A-n41A\_n79A-n83A | SUL\_n79A-n83A | n28 | 5, 10, 15, 20, 30 | 0 |
|  |  | n41 | 10, 15, 20, 30, 40, 50, 60, 70, 80, 90, 100 |  |
|  |  | n79 | 40, 50, 60, 80, 100 |  |
|  |  | n83 | 5, 10, 15, 20, 30 |  |
| NOTE 1: The SCS of each channel bandwidth for NR band refers to Table 5.3.5-1. | | | | |

Table 5.5C-5: Supported channel bandwidths per SUL band combination  
with inter-band CA (two SUL cells)

| SUL band combination with CA | Uplink CA  configuration or SUL configuration | NR Band | Channel bandwidth (MHz) (NOTE 1) | Bandwidth combination set |
| --- | --- | --- | --- | --- |
| CA\_n41A-n95A\_n79A-n98A | SUL\_n41A-n95A  SUL\_n79A-n98A  CA\_n41A-n79A | n41 | 10, 15, 20, 25, 30, 35, 40, 45, 50, 60, 70, 80, 90, 100 | 0 |
| n79 | 10, 20, 30,40, 50, 60, 70, 80, 90, 100 |
| n95 | 5, 10, 15 |
| n98 | 5, 10, 15, 20, 25, 30, 40 |
| CA\_n41A-n98A\_n79A-n95A | SUL\_n41A-n98A  SUL\_n79A-n95A  CA\_n41A-n79A | n41 | 10, 15, 20, 25, 30, 35, 40, 45, 50, 60, 70, 80, 90, 100 | 0 |
| n79 | 10, 20, 30,40, 50, 60, 70, 80, 90, 100 |
| n95 | 5, 10, 15 |
| n98 | 5, 10, 15, 20, 25, 30, 40 |
| CA\_n41A-n83A\_n79A-n98A | SUL\_n41A-n83A  SUL\_n79A-n98A  CA\_n41A-n79A | n41 | 10, 15, 20, 25, 30, 35, 40, 45, 50, 60, 70, 80, 90, 100 | 0 |
| n79 | 10, 20, 30,40, 50, 60, 70, 80, 90, 100 |
| n83 | 5, 10, 15, 20,30 |
| n98 | 5, 10, 15, 20, 25, 30, 40 |
| CA\_n41A-n83A\_n79A-n95A | SUL\_n41A-n83A  SUL\_n79A-n95A  CA\_n41A-n79A | n41 | 10, 15, 20, 25, 30, 35, 40, 45, 50, 60, 70, 80, 90, 100 | 0 |
| n79 | 10, 20, 30,40, 50, 60, 70, 80, 90, 100 |
| n83 | 5, 10, 15, 20,30 |
| n95 | 5, 10, 15 |
| CA\_n78C\_n80A-n84A | SUL\_n78A-n80A  SUL\_n78A-n84A  CA\_n78C2 | n78 | CA\_n78C\_BCS1 | 0 |
| n80 | 5, 10, 15, 20, 25, 30, 40 |
| n84 | 5, 10, 15, 20, 25, 30, 40, 50 |
| CA\_n78C\_n81A-n84A | SUL\_n78A-n81A  SUL\_n78A-n84A  CA\_n78C2 | n78 | 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, 100 CA\_n78C\_BCS1 | 0 |
| n81 | 5, 10, 15, 20 |  |
| n84 | 5, 10, 15, 20, 25, 30, 40, 50 |  |
| CA\_n78C\_n84A-n89A | SUL\_n78A-n84A SUL\_n78A-n89A CA\_n78C | n78 | CA\_n78C\_BCS1 | 0 |
|  |  | n84 | 5, 10, 15, 20, 25, 30, 40, 50 |  |
|  |  | n89 | 5, 10, 15, 20 |  |
| NOTE 1: The SCS of each channel bandwidth for NR band refers to Table 5.3.5-1.  NOTE 2: Power Class 2 is allowed for this uplink combination or single uplink carrier in this downlink/uplink combination.  NOTE 3: Minimum requirements for Power Class 2 are applicable for this uplink combination with up to 2Tx antenna connectors in this downlink/uplink combination. | | | | |

## 5.5D Reserved

## 5.5E Void

### 5.5E.1A Configurations for Sidelink CA

For NR SL CA operation, the SL CA channel bandwidths for intra-band contiguous are specified in clause 5.5E.1A.1. The same (symmetrical) channel bandwidth is specified for both the transmission and reception path.

#### 5.5E.1A.1 Configurations for Sidelink intra-band contiguous CA

Table 5.5E.1A.1-1 NR SL CA configurations and bandwidth combination set for SL intra-band contiguous CA in FR1

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| **Sidelink CA configuration / Bandwidth combination set** | | | | | | | |
| Sidelink CA configuration | Sidelink CA configuration for TX | Component carriers in order of increasing carrier frequency | | | | Maximum aggregated  bandwidth [MHz] | Bandwidth combination set |
| Channel bandwidths for carrier [MHz] | Channel bandwidths for carrier [MHz] | Channel bandwidths for carrier [MHz] | Channel bandwidths for carrier [MHz] |
| SL\_n47B | SL\_n47B | 10 | 10, 20,30 |  |  | 70 | 0 |
|  |  | [20] | [20,30] |  |  |  |  |
|  |  | 30 | 30,40 |  |  |  |  |