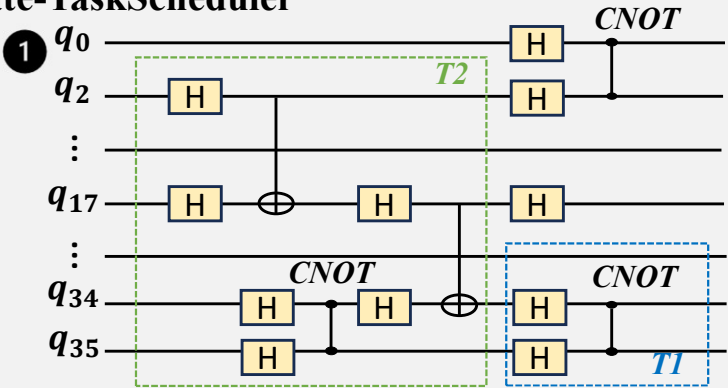


Gate-TaskScheduler



2 Task of gate sequence

T1	T2	...	T74	T75	...
...	T150	T151	T152	...	TN

T1: { $q_1, q_2, \dots$ } Processing order

Ex) T1 {34, 35 ... ...}

Qubit Index

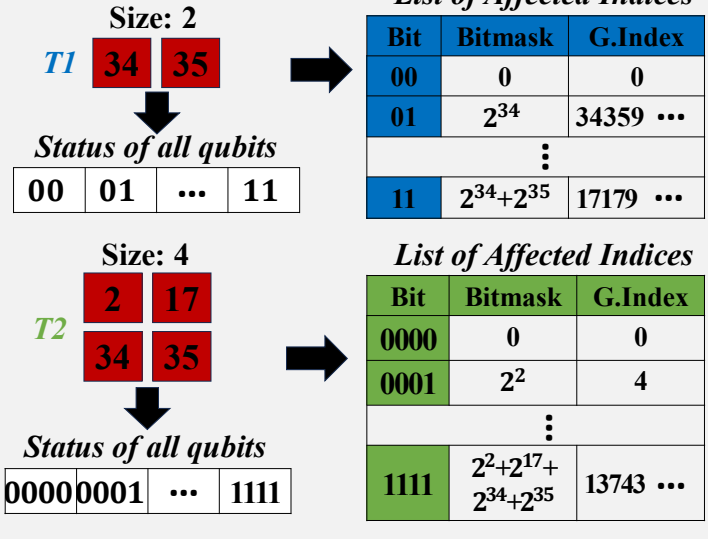
0	1	2	...	17	18	...
26	27	...	33	34	35	36

T1: {34, 35}

T2: {2, 17, 34, 35}

AffectedIndicesBuilder

3



GeneratorStructure

4

StateSpace Structure

# of Qubits	Global Index	Node ID	Node offset	GPU ID	Global GPU ID	GPU offset
36	0	0	0	0	0	0
36	34359738368	0	0	3	3	25769804160
...	...	...	...	...	...	...
36	171799216388	3	103079216640	2	14	17179869440

StateSpace Structure

# of Qubits	Global Index	Node ID	Node offset	GPU ID	Global GPU ID	GPU offset
36	0	0	0	0	0	0
36	34359738368	0	0	3	3	25769804160
...	...	...	...	...	...	...
36	137438955268	3	103079216640	3	15	25769804160