

C1, C2, C3, C4, C5, C6, C7, C8, C9, C10

C1, C2, C3, C4, C5, C8, C10

RegDst	1
ALUSrc	0
MemtoReg	0
RegWrite	1
MemRead	0
MemWrite	0
Branch	0
ALUOp	10

C1, C2, C3, C4, C5, C6, C8, C10, C11

RegDst	X
ALUSrc	0
MemtoReg	X
RegWrite	0
MemRead	0
MemWrite	0
Branch	1
ALUOp	01

RegDst	0
ALUSrc	1
MemtoReg	1
RegWrite	1
MemRead	1
MemWrite	0
Branch	0
ALUOp	00