SE Sem IV

Microprocessor (2024-25)

PT-1 Question Bank

Module 1

Draw and explain 8086 CPU Architecture.

Explain Memory Segmentation related to 8086 microprocessor.

Explain Banking in 8086.

Explain Demultiplexing of Address/Data bus of 8086.

Draw and explain Timing diagrams for Read and Write operations in minimum mode.

Flag register of 8086 with suitable diagram.

Module 2

Instruction set-Data Transfer Instructions, Arithmetic Instructions.

Module 3

Draw and explain architecture of 8255 PPI.

Explain control word formats of 8255 PPI.

Sample Questions from Module 2:

i.   Write the mnemonic for instruction to load register DI with the base address of a data table starting at location TABLE.

ii.   Determine the state of status flags after following instructions are executed

        MOV AL, 6DH

        MOV BH, 40H

        ADD AL, BH

iv   What will be the result after the execution of following instructions.

        DAA, DAS, AAA, AAS, AAM, AAD, CBW, CWD, ADC, SBB

Rithesh Kini
Subject In-charge
Computer Eng Dept
TSEC