Simulation Profile : 1

Circuit Diagram :

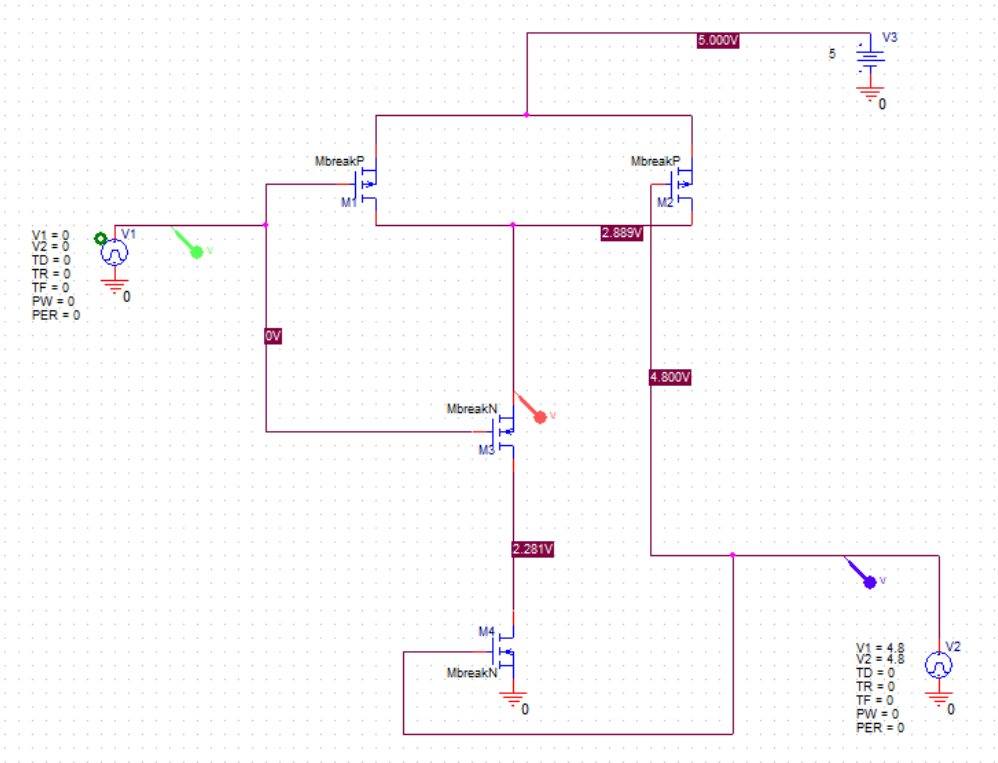


Figure : Circuit Diagram.

Simulation Outcome :

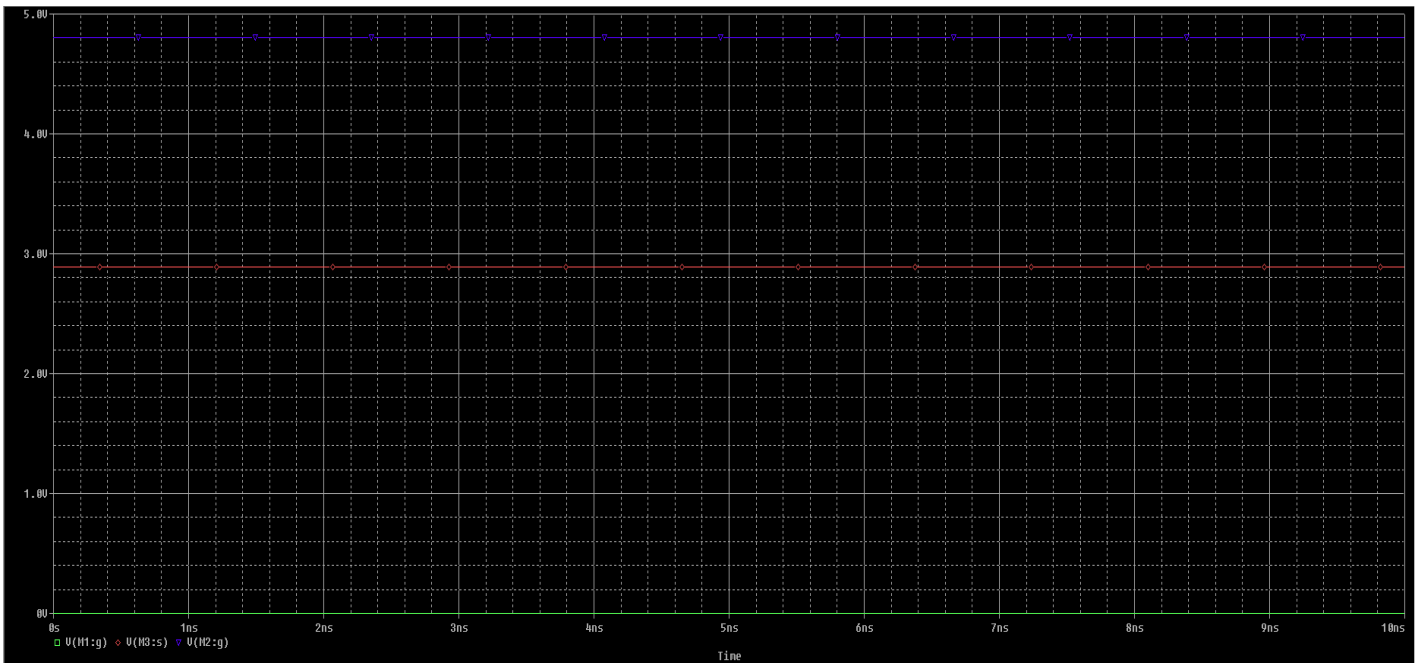


Figure : Simulation outcome.

Simulation Profile : 2

Circuit Diagram :

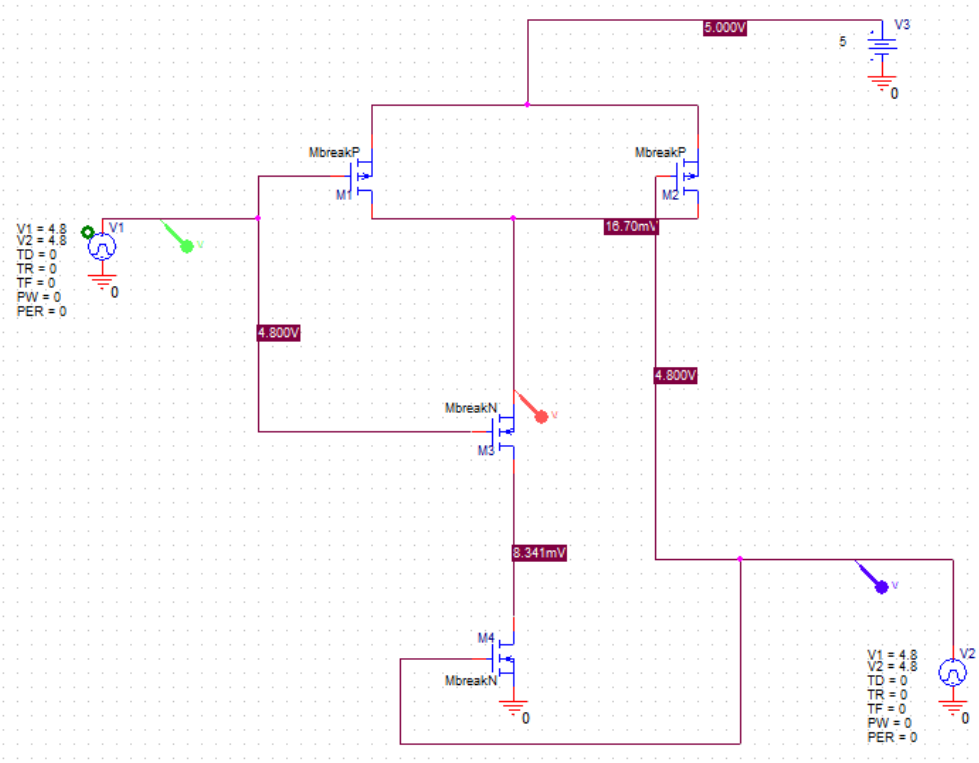


Figure : Circuit Diagram.

Simulation Outcome :

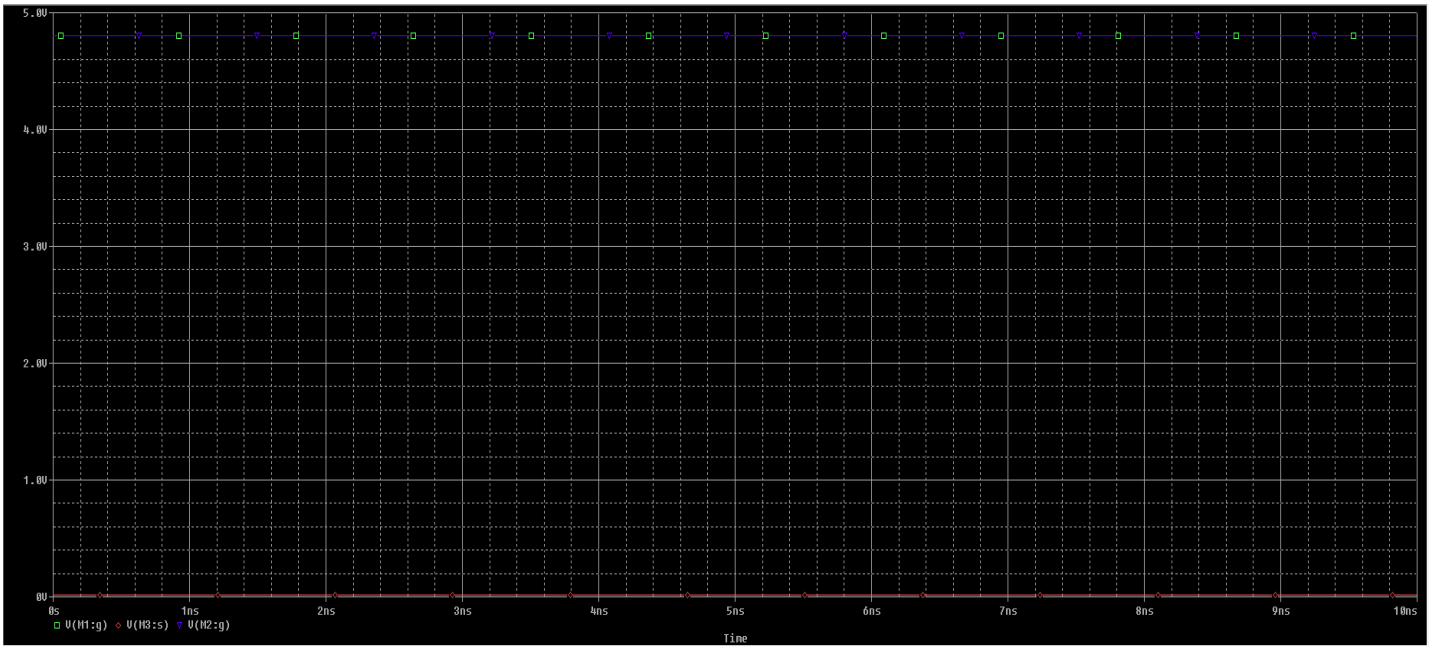
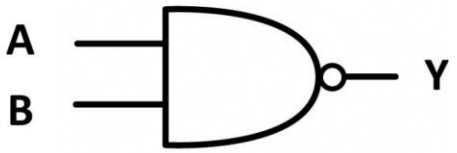


Figure : Simulation outcome.

NOR Gate :

Inputs		Output
A	B	Y
0	0	1
0	1	1
1	0	1
1	1	0



$$Y = \overline{A.B}$$

NAND Gate

Simulation :

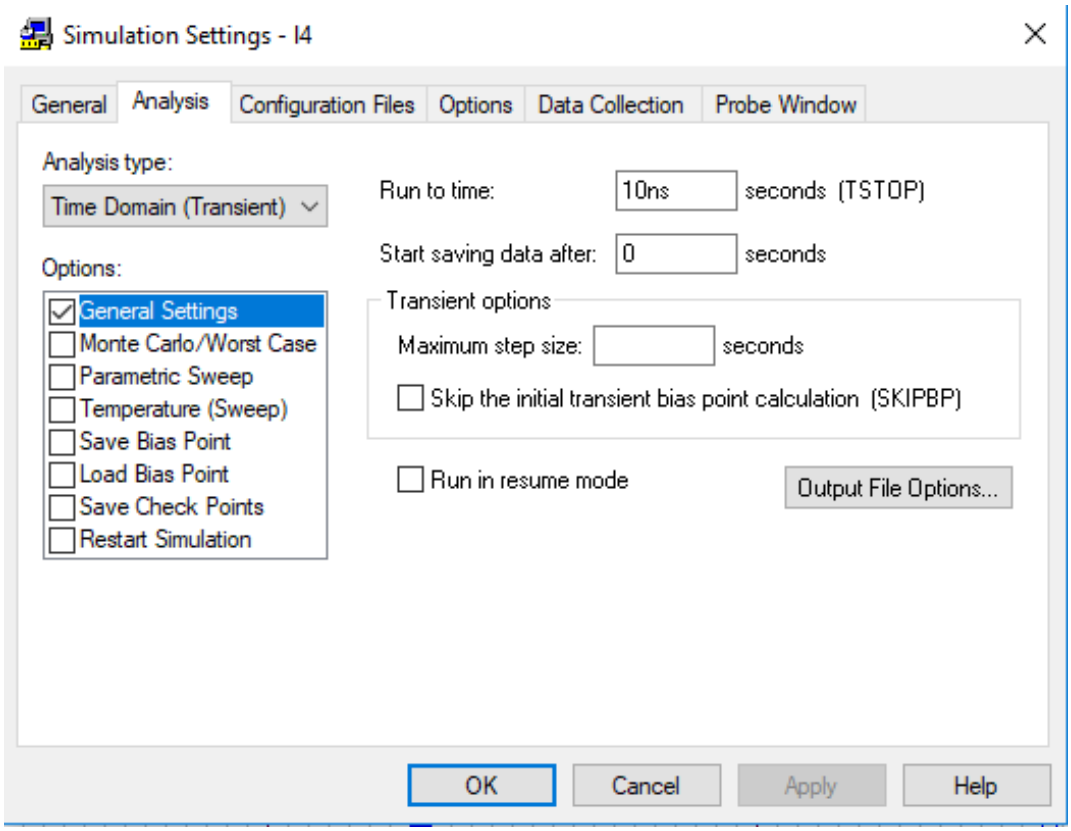


Figure : Simulation profile setting.