

# JILLELLA RAKESH JOSEPH RAJ

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## Career Objective

I have a decent skill set in Java Full Stack Development and am currently interning at KodNest through a CSR placement drive. I am seeking an entry-level opportunity in the IT sector where I can apply my knowledge, grow professionally, and contribute to impactful projects.

## EDUCATION

- Bachelor of Technology in Electrical and Electronics Engineering  
Gates Institute of Technology, Gooty | 2022-25  
Percentage: 75.24%
- Diploma in Electrical and Electronics Engineering  
Government Polytechnic College, Adoni | 2019-22  
Percentage: 89.02%
- SSC (10th Grade)  
Sri Chaitanya EM School, Guntakal | 2019  
GPA: 9.7

## TECHNICAL SKILLS

Programming: Java, HTML, CSS, SQL, Oracle. Tools & Technologies: MS Office. Additional: Editing, Photography.

## SOFT SKILLS

Strong communication, time management, and leadership skills with ability to collaborate in teams, think critically, and solve problems effectively.

## INTERNSHIP EXPERIENCE

Full Stack Developer Intern | KodNest | Feb 24, 2025 – Present

- Selected through a CSR placement drive organized at college.
- Gaining hands-on experience in full-stack development, including front-end, data Base and back-end technologies.
- Learning real-time project workflows, debugging techniques, and software deployment.
- Enhancing skills in Java, SQL, Oracle, MongoDB, HTML, CSS, JavaScript, Additional Frame Works and understanding industry-standard coding practices.

## PROJECTS

Automatic Power Factor Correction (APFC) System

- Designed to improve power factor by switching capacitor banks based on load requirements.
- Implemented using microcontroller and real-time electrical sensing components.
- Helped reduce power losses and improved system efficiency.

## LANGUAGES KNOWN

English, Hindi, Telugu (Mother Tongue)

## DECLARATION

I hereby declare that the information provided above is true to the best of my knowledge and belief.