

Sunny Cho

608-217-1342 | shcho1551@gmail.com | www.linkedin.com/in/sunghwan-cho-sunny

EDUCATION

University of Wisconsin-Madison <i>B.S. Computer Engineering & Computer Science</i> GPA: 3.97 / 4.00 <i>Dean's Honor List</i>	Sep 2022 - Dec 2027 <i>Madison, WI</i>
Australian International School Singapore <i>GPA: 3.90 / 4.00 Valedictorian of Class of 2021</i>	Jan 2019 - Dec 2021 <i>Singapore, Singapore</i>

TECHNICAL SKILLS

Languages: Java, Python, JavaScript, SystemVerilog, MATLAB, C++, SQL
Frameworks & Libraries: React, Next.js, Node.js, Flask
Databases, Cloud & Tools: MySQL, MongoDB, AWS, Docker, Git, GitHub

EXPERIENCE

Robotics Research Intern <i>Connected & Autonomous Transportation Systems Lab (Prof. Xiaopeng Li)</i>	Sept 2025 - Present <i>Madison, WI</i>
<ul style="list-style-type: none">Developed computer vision pipelines in Python for real-time image/video processing, collaborating with professors to train deep learning models for environment recognition and autonomous decision-makingBuilt an AI-driven control system for a robotic arm integrated with an autonomous vehicle, designed to automate concrete surface repair and reduce human exposure to hazardous roadwork conditions	
Undergraduate Teaching Assistant <i>University of Wisconsin-Madison</i>	Aug 2025 - Present <i>Madison, WI</i>
<ul style="list-style-type: none">Instructed 100+ students through one-on-one tutoring and weekly office hours by applying active learning and problem-based instruction in ECE 352: Digital System Fundamentals in SystemVerilogCollaborated with professors and fellow TAs to implement a reverse classroom model, improving student engagement through interactive problem-solving sessions and peer discussions	
Information Technology Consultant <i>Deloitte</i>	June 2025 - Aug 2025 <i>Seoul, South Korea</i>
<ul style="list-style-type: none">Initiated the digital transformation of a \$50B financial institution by evaluating 10+ benchmark models from banks using enterprise architecture frameworks to develop blueprint for modernization and technology integrationAnalyzed AI-OCR, blockchain, and eForm solutions through cross-industry benchmarking and feasibility scoring to recommend scalable technologies for enterprise-wide document digitizationPartnered with stakeholders to analyze Java-based application source code and define a reference architecture by creating inter-application connections that streamlined workflows and improved system efficiency	
Sergeant, Network Engineer <i>Republic of Korea Army</i>	June 2023 – Dec 2024 <i>Seoul, South Korea</i>
<ul style="list-style-type: none">Operated the PRC-999K tactical radio system and trained corporals to manage secure signal operations across varied combat scenarios, ensuring stable communication channels during brigade-level missionsPerformed geospatial analysis to optimize relay-station placement, sustaining brigade-level communications for 1000+ troops across varied terrain and minimizing signal disruption during field operationsServed as the communication link between command and front-line units by integrating TICN (Tactical Information and Communication Network) to enable real-time data flow and enhance situational awareness	
Student Tutor <i>University of Wisconsin-Madison</i>	Jan 2025 – Present <i>Madison, WI</i>
<ul style="list-style-type: none">Tutored students in Physics, Chemistry, Mathematics, and Computer Engineering, tailoring explanations to diverse learning styles and reinforcing conceptual understanding through problem-based instruction	

PROJECTS

Digital Control System Development GitHub SystemVerilog , Synopsys Design Compiler	Aug 2025–Present
<ul style="list-style-type: none">Designed and implemented a complete FPGA-based system for self-balancing control, integrating a fixed-point PID controller, sensor interfaces (SPI, UART), and motor PWM drivers into a cohesive real-time SoC.Developed modular RTL components in SystemVerilog achieving race-free standards, and verified design functionality through self-checking testbenches and file-driven random stimuli.	