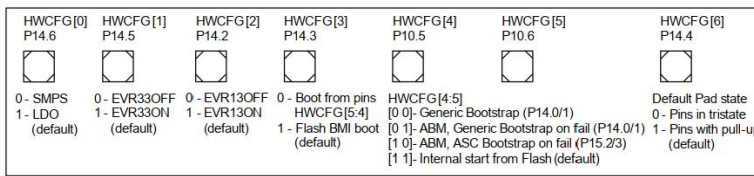


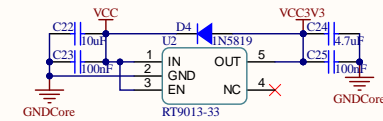
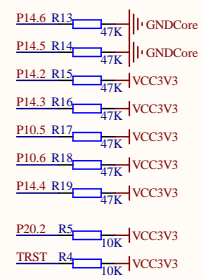
A

VAREF1:3.3V  
VAGND1:GND  
VDDM:3.3V  
VDD/VDDSB:1.3V  
VDD:1.3V  
VEXT:3.3V  
VDDP3:3.3V  
VDDFL3:3.3V  
VFLEX:3.3V  
VSS:GND  
VSSM:GND

默认  
HWCFG0=0  
HWCFG1=0  
HWCFG2=1  
HWCFG3=1  
HWCFG4=1  
HWCFG5=1  
HWCFG6=1



- 1) HWCFG[6] has weak internal pull-up active at start-up if the pin is left unconnected.
- 2) If HWCFG[6] is left unconnected or is externally pulled high, HWCFG[0:5] pins have weak internal pull-ups active at start-up.
- 3) If HWCFG[6] is connected to ground, HWCFG[0:5] pins are in tristate. External pull devices required for all HWCFG pins.
- 4) In packages smaller than QFP144, HWCFG[0:2] pins are absent & internally pulled high ensuring EVR13 (LDO) & EVR33 is active
- 5) HWCFG[0:2] and HWCFG[6] pins are latched during supply ramp-up (VEXT < 2.97V) and stored in PMSWSTAT.HWCFGEV & TRIST register bits. The remaining HWCFG pins are latched on internal reset release (between 100us - 180us after reset assertion) and stored in STSTAT register.



### 启动BOOT选择

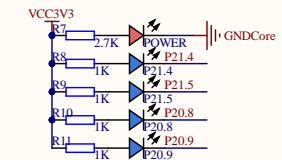
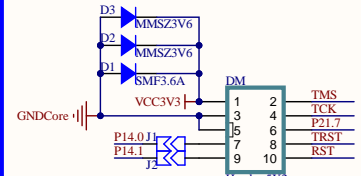
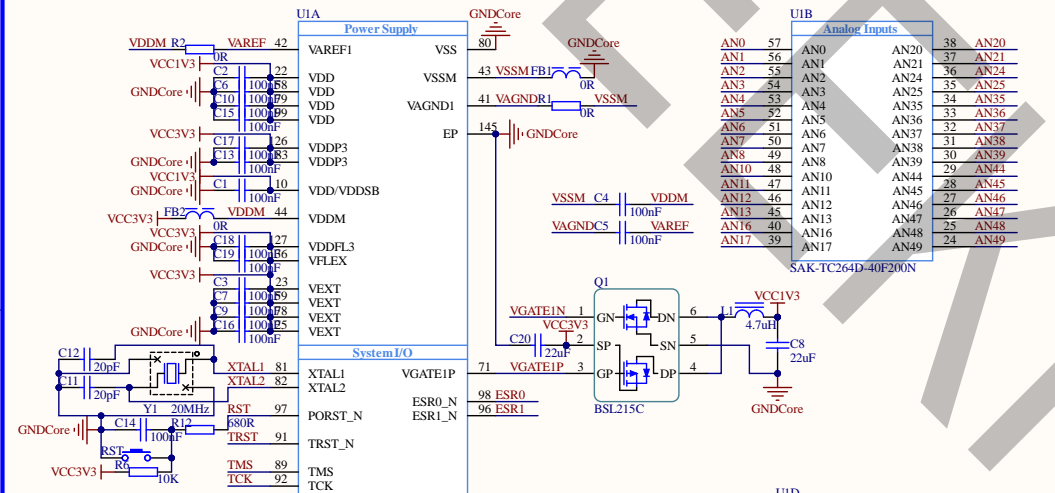
### 3.3V电源

B

### 单片机等

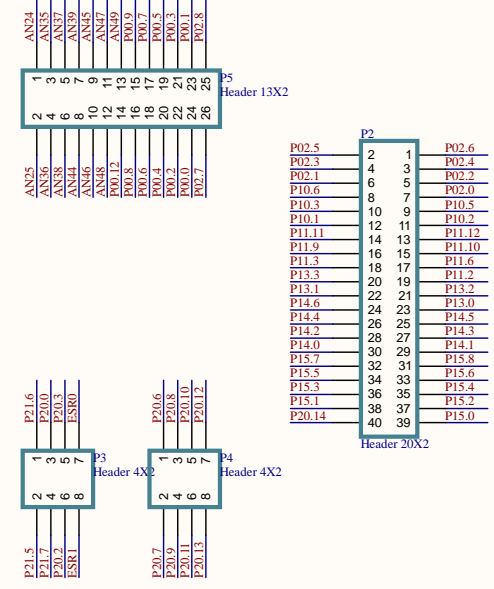
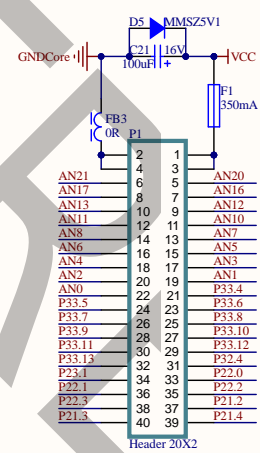
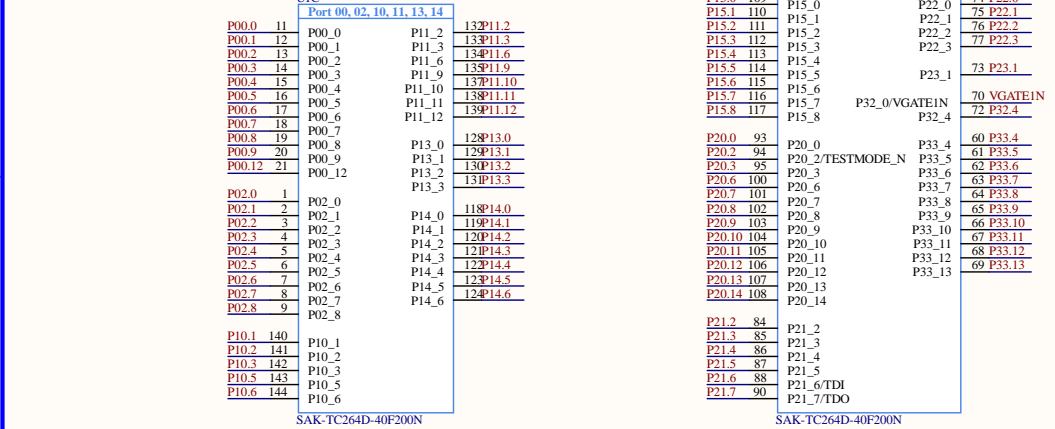
### 下载口

### 指示灯



C

### 插件



D