ID:

1:

Vreg 5V

Vref 3V

High Temp rated IC

Lower communication (fix made)

Higher communication (fix to be made)

Cell measurement (up to 8)

Discharge (untested)

2:

Vreg 5V

Vref 3V

High Temp rated IC

Lower communication (fix made)

Higher communication

Cell measurement

Discharge

3:

No voltages as they should be ☹ - Fucked…

Lower communication (fix made)

Higher communication

Cell measurement

Discharge

4:

Reg : 8.5v

Ref:

Lower communication (fix made)

Higher communication

Cell measurement

Discharge

5:

Components soldered but LTC removed due to ruined tracks

6:

High Temp rated IC

Lower communication (fix made)

Higher communication

Cell measurement

Discharge

7:

High Temp rated IC, salvaged from 5

Lower communication (fix made)

Higher communication

Cell measurement

Discharge

8:

Not high temp, salvaged from prototype

Lower communication (fix made)

Higher communication

Cell measurement

Discharge

9:

Not yet soldered

10:

Not high temp, salvaged from prototype

Lower communication (fix made)

Higher communication

Cell measurement

Discharge

Soldering procedure:

1. Solder LTC IC
2. Solder headers P3, P6 and P7
3. Solder 2x100R power filter resistors and L1
4. Solder R18, R19, R20, R21 100R resistors
5. Solder R14 1K
6. Solder R13, R6, R15 1M

Drive control:

* Receving data from multiple wavesculptors: check
* Ability to detect wavesculptor type from received data: check
* Ability to independently control wavesculptors: check
* Ability to