

# Sunwoo Kim

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## Summary

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Incoming ECE PhD student focused on computing system design with heterogeneous architecture.

## Education

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<b>PhD</b>	<b>Cornell University</b> , Electrical and Computer Engineering	Aug 2025 – present
<b>BS</b>	<b>Seoul National University</b> , Electrical and Computer Engineering, <b>cum laude</b>	Mar 2019 – Feb 2025

## Publications

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**G<sup>3</sup>SA: A GPU-Accelerated Gold Standard Genomics Library for End-to-End Sequence Alignment**  
Yeejoo Han\*, **Sunwoo Kim\***, Seongyeon Park, Jinho Lee (\*equal contribution)  
ACM ICS 2025 (to appear)

## Awards & Honors

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<b>Fulbright STEM Graduate Studies Program (declined)</b> , \$40k – US and ROK govs	Sept 2024
<b>Presidential Science Scholarship</b> , Full tuition and stipend – ROK gov	Mar 2019

## Teaching & Leadership

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<b>Undergraduate Tutor</b>	Mar 2022 – June 2024
• <b>Algorithms</b> : offered 4 office hours every week for ECE students for a semester.	
• <b>Calculus</b> : offered 2 office hours every week for STEM students for 3 semesters.	
<b>President, College of Engineering Christian Society</b>	Sept 2022 – Dec 2024
• Organized events for students and professors: international religious service, volunteer trip, club fair.	
<b>Squad Leader, Capital Corps, ROK Army</b>	2021

## Research & Academic Experience

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<b>Accelerating Genomics Software with GPUs</b>	Jan 2024 – Dec 2024
• Implementing BWA-MEM in CUDA then improve alignment throughput with algorithmic enhancements.	
<b>Analog Computing Fourier Series</b>	Apr 2022 – June 2022
• Designed and implemented an op-amp differentiator based inverse Fourier transform unit on PCB.	
<b>Systems and Architecture Course Projects</b>	Sept 2021 – Dec 2024
• Extending the xv6 kernel with ULE scheduler, Linux ZSwap, and full path-indexed file systems.	
• Designed and implemented a pipelined MIPS-like CPU and a systolic array-based NPU on FPGA.	
• Designed and implemented a syntax-directed sub-C compiler with Bison.	
• Experiences in parallel programming with CUDA and pthread.	

## Technical Skills & Languages

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**Hardware Design:** Verilog, FPGA, SPICE simulation

**System Programming:** C/C++, CUDA, Make, Python, Bash

**Languages:** English (Fluent: TOEFL R30 L29 S24 W26), Korean (Native)