

Yiğit Süoğlu

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Education

October 2018 - Present	RWTH Aachen Aachen, Germany Micro- and Nanoelectronics (MINA)
September 2013 - January 2018	Sabancı University Istanbul, Turkey B.S. Electronics Engineering, 50% Tuition Scholarship GPA 3.26/4.00, Transcript: suoglu.github.io/misc/SuoğluYiğit_transcript.pdf

Experience

December 2022 - Present	Master Thesis Student, RWTH Aachen Aachen, Germany At Chair of Integrated Digital Systems and Circuit Design. Thesis title: Data Management for FPGA Compute Clusters Targeting Biological Neural Network Simulations
November 2020 - November 2022	Wissenschaftliche Hilfskraft, RWTH Aachen Aachen, Germany At Chair of Integrated Digital Systems and Circuit Design. I worked on a project on the accelerated simulation of biological neural networks. My tasks were the design and implementation of hardware modules/IPs on FPGA; and MicroBlaze firmware and host software development.
August - September 2017	Summer Intern, Mikroelektronik Ltd. Istanbul, Turkey During my internship at Mikroelektronik, I worked on a bandgap reference circuit and a fully differential folded cascode operational transconductance amplifier.
July - August 2017	Summer Intern, TÜBİTAK BİLGEM UEKAE Gebze, Turkey National Research Institute of Electronics and Cryptology. I designed a driver for Digilent Pmod AD5
June - July 2017	Summer Intern, İTÜ GSTL Istanbul, Turkey Istanbul Technical University Embedded Systems Design Laboratory. I worked on image processing with Zedboard. I designed simple IPs for the ARM processor on Zedboard and programmed the processor.
July - August 2016	Summer Intern, AirTies Wireless Networks Istanbul, Turkey SummerSeed 2016 is a monthlong summer camp to introduce students to Embedded Systems and Linux.

Skills

Computer:

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| – Verilog HDL | – Xilinx Vivado | – Python |
| – Cadence Virtuoso | – C++ | |

Language:

ENGLISH:	Professional working proficiency
TURKISH:	Native
GERMAN:	Elementary proficiency

Hobbies:

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| – Scuba Diving | – Running | – Photography |
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