Yigit Suoglu

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Education:

2013 - Present **Sabanci University, Istanbul, Turkey**
B.S. Electronics Engineering, 50% Tuition Scholarship
Current GPA 3.18/4.00, Transcript: suoglu.github.io/misc/Suoglu-Yigit-transcript.pdf

2009 - 2013 **Besiktas Ataturk Anatolian High School, Istanbul, Turkey**

Experience:

Fall 2016 **Undergraduate Teaching Assistant, Sabanci University, Turkey**
for CS 303, Logic and Digital System Design at Fall Term
I held weekly office hours, supervised exams and lab sections.

July - August 2016 **Summer Intern, AirTies Wireless Networks, Istanbul, Turkey**

Summer 2015 **Undergraduate Teaching Assistant, Sabanci University, Turkey**
CS 201, Introduction to Computing (C++)
I held weekly office hours and helped students learn coding.

Skills:

- **Computer**
  - Verilog HDL
  - C/C++
  - Cadence Virtuoso
  - Xilinx ISE
  - HTML & CSS
  - JavaScript
  - Assembly Language
  - Agilent ADS
  - MPLAB X
  - Arduino
- **Language**
  - English: Professional working proficiency
- **Hobbies**
  - Scuba Diving: PADI Advanced Open Water Diver, 1407UB7824

Projects:

- **Visible Light Communication using RGB LEDs and Arduino**
  We built a simple communication system using Arduino Uno, 1w RGB LEDs and RGB colour sensor in 9 days. At this stage our system can send and receive text based massages from one Arduino to another Arduino using visible light. I Led a team of five. (including me)
  For more information check: github.com/suoglu/RGB_data_transfer

- **Implementation of a Doppler Radar on PCB:**
  As part of Microwaves course we designed and implemented a doppler radar on printed circuit board. In our design discrete amplifiers, mixer and filters were used.
  For more information check: suoglu.github.io/misc/Project-Reports/Kara&Suoglu_projectReport.pdf

- **Simple Queue Management System for Bank:**
  As a part of digital design course we designed a simple queue management system in Verilog and implemented it on BASYS FPGA board. For more information check: github.com/suoglu/Queue-Management-System

- **Two Stage Operational Amplifier:**
  As a part of Analog IC course I designed a two stage opamp with gain of ~79.7 dB and BW of ~905 Hz. Designed amplifier has ~266 µW power consumption, 2.5 V swing rate and ~5.3 V/µs slew rate. Both schematic and layout design made using Cadence Virtuoso with xfab 0.18μ technology.
  For more information check: suoglu.github.io/misc/Project-Reports/suoglu_two-stage-opamp.pdf

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Certifications

- Cisco Networking Academy:
  - IT Essentials: suoglu.github.io/misc/Certificates/Cisco-IT-Essentials.jpg
  - Introduction to Cybersecurity: suoglu.github.io/misc/Certificates/Cisco-Int-to-Cybrsec.pdf
  - Introduction to IoT: suoglu.github.io/misc/Certificates/Cisco-Int-to-IoT.pdf
- Turkcell certificates:
  - Arduino 101 & 201 & 301 & 401
  - Web Programming 101 & 201 & 301