

# Yigit Suoglu

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## Home Address:

Fenerli Ahmet sk. Dilek apt. No 6 D: 4  
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34724

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## Education:

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| 2013 - Present | <b>Sabanci University, Istanbul, Turkey</b><br>B.S. Electronics Engineering, 50% Tuition Scholarship<br>Current GPA 3.18/4.00, Transcript: <a href="https://suoglu.github.io/misc/Suoglu-Yigit-transcript.pdf">suoglu.github.io/misc/Suoglu-Yigit-transcript.pdf</a> |
| 2009 - 2013    | <b>Besiktas Ataturk Anatolian High School, Istanbul, Turkey</b>  |

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## Experience:

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| Fall 2016          | <b>Undergraduate Teaching Assistant, Sabanci University, Turkey</b><br>for CS 303, Logic and Digital System Design at Fall Term<br>I held weekly office hours, supervised exams and a lab section.<br>I worked under İlker Hamzaoglu and graduate TAs. |
| July - August 2016 | <b>Summer Intern, AirTies Wireless Networks, Istanbul, Turkey</b>  |
| Summer 2015        | <b>Undergraduate Teaching Assistant, Sabanci University, Turkey</b><br>CS 201, Introduction to Computing (C++)<br>I held weekly office hours and helped students learn coding.   |

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## Skills:

- **Computer**
  - Verilog HDL
  - C/C++
  - Cadence Virtuoso
  - Xilinx ISE
  - HTML & CSS
  - JavaScript
  - Assembly Language
  - Agilent ADS
  - MPLAB X
  - Arduino
  - LTspice
- **Language**
  - English: Professional working proficiency
- **Hobbies**
  - Scuba Diving: PADI Advanced Open Water Diver, 1407UB7824

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## Projects:

- **Visible Light Communication using RGB LEDs and Arduino**

We built a simple communication system using Arduino Uno, 1w RGB LEDs and RGB colour sensor in 9 days. At this stage our system can send and receive text based messages from one Arduino to another Arduino using visible light. I Led a team of five. (including me)  
For more information check: [github.com/suoglu/RGB\\_data\\_transfer](https://github.com/suoglu/RGB_data_transfer)

- **Implementation of a Doppler Radar on PCB:**

As part of Microwaves course we designed and implemented a doppler radar on printed circuit board. In our design discrete amplifiers, mixer and filters were used.

For more information check: [suoglu.github.io/misc/Project-Reports/Kara&Suoglu\\_projectReport.pdf](https://suoglu.github.io/misc/Project-Reports/Kara&Suoglu_projectReport.pdf)

- **Simple Queue Management System for Bank:**

As a part of digital design course we designed a simple queue management system in Verilog and implemented it on BASYS FPGA board. For more information check: [github.com/suoglu/Queue-Management-System](https://github.com/suoglu/Queue-Management-System)

- **Two Stage Operational Amplifier:**

As a part of Analog IC course I designed a two stage opamp with gain of  $\sim 79.7$  dB and BW of  $\sim 905$  Hz. Designed amplifier has  $\sim 266$   $\mu$ W power consumption, 2.5 V swing rate and  $\sim 5.3$  V/ $\mu$ s slew rate. Both schematic and layout design made using Cadence Virtuoso with xfab 0.18 $\mu$  technology.

For more information check: [suoglu.github.io/misc/Project-Reports/suoglu\\_two-stage-opamp.pdf](https://suoglu.github.io/misc/Project-Reports/suoglu_two-stage-opamp.pdf)

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## Certifications

- Cisco Networking Academy:

- IT Essentials: [suoglu.github.io/misc/Certificates/Cisco-IT-Essentials.jpg](https://suoglu.github.io/misc/Certificates/Cisco-IT-Essentials.jpg)
- Introduction to Cybersecurity: [suoglu.github.io/misc/Certificates/Cisco-Int-to-Cybrsec.pdf](https://suoglu.github.io/misc/Certificates/Cisco-Int-to-Cybrsec.pdf)
- Introduction to IoT: [suoglu.github.io/misc/Certificates/Cisco-Int-to-IoT.pdf](https://suoglu.github.io/misc/Certificates/Cisco-Int-to-IoT.pdf)

- Turkcell certificates:

- Arduino 101 & 201 & 301 & 401
- Web Programming 101 & 201 & 301

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## References

In alphabetical order:

Yaşar Gürbüz, Ph.D.

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