4ᵀᴴ International Conference on

NANOELECTRONICS, MACHINE LEARNING, INTERNET OF THINGS & COMPUTING SYSTEMS (NMIC-2024)

INDIAN SOCIETY FOR VLSI EDUCATION

RANCHI-835217 (JHARKHAND) INDIA

Participation Certificate

Technical Session: Signal Processing, Deep Learning & Computational Intelligence

This is to certify that Pranay Narendra Patil *of* Dr. D. Y. Patil Institute of Technology, Pune, Maharashtra. has/have been participated and presented a paper title Machine Learning Approach to Sign Language Recognition using Teachable Machine in the 4ᵗʰ *International Conference on Nanoelectronics, Machine Learning, Internet of Things & Computing Systems(NMIC-2024)* organized by ISVE at **Advanced Regional Telecom Training Centre**, **BSNL,** Hazaribag Road, Ranchi-835217, Jharkhand, India *during April 13-14ᵗʰ , 2024* in ***Virtual Mode.***

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