

SMs

Compute units

Reg.

Core

L1/L2 TLB

L1
cache

Shared
mem.

L2 cache

L3 TLB

Off-chip dev. mem.

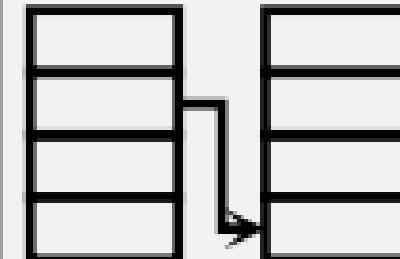
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Phys.

Pagetable



Local
mem.

Global
mem.



Virt.