

The Repressilator

Henrik Åhl

January 27, 2016

What is the Repressilator?

What is the Repressilator?

- ▶ The design and construction of a synthetic network with oscillatory behaviour.

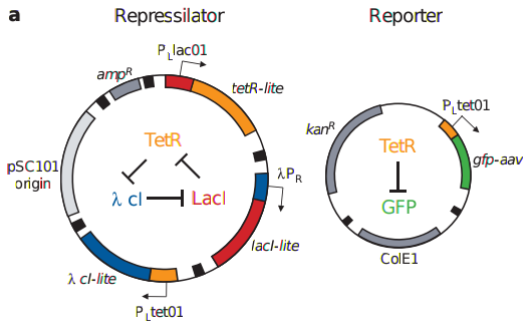
What is the Repressilator?

- ▶ The design and construction of a synthetic network with oscillatory behaviour.
- ▶ Added to e-coli in order to observe whether oscillatory effects would occur.

What is the Repressilator?

- ▶ The design and construction of a synthetic network with oscillatory behaviour.
- ▶ Added to e-coli in order to observe whether oscillatory effects would occur.
- ▶ Result: Noisy behaviour, but indeed – oscillations.

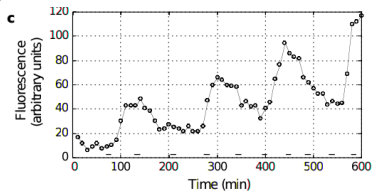
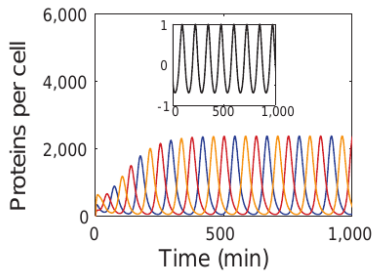
Design



Design

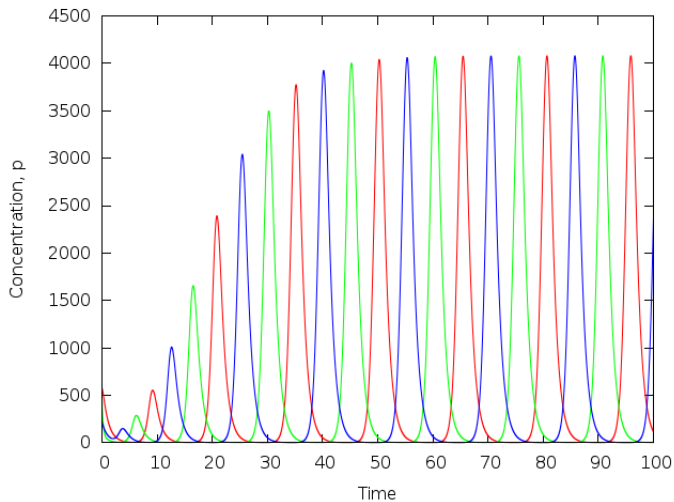
$$\begin{aligned}\frac{dm_i}{dt} &= -m_i + \frac{\alpha}{(1 + p_j^n)} + \alpha_0 \\ \frac{dp_i}{dt} &= -\beta(p_i - m_i)\end{aligned} \quad \begin{pmatrix} i = lacl, tetR, cl \\ j = cl, lacl, tetR \end{pmatrix}$$

Results



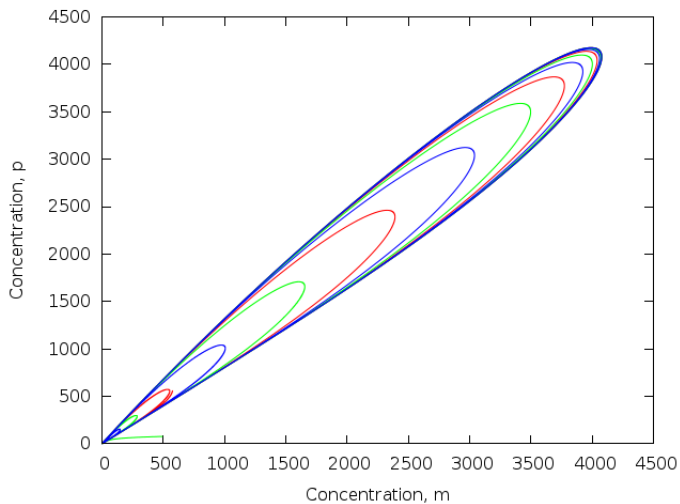
Results

► Stable oscillations



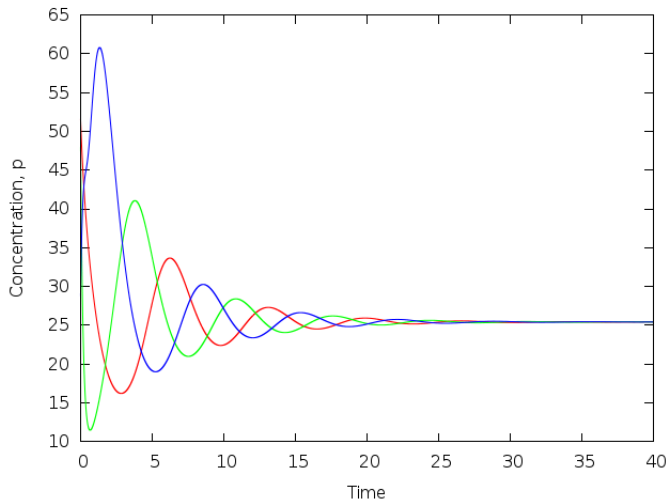
Results

- Repressor-expressor convergence towards cyclic state



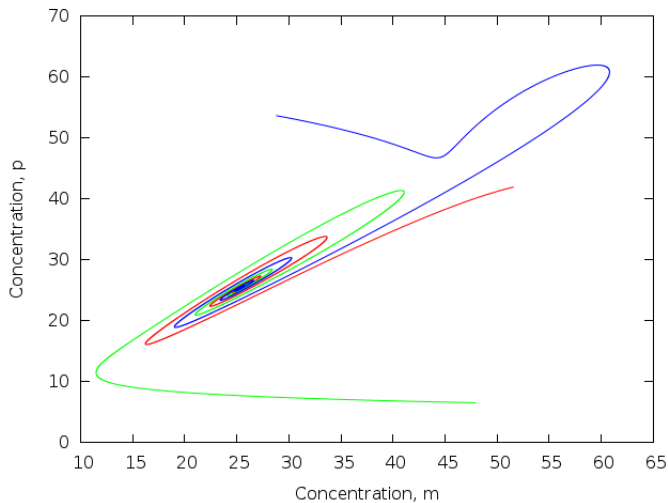
Results

► Non-oscillating convergence



Results

- Repressor-expressor convergence towards "flat" state.



Code

- ▶ Implemented in Java
- ▶ Fourth order Runge-Kutta