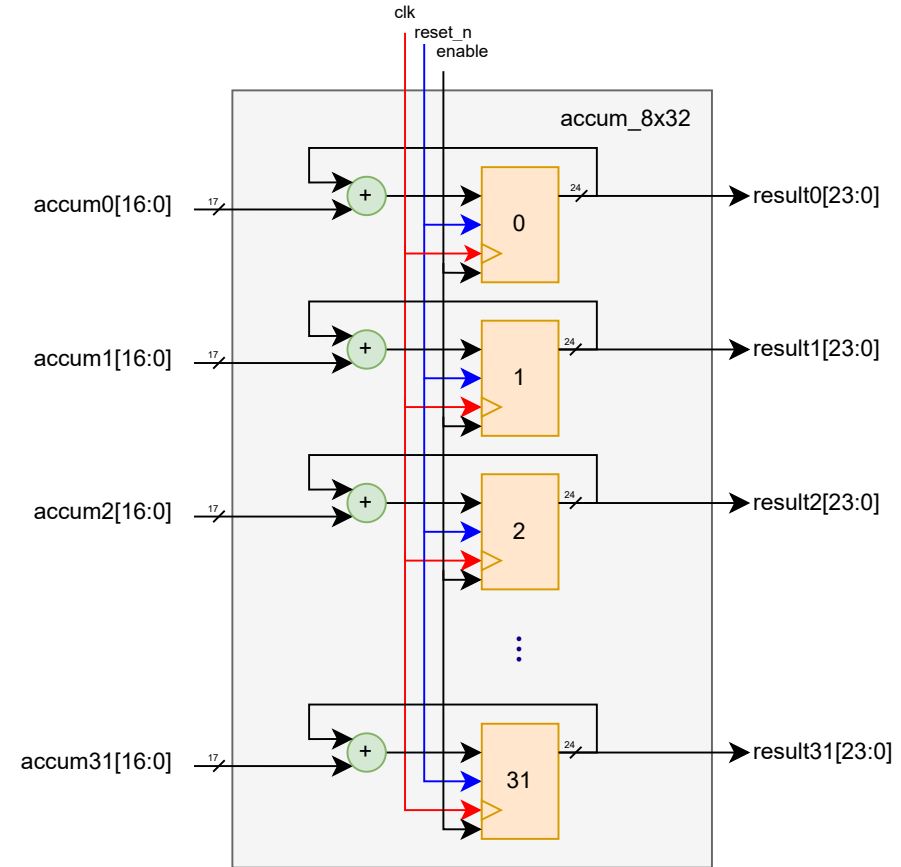
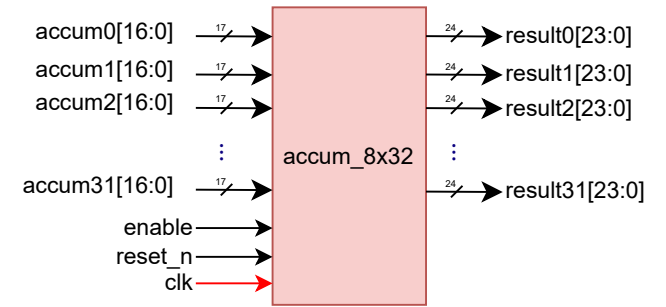
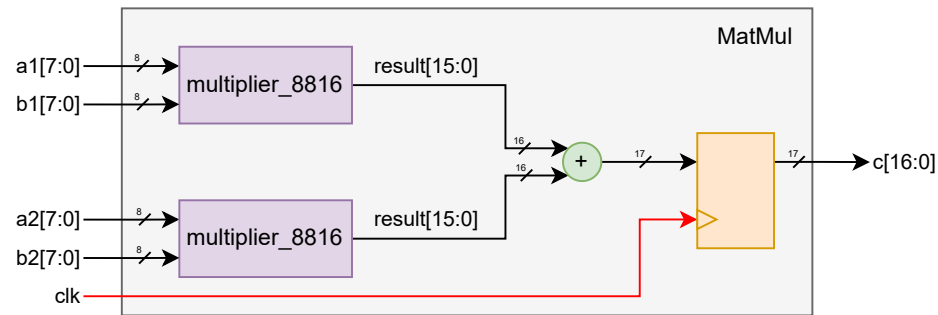
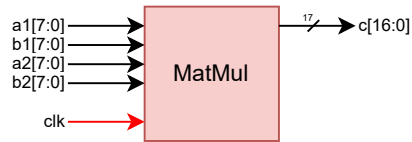
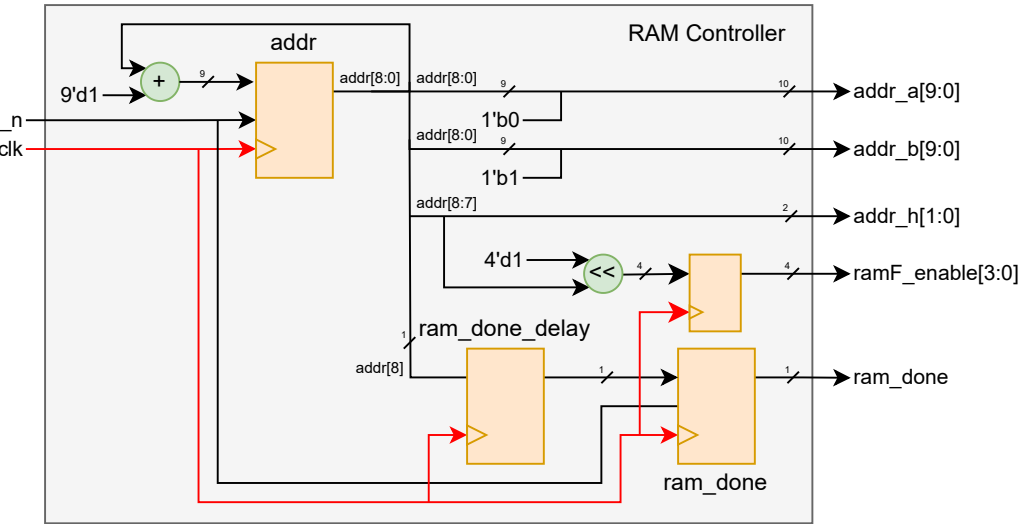
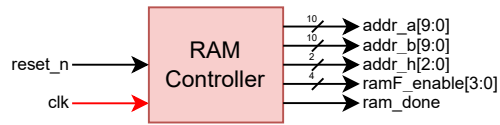
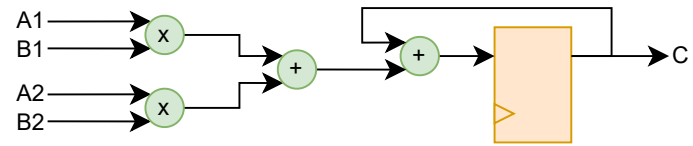


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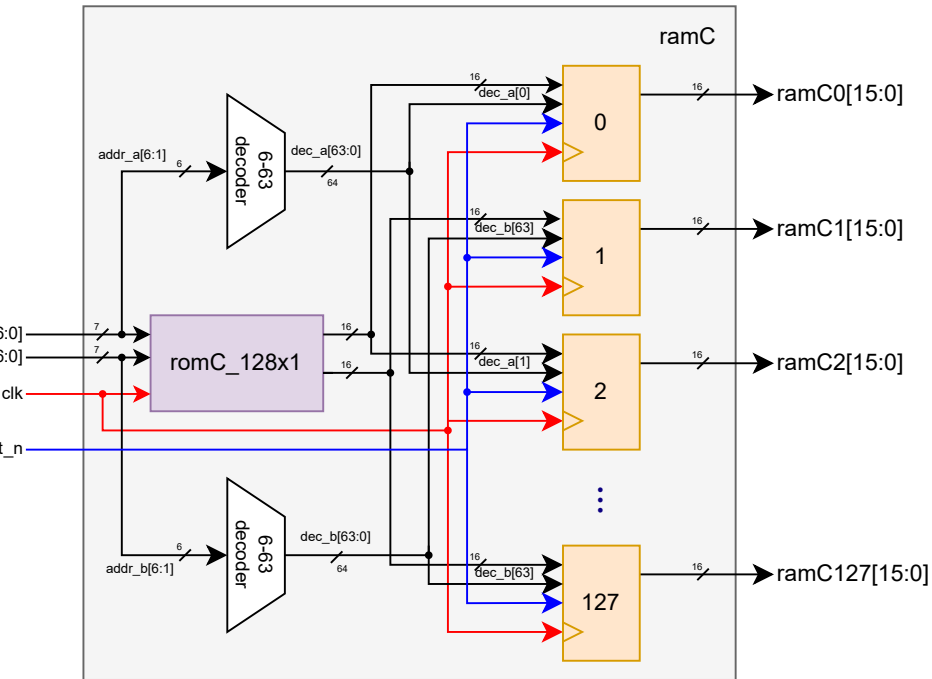
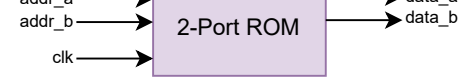
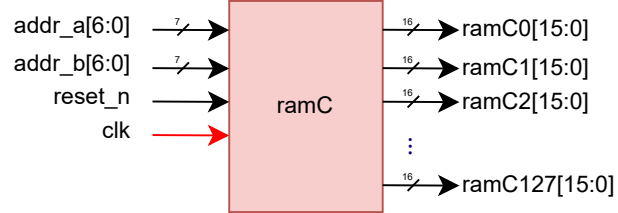
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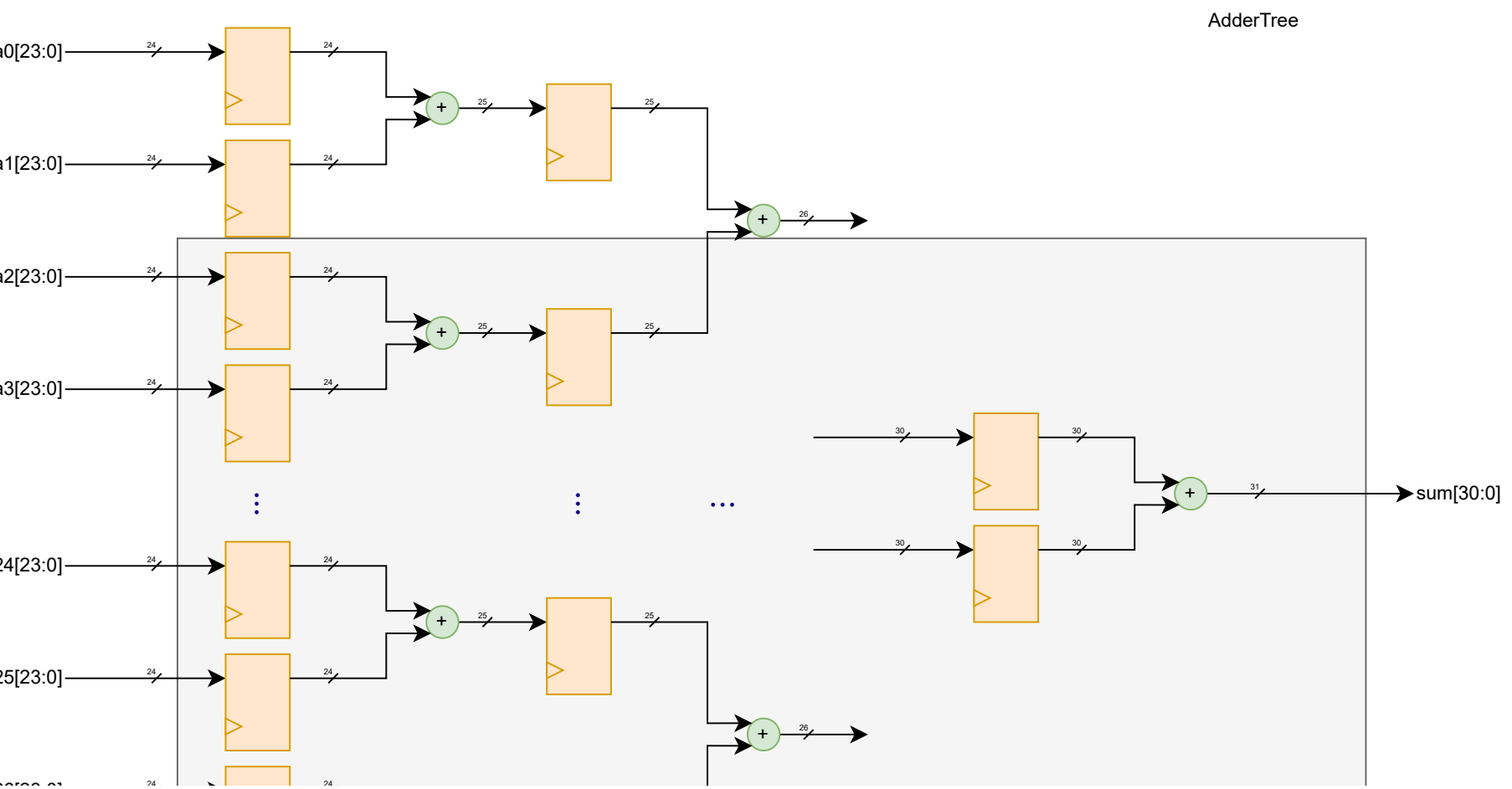
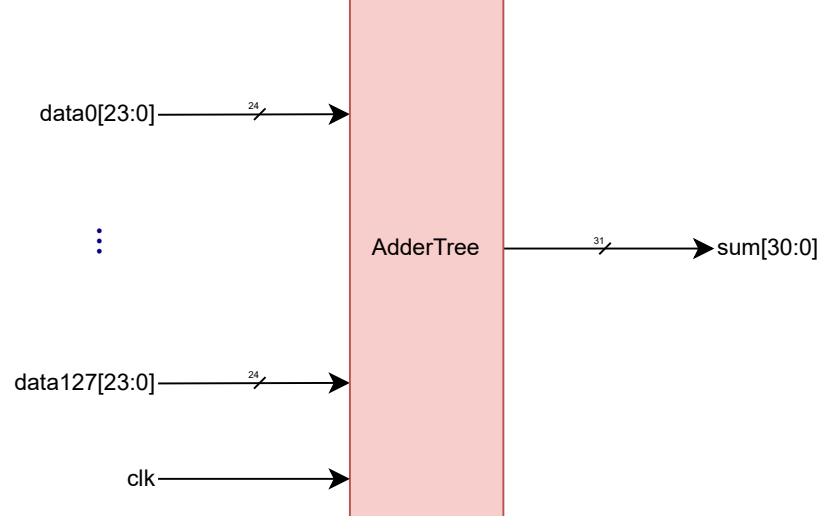


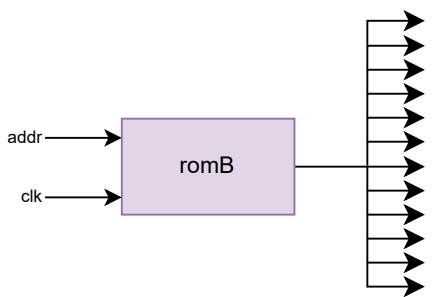
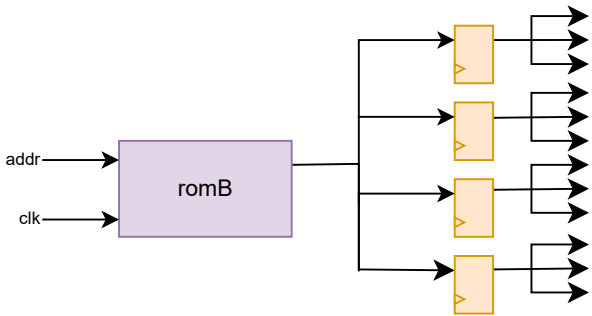


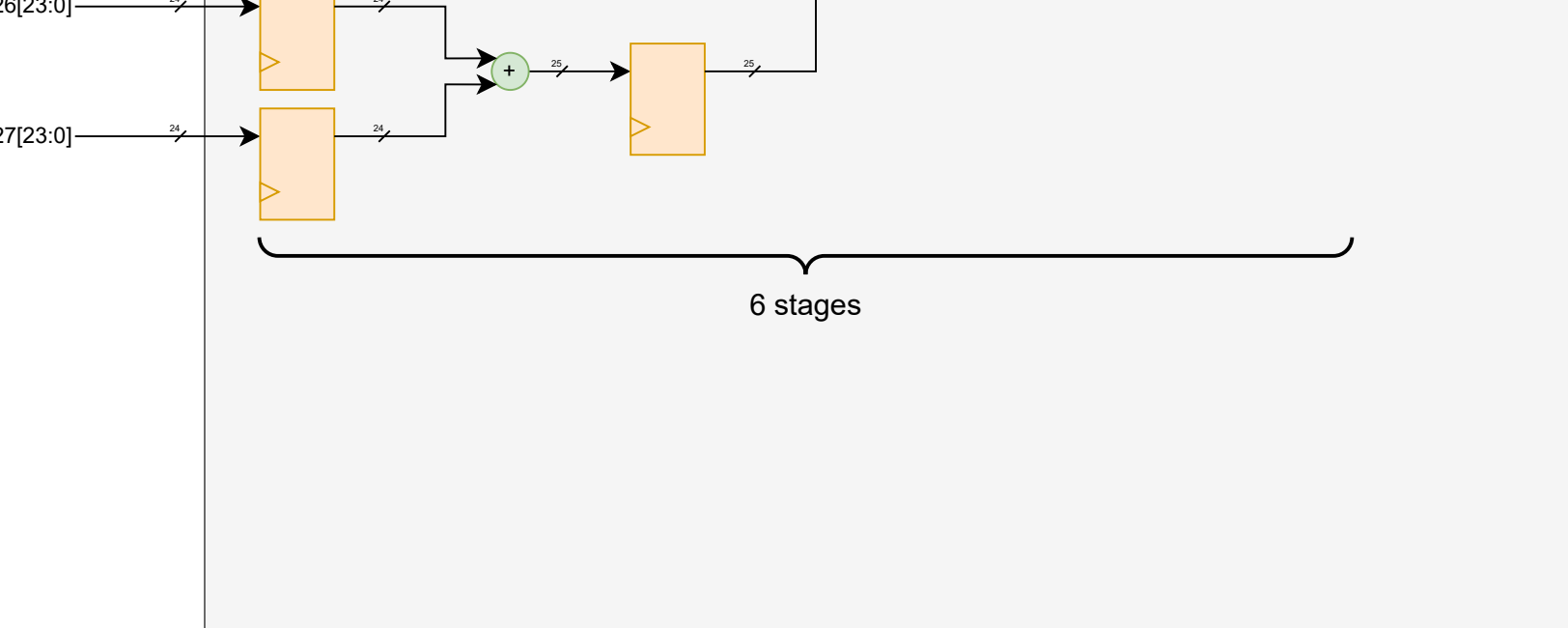
addr_a[6
addr_b[6

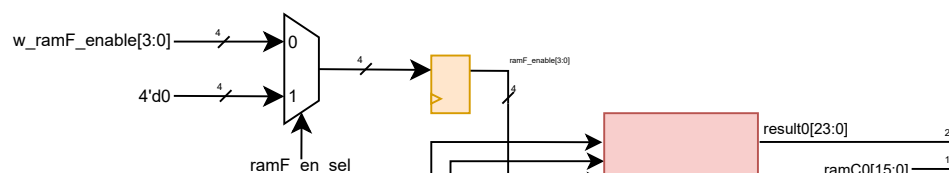
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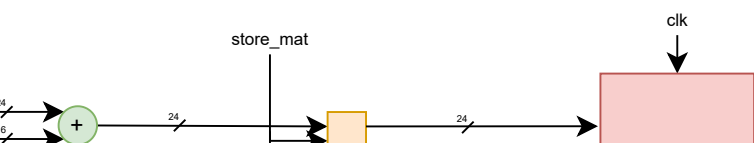








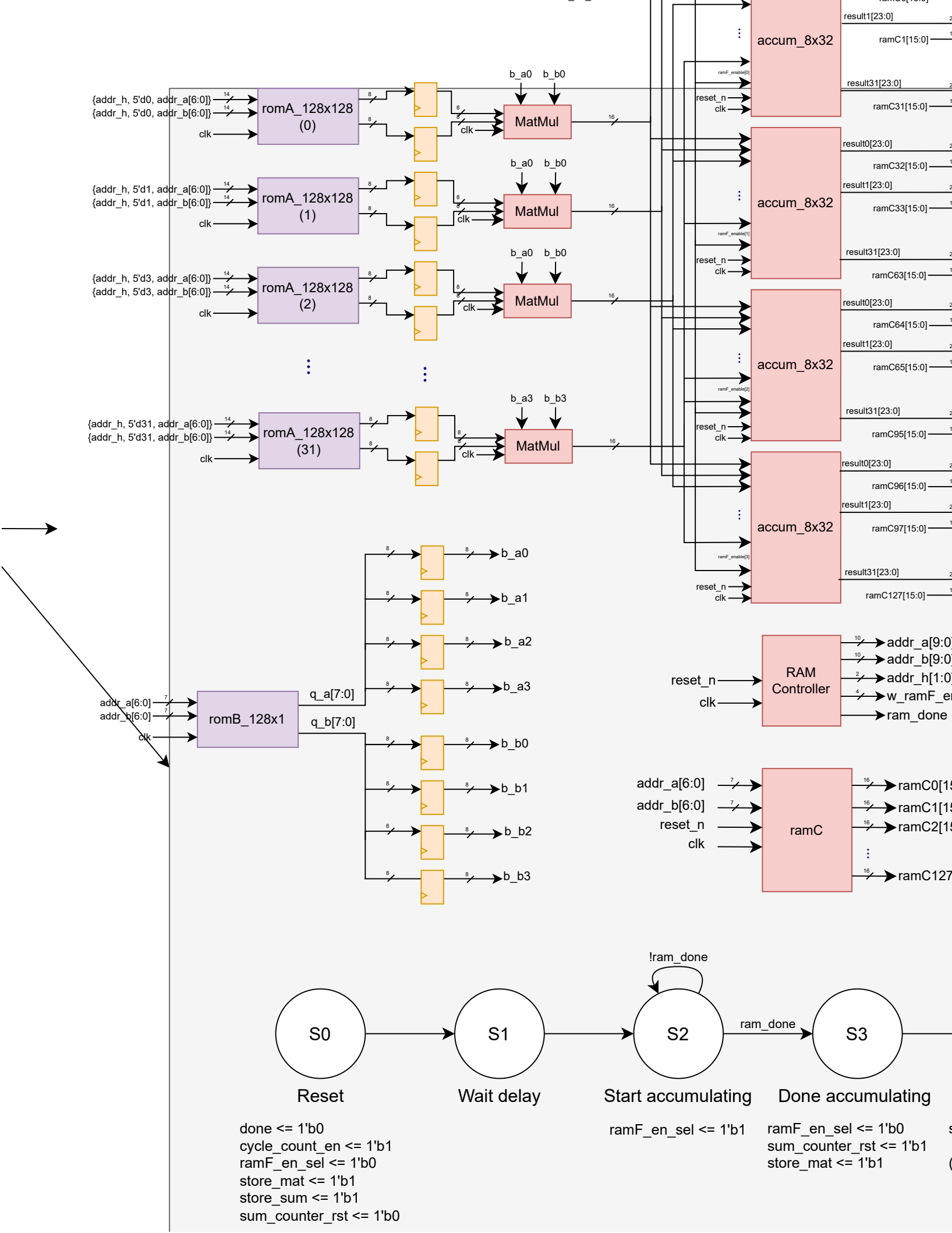


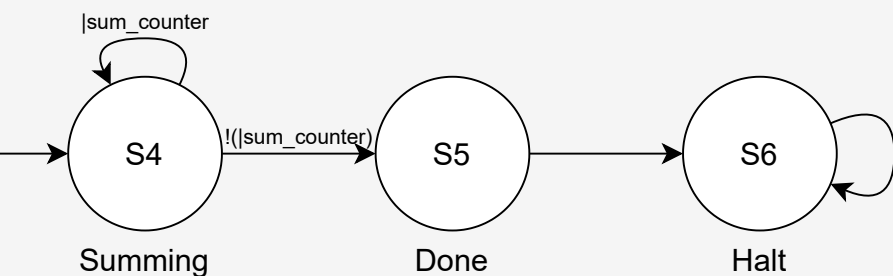
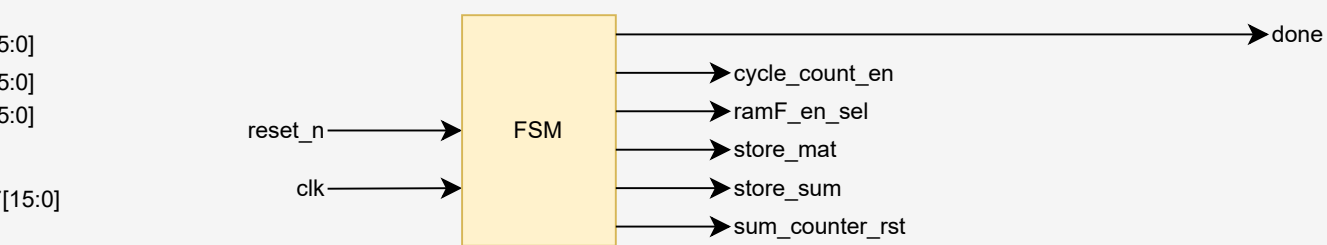
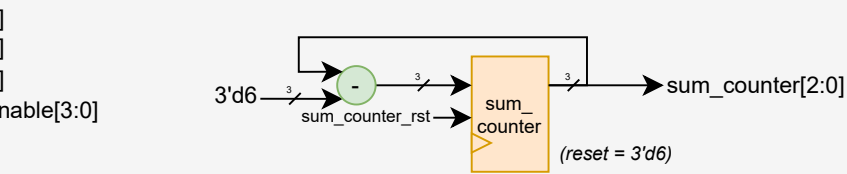
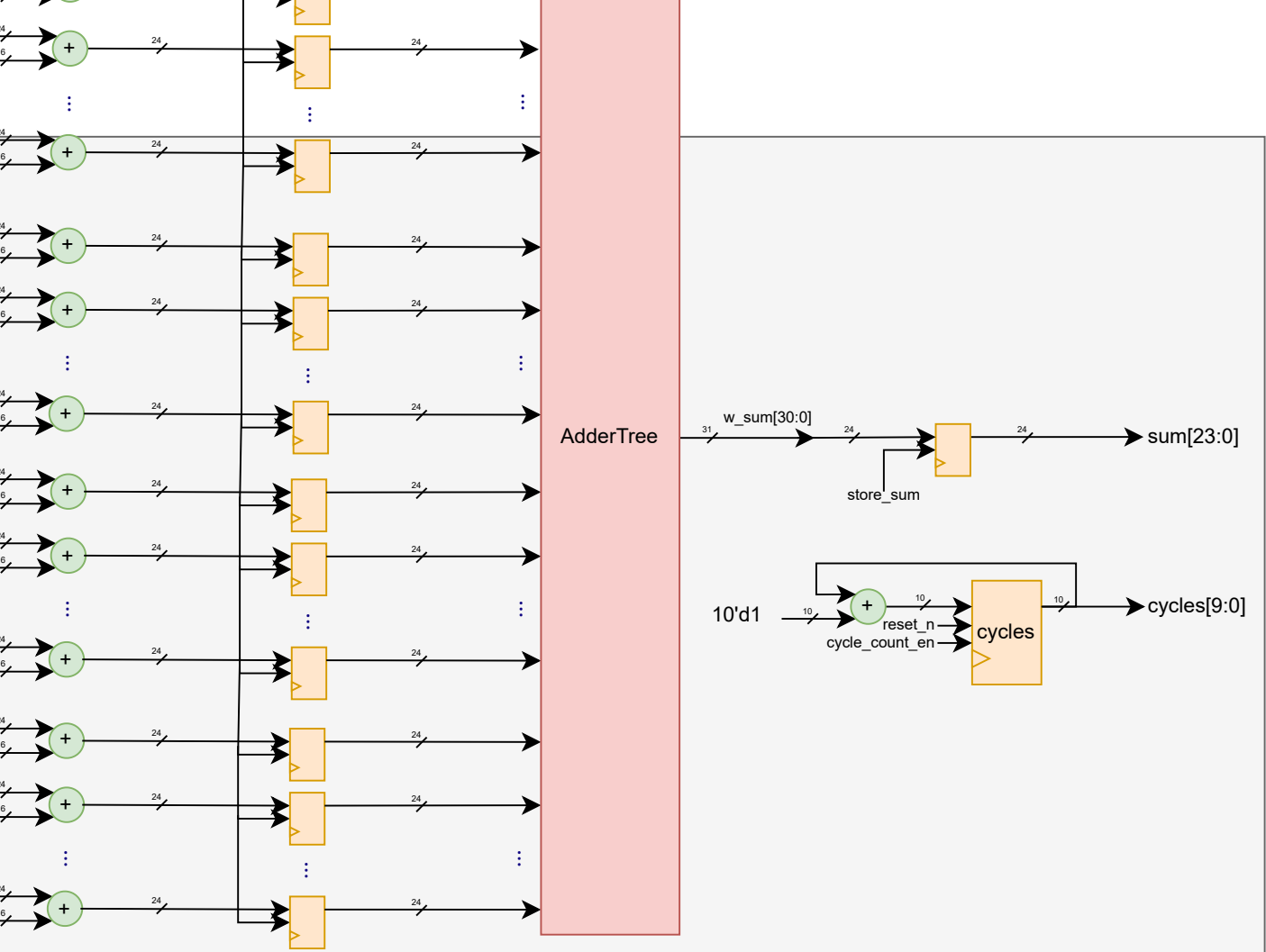


MyTop

reset_n

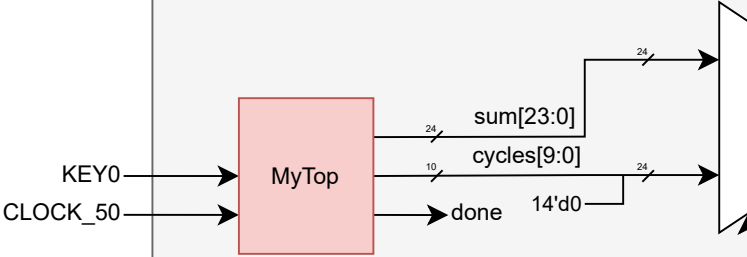
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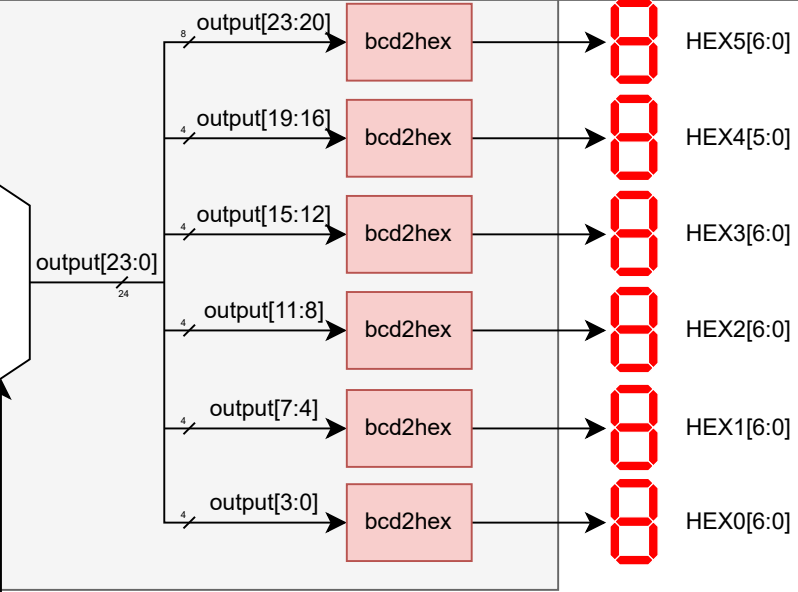
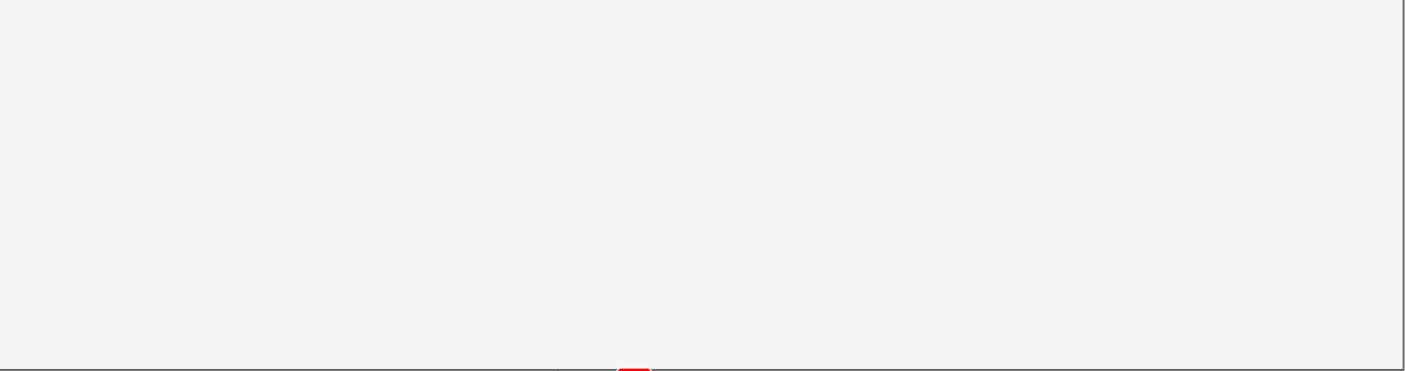




sum_counter_rst <= 1'b0
 (wait for AdderTree)
 done <= 1'b1
 store_sum <= 1'b1
 cycle_count_en <= 1'b0

ChipInterface





W0

